ADC Synchronization

Example Results

* Includes \( t_d = 760\text{ns} \) of delay in feedback loop

\( DT_3 = t_{d,\text{sum}} = 360\text{ns} \)
Inclusion of Delay

$$G_{vu}(s) = G_{vu}(s)e^{-st_d}$$

Current Control

$$k_{\text{avl}} = \phi$$