





Donald W. Bouldin, Ph.D.
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Dr. Don Bouldin Lounge

Dr. Don Bouldin was a member of this faculty for 36 years from 1975 to 2011. He taught courses and conducted research in the design of very large scale integrated (VLSI) circuits and field-programmable gate arrays (FPGAs). He obtained hardware and software donations from several companies including UTK's largest ever gift-in-kind of \$47M from Cadence Design Systems. He served as an instructor for eight NSF-sponsored faculty enhancement courses on VLSI/FPGA design in which he taught over 200 faculty from other U.S. universities. He presented seminars in 18 countries around the world. He was the major advisor for 14 Ph.D. and 98 M.S. students at UTK.

Dr. Bouldin was recognized as a Fellow of the IEEE for contributions to the design of special-purpose architectures using VLSI processors. He served as Editor-in-Chief of the *IEEE Transactions on VLSI Systems*. He served twenty years as editor of the "VLSI Designer's Interface" column for *IEEE Circuits & Devices Magazine*. He co-authored over 250 publications and conducted research sponsored by DARPA, ONR, NSF and ORNL. He received the UTK Chancellor's Award for Research and Creative Achievement, the Allen and Hoshall Award from the UTK College of Engineering and the ASEE Outstanding Teacher Award. He is the co-inventor of four touch-screen patents.

Prof. Bouldin received both his Ph.D. and B.E. degrees in electrical engineering from Vanderbilt University and his M.S. from Georgia Tech. He served four years as Electronics Officer in the U.S. Navy.

The lounge is named in honor of Dr. Bouldin for the minds and lives he impacted positively. Given by alumnus Michael C. Crabtree, co-founder of CTI (now Siemens Molecular Imaging, Inc.), who received his B.S. and M.S. from this department.