MARK YOUR CALENDAR

San Jose State University College of Engineering
And the Cypress University Alliance
Cordially Invite You to Attend
A PSoC™ Professor’s Workshop

WHY:

• Learn all about Cypress PSoC technology. A family of mixed signal programmable devices, PSoC is truly the world’s first Programmable System-On-Chip

• Leave with a CY3210-PSoC EVAL1 Development Kit, associated software and information on how to enhance your student’s learning by adding PSoC to your curriculum

WHO SHOULD ATTEND:
Professors who teach digital, analog and embedded topics; as well as subjects as diverse as robotics, mechanatronics, industrial controls, and aeronautics.
WHEN:
Thursday and Friday, March 29 and 30, 2007

Day 1: 8:30 – 5:00
Day 2: 8:30 – 12:30

WHERE:
San Jose State University
Engineering Building Room 288

Working Agenda:

Day 1
8:30am - 9:00am Refreshments / Lab Set-up
9:00am - 10:00am Introduction to PSoC
10:00am - 12:00am Lab 1: PWM
12:00am - 1:00pm Lunch
1:00pm – 3:00pm Lab 2: ADC
3:00pm – 3:30pm Break
3:30pm – 5:00pm Lab 3: Dynamic Reconfiguration

Day 2
8:30am – 9:00am Refreshments / Lab Set-up
9:00am – 9:30am Complete lab 3 (if required) / review
9:30am - 10:15am Introduction to PSoC Express
10:15 – 12:15am Lab 4: PSoC Express Lab
12:15 – 12:30am Wrap Up

Register online at: http://www.zoomerang.com/survey.zgi?p=WEB225ZVS4T5QJ
Or call Carey Patrick at 408–544–1089

More information is available about PSoC and the University Alliance at:

www.cypress.com/cuanews