CALL FOR PAPERS

“1st IEEE International Conference on Circuits and Systems for Communications”

St.Petersburg, Russia
June 26 - 28, 2002
St.Petersburg State Technical University

The main Conference theme is:
“Circuits and Systems in Broadband Communication Technologies”

Author's Schedule:
Deadline submission of extended abstract or full paper                 17 December, 2001
Deadline for notification of acceptance                                           25 February, 2002
Deadline for final version                                                                29 March, 2002

Communication systems will be a major player in the global world market of this new century. Extending communication technologies to broadband services stimulates the development of new types of circuits and systems oriented toward the realization of low-cost, low-voltage, and portable communication devices. These reasons have determined the choice of the Conference Theme. In the opinion of the Organizing Committee, session topics include the main part of perspective questions and problems that are around the Conference title. Thus, the major goal of the Conference is to bring together researchers from universities, institutes, and industry to discuss the new results in their area as well as their vision of future promising developments in their field.

The Conference will consist of plenary sessions, oral presentations, and poster sessions. The working language is English. Exhibition desks will be made available during the Conference for companies and organizations that would like to promote their products in related areas. The special award for "Young Engineer Best Paper" will be organized. A cultural program including the Hermitage, museums, and beautiful sceneries around St. Petersburg will be available as well. Last but not least, visitors are expected to capture a lifetime impression of the famous season of June in St. Petersburg called "White Nights".

Papers are solicited in subject areas including but not limited to the following:

1. Analog circuits and systems –
   Amplifiers, continuous and discrete time filters, current mode and switched-currents circuits, switched-capacitor circuits, A/D and D/A converters, analog front end.

2. Digital circuits and systems –
   Digital filters, modulators, phase-locked loops, CODEC, microprocessors and co-processors, logic arrays.

3. Radio-frequency microelectronic circuits –
   LNA, mixers, transceiver circuits and sub-circuits for RF baseband, RF components, optical interface and processor.
4. **Mixed analog/digital systems** –
System-on-a-chip design and methodologies, digital and mixed signal processors and ASIC, multimedia signal processors.

5. **Signal Processing and Implementation** –
Image and voice-band processing for 3G communications, linear and non-linear adaptive systems, chaotic signals and applications in communications.

6. **Neural integrated circuits and systems** –
System modeling, simulation, neural networks CMOS design, applications of neural computing in communications.

7. **Control and tuning systems** –
Voltage-controlled oscillators, phase noise in VCO, design and implementation, models and simulation.

8. **Microelectronic realization and manufacturing** –
Bipolar, CMOS, BiCMOS circuits and systems design, models, tools, simulations, high-frequency microelectronic design.

**Conference Co-Chairs:**
- Prof. R.J.P. de Figueiredo (University of California, Irvine, USA)
- Prof. V.V. Shakhguildian (Moscow Technical University of Communications and Informatics, Russia)

**Chair of Technical Program Committee:**
- Prof. A.S. Korotkov (St.Petersburg State Technical University, Russia)

**Technical Program Committee:**
- Dr. M. Banu (AGERE Systems, New Jersey, USA)
- Prof. A.V. Bondarenko (St.Petersburg Civil Engineering University, Russia)
- Prof. J. Da Franca (Ins. Superior Tecnico, Portugal)
- Dr. G. De Veirman (AGERE Systems, Huntington Beach, USA)
- Prof. A.S. Dmitriev (Radio Eng and Electronics Ins., Moscow, Russia)
- Prof. V.F. Dmitrikov (St.Petersburg Technical University of Communications, Russia)
- Prof. A.L. Fradkov (Ins. for Problems of Mechanical Engineering, St.Petersburg, Russia)
- Prof. M. Gabbouj (Tampere University of Technology, Finland)
- Prof. R. Geiger (Iowa State University, USA)
- Prof. L. Goras (Polytechnic Ins., Iasi, Romania)
- Mr. H. Hauer (Fraunhofer Ins., Erlangen, Germany)
- Prof. A.V. Krivosheikin (Wireless Communications, St.Petersburg, Russia)
- Prof. S.G. Krutchinsky (Taganrog State University of Radio Engineering, Russia)
- Prof. V.Kukk (Tallin Technical University, Estonia)
- Dr. Luo Fa-Long (Resound, Redwood, USA)
- Dr. G. Mollova (University of Architecture, Civil Eng and Geodesy, Sofia, Bulgaria)
- Prof. T.S. Ng (University of Hong Kong)
- Prof. J.A. Nossek (Technical University of Munich, Germany)
- Dr. S. Ozoguz (Istanbu Technical University, Turkey)
- Dr. S. Szczepanski (Technical University of Gdansk, Poland)
Session Chairs:
1. Analog:
   Prof. A.V. Bondarenko (St.Petersburg Civil Engineering University, Russia)
2. Digital:
   Prof. I.A. Tsikin (St.Petersburg State Technical University, Russia)
3. RF:
   Dr. M.Banu (AGERE Systems, New Jersey, USA)
4. Mixed systems:
   Dr. G. De Veirman (AGERE Systems, Huntington Beach, USA)
5. Signal Processing:
   Prof. R. Unbehauen (Erlangen-Nuremberg University, Germany)
6. Neural Networks:
   Dr. Luo Fa-Long (Resound, Redwood, USA)
7. Control & Tuning Systems:
   Prof. A.L. Fradkov (Ins. for Problems of Mechanical Engineering, St.Petersburg, Russia)
8. Microelectronic Realization & Manufacturing:
   Mr. H.Hauer (Fraunhofer Ins., Erlangen, Germany)
   Prof. A.V. Krivosheikin (Wireless Communications, St.Petersburg, Russia)

Young Engineer Best Paper Award:
Dr. S. Ozoguz (Istanbul Technical University, Turkey)

Address for Correspondence:
Prof. A.S. Korotkov
Radio Engineering and Telecommunications Department,
St.Petersburg State Technical University,
Polytechnicheskaya St., 29
St.Petersburg, 195251
Russia
Tel.: +7 812 5527639
E-mail: korotkov@twonet.stu.neva.ru