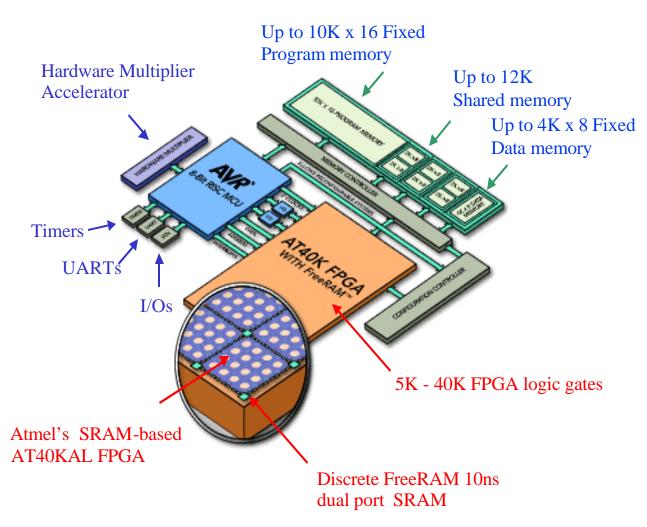


FPSLIC Advantages

- Single chip solution
- 30MIPS performance
- Very low power
- No minimum volume requirements
- No IP issues
- No NRE costs
- Very low design risk
- Fully reconfigurable
- Faster time-to-market
- Complete design tool Atmel's SRAM-based suite for \$995

 AT40KAL FPGA





FPSLIC Starter Kit

ATSTK94

- Text book guides customers step by step through the design process
- System Designer tool w/4 month license
 - Synthesis/VHDL Simulator/Coverification
 - AVR studio debugger
 - Place and Route IDS7
 - IAR and Imagecraft C compiler (one month trial period)
- Comprehensive development board
 - Design examples
 - AT94K40 FPSLIC device
 - AT17LV010 Configurator
 - 2 Serial Ports
 - Programmable switches & LEDs
 - ISP cables for programming configurator

