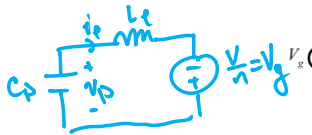


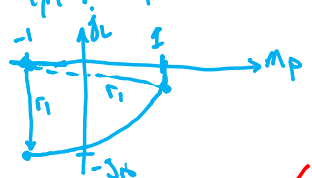


# Dual Active Bridge Converter

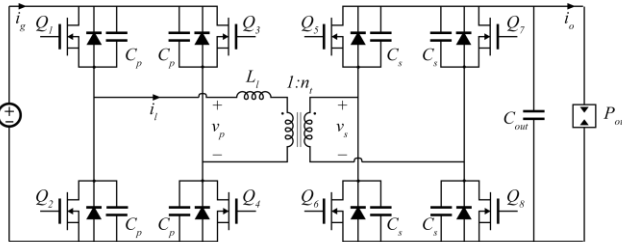
Ss of primary



$v_p(t) = -V_g \int \frac{2v_p(t)}{dt} dt$   
 $i_p(t) = -I_p$



$V_{base} = V_g$   
 ZVS condition:  $r_1 \geq 2 \rightarrow S_p \geq 2$



Need large inductor for ZVS at low currents

Issues:

- ZVS lost at light load
- XF saturation may occur if  $v_p$  or  $v_s$  are not purely AC
- Performs poorly at  $\frac{V}{V_g} \neq n$

ZVS cond'n on primary

$S_p \geq 2$

$I_{base} S_p \geq 2 I_{base}$

$I_p \geq 2 \frac{V_g}{R_o} = 2 V_g \sqrt{\frac{C_p}{L_f}}$

$\frac{1}{2} L_f I_p^2 > (2V_g)^2 C_p \frac{1}{2}$

E in  $L_f$  @ start of prim dead time  $\rightarrow$  Energy needed to ZVS primary devices

$I_p \leftrightarrow I_{out}$

$V_{base} = V_g$

$I_{base} = \frac{V_g}{R_o}$

$R_o = \sqrt{\frac{L_f}{C_p}}$

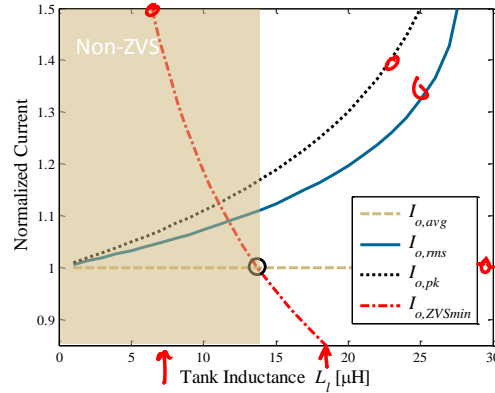
Large  $L_f$  desired to obtain ZVS at low  $P_{out}$

$\rightarrow$  DAB in Phase-shift modulation will lose ZVS at light load

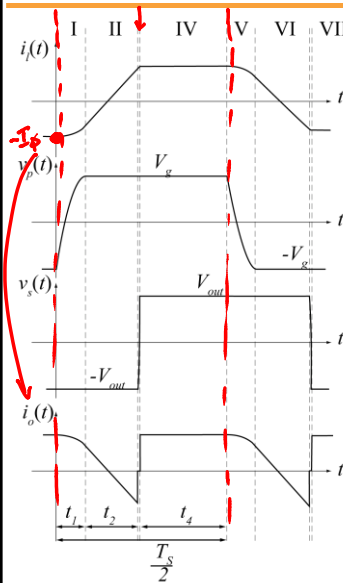


## Constraints on Inductance

- From previous analysis, smaller  $L_l$  results in smaller RMS currents in converter
- However,  $L_l$  must be large enough to store energy necessary for ZVS
- Near-optimal design at a single operating point by selecting  $L_l$  just large enough to obtain ZVS



## DAB Operation Analysis



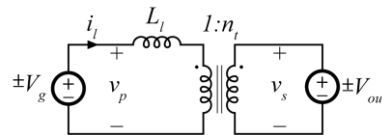
- Phase-shifted DAB has four unique intervals per half-period

- Primary dead time **I**
- Phase shift **II**
- Secondary dead time **III**
- Main power delivery **IV**

- Begin by considering DCX operation, where

$$V_{out} = n_t V_g$$

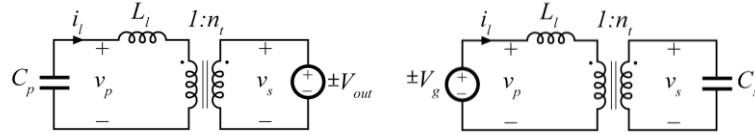
- Even-numbered intervals, circuit reduces to





# Resonant Interval Analysis

- Resonant intervals have equivalent circuits of the form:



- Both of which can be simplified to:

