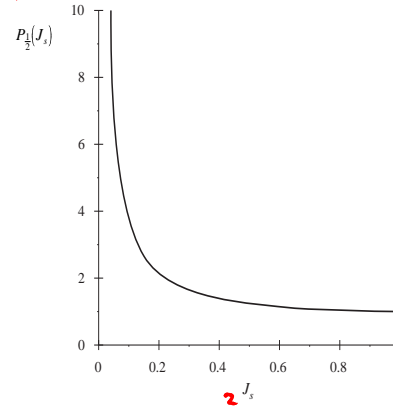




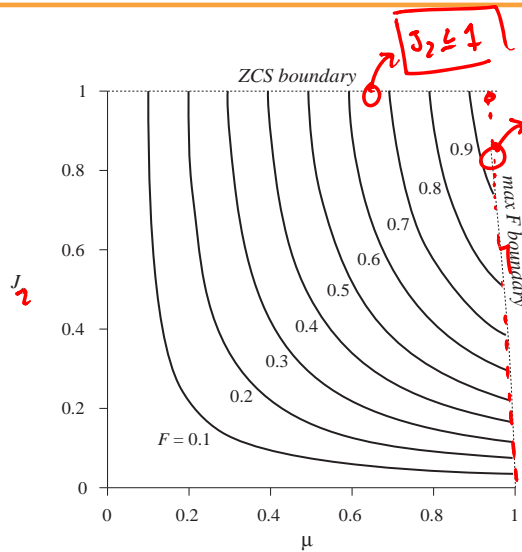
## Analysis result: switch conversion ratio $\mu$

$\downarrow$   
 $\mu \rightarrow F P_{V_2}(S_2)$

$P_{V_2}(S_2)$



## Characteristics of the half-wave ZCS resonant switch



Constraint:

$$\frac{2\pi}{F} \geq \alpha + \beta + \delta$$

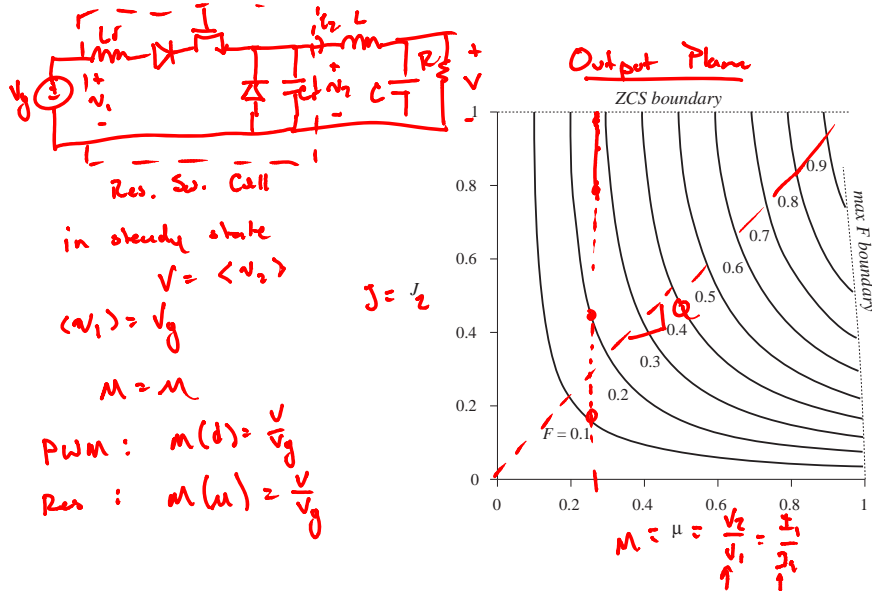
$$\mu = \frac{F}{2\pi} [\frac{\alpha}{2} + \beta + \delta]$$

$$\mu \leq \frac{F}{2\pi} [\alpha + \beta + \delta]$$

$$\mu \leq \mu + \frac{F}{2\pi} \frac{S_2}{2}$$

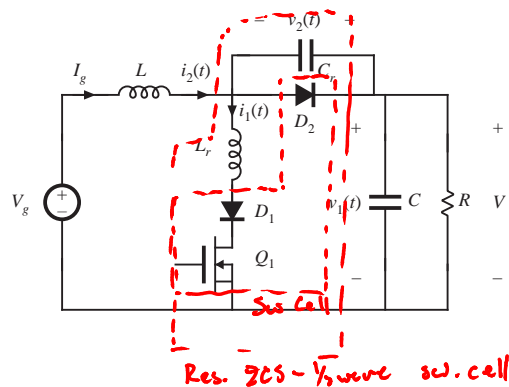
$$\boxed{\mu \leq 1 - \frac{F S_2}{4\pi}}$$

**UT** Buck converter containing half-wave ZCS quasi-resonant switch



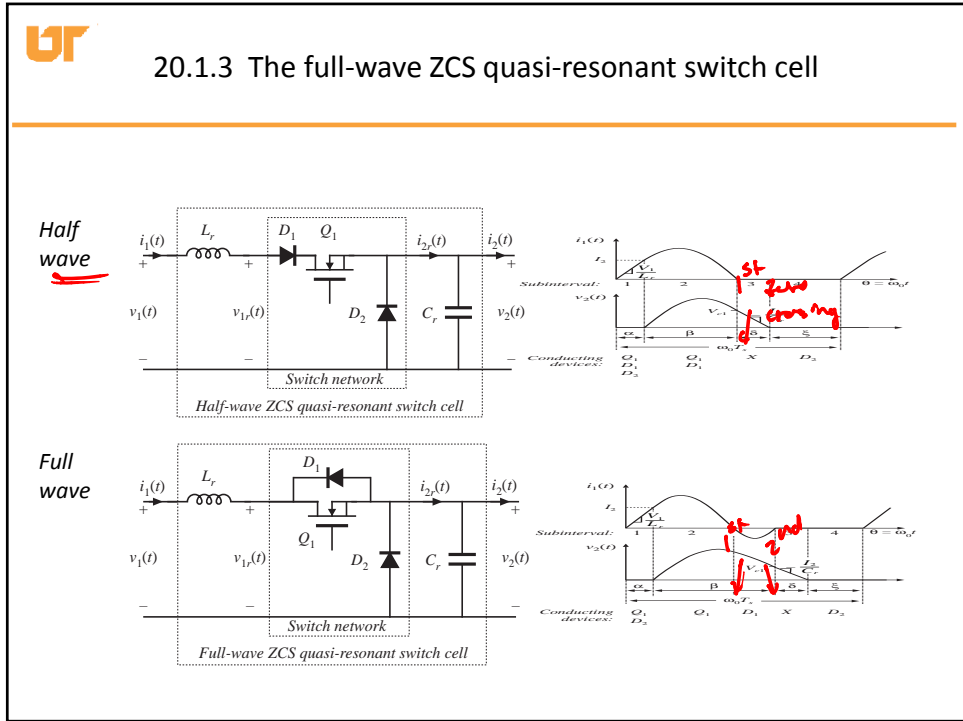
**UT** Boost converter example

$M = \frac{1}{1-\mu}$

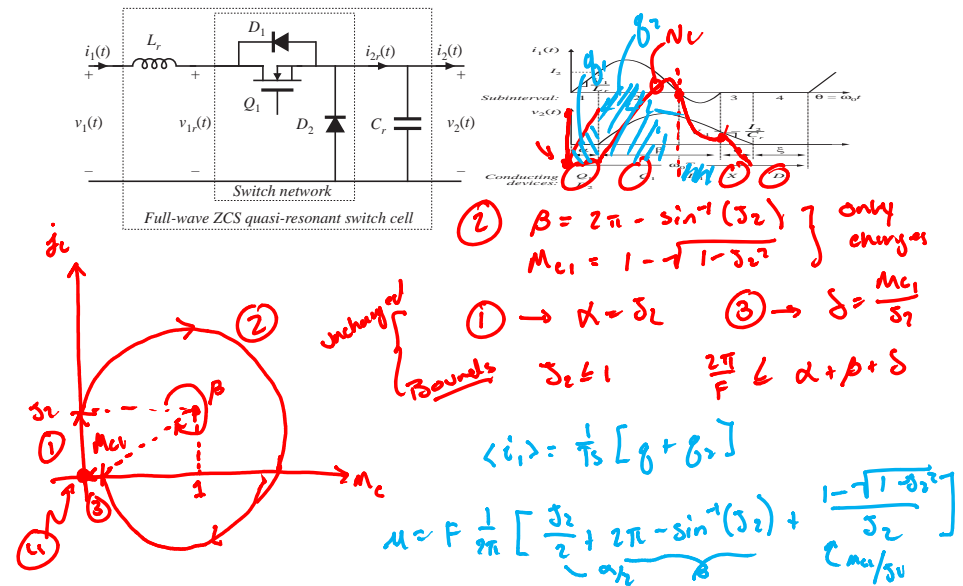




### 20.1.3 The full-wave ZCS quasi-resonant switch cell



### Analysis: full-wave ZCS





## Full-wave cell: switch conversion ratio $\mu$

looks like  $\mu > F$   
 if  $M_{c1} \approx \phi \neq \alpha \ll \frac{2\sqrt{F}}{F}$  ( $g_1 \ll g_2$ )

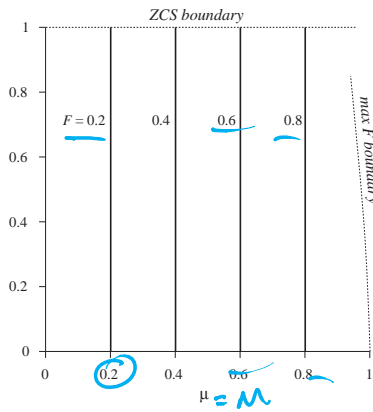
$$\langle i_1 \rangle \approx \frac{1}{T_s} [I_s t_2]$$

$$\mu = \frac{\langle i_1 \rangle}{I_s} \approx \frac{1}{T_s} t_2$$

if  $t_2$  is exactly one full period of resonator  $J = \frac{1}{2}$

$$t_2 = \frac{1}{f_0}$$

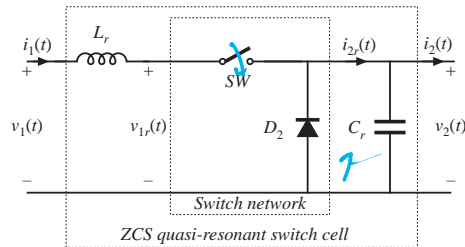
$$\mu = \frac{f_s}{f_0} = F$$



## 20.2 Resonant switch topologies

Basic ZCS switch cell:

ZCS - QR



SPST switch SW:

- Voltage-bidirectional two-quadrant switch for half-wave cell
- Current-bidirectional two-quadrant switch for full-wave cell

Connection of resonant elements:

Can be connected in other ways that preserve high-frequency components of tank waveforms

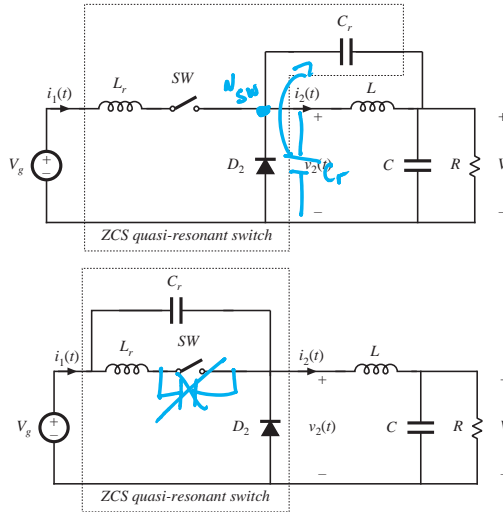


## Connection of tank capacitor

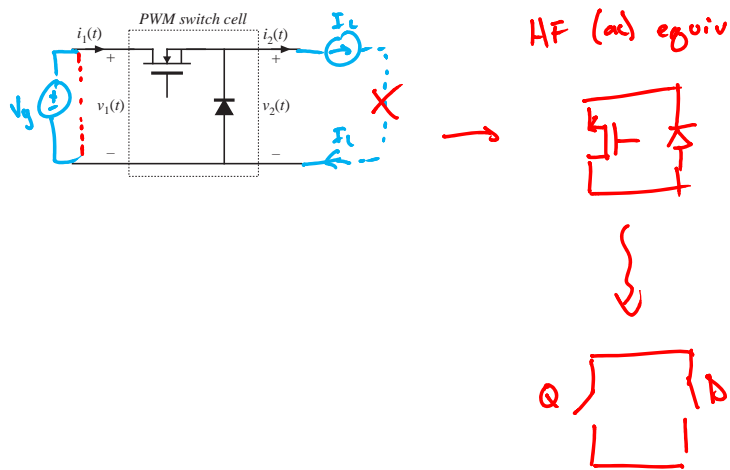
Connection of tank capacitor to two other points at ac ground.

This simply changes the dc component of tank capacitor voltage.

The ac high-frequency components of the tank waveforms are unchanged.



## PWM switch cell topology: HF (ac) view



**UT**

## PWM Switch Cell of PWM Converters

Converter examples

Buck

Boost

Buck-Boost

Cuk

High-frequency view of the switch network

Basic switch implementation options

- $Q$ : single-quadrant (transistor)
- $D$ : single-quadrant (diode)
- $Q$ : current-bidirectional (e.g. MOSFET)
- $D$ : current-bidirectional synchronous rectifier (e.g. MOSFET)

The slide shows four converter topologies: Buck, Boost, Buck-Boost, and Cuk. Each is represented by a circuit diagram with a red dashed box highlighting the switch network. Blue arrows point from these networks to a central square circuit labeled 'High-frequency view of the switch network'. This square circuit has a switch  $Q$  on the left and a diode  $D$  on the right. A legend below explains the implementation options for  $Q$  and  $D$ .

**UT**

## A test to determine the topology of a resonant switch network

Replace converter elements by their high-frequency equivalents:

- Independent voltage source  $V_g$ : short circuit
- Filter capacitors: short circuits
- Filter inductors: open circuits

The resonant switch network remains.

If the converter contains a ZCS quasi-resonant switch, then the result of these operations is

$ZCS-QR$

The diagram shows a series combination of an inductor  $L_r$  and a switch  $SW$ . This is connected to a parallel combination of a diode  $D_2$  and a capacitor  $C_r$ . The current through the inductor is labeled  $i_1(t)$  and the voltage across the capacitor is  $v_2(t)$ . Blue arrows and the label 'ZCS-QR' point to the switch and the parallel branch.

**UT**

## ZCS-QR

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Converter examples

Buck

Boost

Buck-Boost

Cuk

High-frequency view of the switch network

Basic switch implementation options

- $Q$ : single-quadrant (transistor)
- $D$ : single-quadrant (diode)
- $Q$ : current-bidirectional (e.g. MOSFET)
- $D$ : current-bidirectional synchronous rectifier (e.g. MOSFET)

**UT**

## Zero-current and zero-voltage switching

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ZCS quasi-resonant switch:

- Tank inductor is in series with switch; hence SW switches at zero current
- Tank capacitor is in parallel with diode  $D_2$ ; hence  $D_2$  switches at zero voltage

Discussion

- Zero voltage switching of  $D_2$  eliminates switching loss arising from  $D_2$  stored charge. (ca) ✓
- Zero current switching of SW: device  $Q_1$  and  $D_1$  output capacitances lead to switching loss. In full-wave case, stored charge of diode  $D_1$  leads to switching loss. (cos) X (Lp, Ls) ✓
- Peak transistor current is  $(1 + J_s) V_g / R_0$ , or more than twice the PWM value. ✓