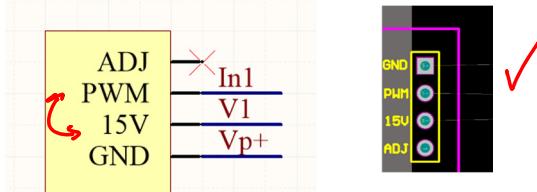


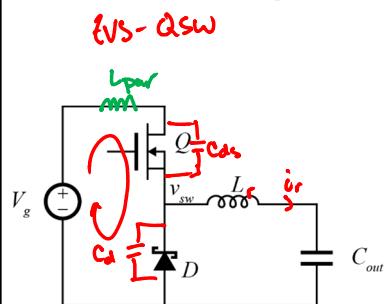
Announcements

- Updated PCB Layout

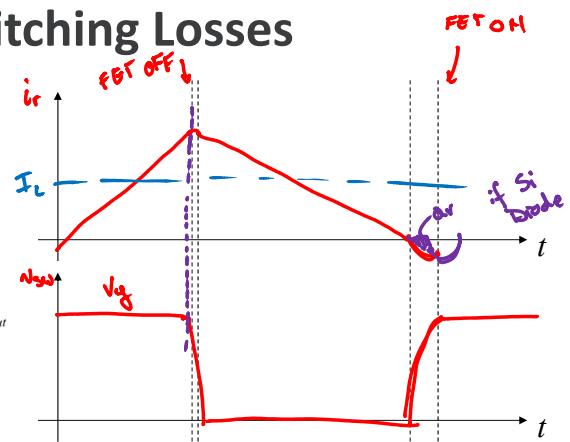


THE UNIVERSITY OF
TENNESSEE
KNOXVILLE

Remaining Switching Losses



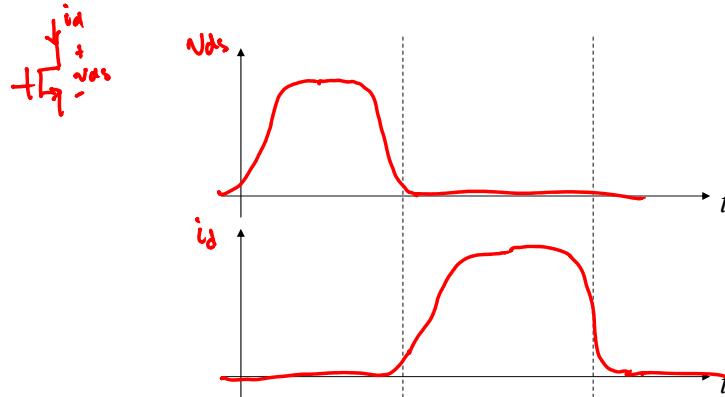
- Switching losses
- 1. Coss / Cd ✓
- 2. Overlap ON ✓
- 3. Overlap OFF ↓
- 4. Reverse Recovery ↓



- 5. Qg - from Gate Driver —
- 6. Body Diode conduction —
- 7. Lpar losses —
- 8. $\frac{dv}{dt}$ induced gate drive distortion

THE UNIVERSITY OF
TENNESSEE
KNOXVILLE

Idealized Switching Waveforms



THE UNIVERSITY OF
TENNESSEE 
KNOXVILLE

Chapter 20: Resonant Switch Topologies

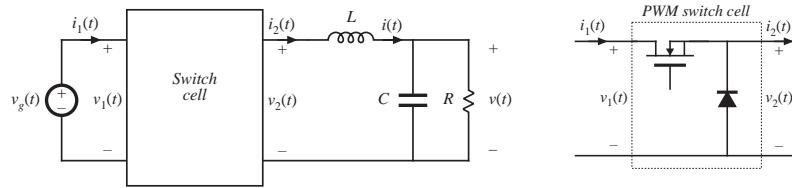
- Introduction
- 20.1 The zero-current-switching quasi-resonant switch cell
 - 20.1.1 Waveforms of the half-wave ZCS quasi-resonant switch cell
 - 20.1.2 The average terminal waveforms
 - 20.1.3 The full-wave ZCS quasi-resonant switch cell
- 20.2 Resonant switch topologies
 - 20.2.1 The zero-voltage-switching quasi-resonant switch
 - 20.2.2 The zero-voltage-switching multiresonant switch
 - 20.2.3 Quasi-square-wave resonant switches
- 20.3 Ac modeling of quasi-resonant converters
- 20.4 Summary of key points

THE UNIVERSITY OF
TENNESSEE 
KNOXVILLE

The resonant switch concept

General idea:

- 1. PWM switch network is replaced by a resonant switch network
- 2. This leads to a quasi-resonant version of the original PWM converter



Example: realization of the switch cell in the buck converter

THE UNIVERSITY OF
TENNESSEE KNOXVILLE

