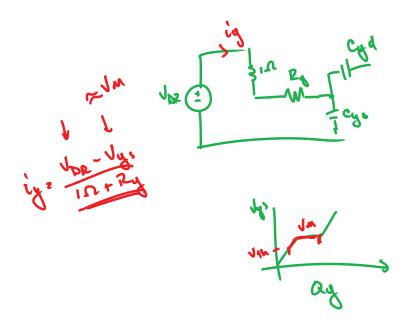
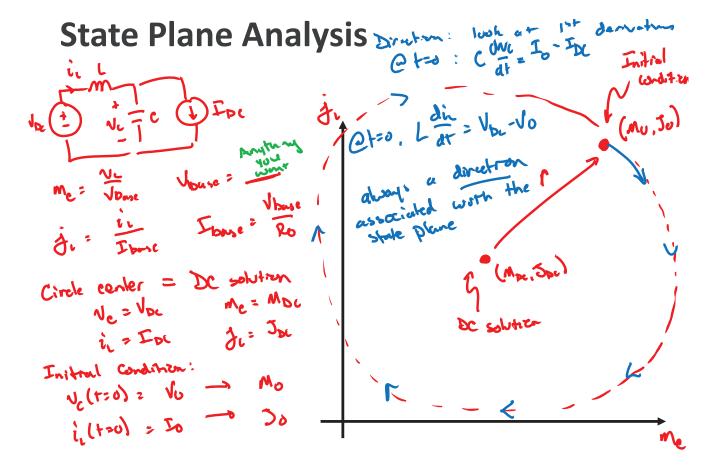
Announcements

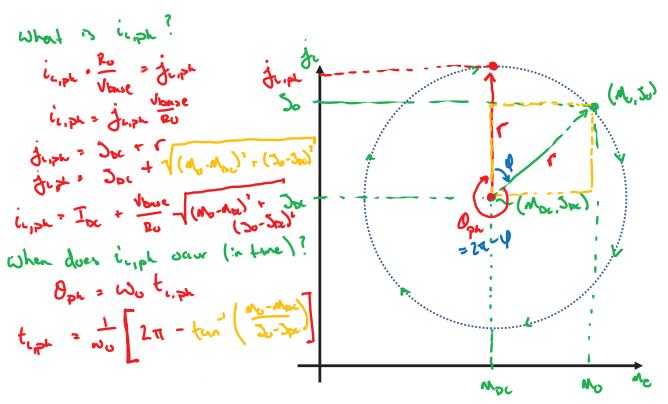
No Office Hours Tuesday, 9/18



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Example Analysis



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State Plane Algorithm

Stort w | Circuit with 1 L \$ 1 C \$ DC bias

- May regume manipulation or approximation to rachee

circuit to 11-1C.

(1) Normalize:
$$M_X = \frac{J_X}{V_{Dase}}$$
, V_{Dase} is unastracted $J_X = \frac{i_X}{I_{Dase}}$, $I_{Dase} = \frac{V_{Dase}}{R_0}$
 $J_X = \frac{i_X}{I_{Dase}}$, $I_{Dase} = \frac{V_{Dase}}{R_0}$

(2) Plut resonant trajectory on Mx-fx axes

- forms a well with:

- center @ DC solution

- one point @ intial condition

one point @ intial condition

(3) Solve for parameters of interest on state plane

(4) Denormalize.

