Typical WSN deployment environment is prone to various malicious attacks

What makes security for WSN so unique ...

- Scarce resources - energy, memory, computation
- Ad-hoc nature and extreme dynamic environments
- High node density (scalability)
- Existing security solutions can not be directly applied

Moreover, ad-hoc node cluster formation is unique

- Unpredictable location, scope and dynamics
- Requires short response times
Public key infrastructure (PKI) is a powerful and proven technology for addressing Confidentiality, Authentication and Message integrity. However, due to resource limitations in WSN, existing PKI solutions cannot be directly applied due to low computational capabilities, limited memory space, and energy constraints imposed on communications. It would be highly desirable to have public key generation methodologies specifically designed and optimized for ad-hoc clusters of wireless sensor nodes.
Prior Work: Random Key Pre-distribution Schemes

- Each node \( (i) \) is loaded with a small subset, \( C_i \), of a large key chain, \( C \), prior to deployment
  - Two nodes that wish to communicate are required to identify a common key
  - If they do not share a common key, a “key discovery” process is required
- Fundamental limitations of random key pre-distribution
  - Scalability – w.r.t. node memory and network size
  - Communication framework – finding nodes that share keys
  - Cryptographic robustness – inherently offers “statistical” robustness, which is always questionable

Public key distribution systems, if made feasible, overcome all of the above limitations
What is an Elliptic Curve?

In \( GF(p) \) an ordinary elliptic curve \( E \) suitable for elliptic curve cryptography is defined by the set of points \((x; y)\) that satisfy the equation:

\[
y^2 = (x^3 + a \cdot x + b) \mod p \quad a; b \in GF(p)
\]

Why use Elliptic Curve Cryptography (ECC)?

- Shorter key sizes (160 bit ECC cryptocomplexity equivalent to 1024 bit RSA)
- Faster calculations
- Less memory is required
- Recent work established its viability for WSN
Self Certified DH Key Generation: Fixed Key Addressing the Authentication Issue

The CA (Certifying authority) provides each user with a set of public and private keys: \((U_v, X_v)\)

\[ x_i \times [H(ID_j, U_j) \times U_j + R] = x_i \times [H(ID_i, U_i) \times U_i + R] \]

- **ID** \(_v\): identification of node \(v\)
- **U** \(_v\): user \(v\)'s public key, generated by the CA
- **X** \(_v\): user \(v\)'s private key, generated by the CA
Self Certified DH Key Generation: Ephemeral Key Addressing the Authentication Issue

The CA (Certifying authority) provides each user with a set of public and private keys: \((U_v, X_v)\)

\[
P_{v_i} \times [H(ID_j, U_j) \times U_j + R] + (x_i + P_{v_i}) E_v_j = P_{v_j} \times [H(ID_i, U_i) \times U_i + R] + (x_j + P_{v_j}) E_{v_i}
\]

\(ID_v\): identification of node \(v\) - scalar
\(U_v\): user \(v\)'s public key, generated by the CA - a point on the curve
\(X_v\): user \(v\)'s private key, generated by the CA - scalar
\(P_{v_v}\): a random number generated by user \(v\) - scalar
\(E_{v_v} = P_{v_v} \times G\)
Intel Mote 2 Sensor Network Platform

Electronic

- 320/416/520MHz PXA271 XScale Processor (Dynamic voltage scaling)
- Programming in NeSC
- 32MB Flash on-board
- 32MB SDRAM on-board
- Mini-USB Client (slave), multiplexed with RS232 console over USB, power
- I-Mote2 Basic Sensor connector (31+ 21 pin connector)
- Zigbee [802.15.4] Radio (ChipCon CC2420)
- Tri-color status LED; Power LED; battery charger LED, console LED
- Switches: on/off slider, Hard reset, Soft reset, User programmable switch

Mechanical

- Size: 1.89inches x 1.42in. PCB Thickness 0.069in
- Size: 48mm x 36mm. PCB Thickness 1.75mm
Fixed key requires one on-line point by scalar multiplication

\[ x_i[H(ID_j, U_j) \times U_j + R] = x_iH(ID_j, U_j) \times U_j + x_i R \]

Ephemeral key requires two on-line point by scalar multiplication

\[ P_{v_i} [H(ID_j, U_j) \times U_j + R] + (x_i+P_{v_i}) E_{v_j} = P_{v_i} \times (H(ID_j, U_j) \times U_j + (x_i+P_{v_i}) (E_{v_j} + R) - x_i \times R \]

<table>
<thead>
<tr>
<th>Scalar Point Multiplication</th>
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<tbody>
<tr>
<td>EccM</td>
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<tr>
<td>Time (msec)</td>
</tr>
<tr>
<td>190</td>
</tr>
</tbody>
</table>

| TinyEcc                     |
| Time (msec)                 | Voltage (v) | Current (mA) | Energy (mJ) |
| 42                          | 3           | 1.8          | 22          |

<table>
<thead>
<tr>
<th>Radio Transmission (including a 7 byte header)</th>
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<tbody>
<tr>
<td>Time (msec)</td>
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<td>~15</td>
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</table>
Intel Mote 2 Implementation Results (cont.)

- ECC-based 160-bit key generations
  - Includes all computation & communication overheads
- CPU clock frequency ranges from 13 MHz to 312 MHz