Students Name\textsuperscript{1}: 1. 

Check (√)

1. Designing of the flip-flop schematic:

2. Symbol generation:

3. Pre-Spectre waveform of flip-flop

4. Drawing the layout of flip-flop:

5. Doing “Extraction” of the layout

6. Post Spectre simulation:

7. Doing LVS generation:

8. Netlist matches?

9. Rise and fall time =

10. Data for delay vs load capacitance ($C_L$) curve?
    (Vary the $C_L$ from 1pF – 5pF)

\textsuperscript{1} This check-out form must be attached with the report.