TA Check Off Sheet

Lab 8

Student name:

1. Setting up cadence 6
2. Drawing the schematic of the inverter
3. Changing W/L ratio
4. Creating the symbol of the inverter
5. Drawing the layout of the inverter
6. Check DRC
7. Check LVS
8. Setting up test bench for inverter simulation
9. Input and output waveforms of the inverter.

TA Signature