

Steven Robert Young

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EDUCATION

University of Tennessee, Knoxville

Major: Computer Engineering, PhD Graduation Date: December, 2014
GPA: 3.81

University of Tennessee, Knoxville

Major: Electrical Engineering, BS Graduation Date: May, 2010
GPA: 3.88

EXPERIENCE

Machine Intelligence Lab, Knoxville, TN (Nov. 2008 – Present) – Research Assistant

- Developing deep machine learning algorithms
- Collaborating with analog device researchers to implement deep machine learning algorithms in analog electronics

University of Tennessee, Knoxville, TN (Aug. 2011 – May 2013) – Teaching Assistant

- Taught lab for freshman level computer science course
- Developed new lab assignments to replace assignments previously taught using outdated software.

ORNL - Computational Data Analytics Group, Oak Ridge, TN (July 2009 – Aug. 2009, May 2010 – Aug. 2010, May 2011 - Aug. 2011)- Intern

- Implemented MS SharePoint text mining tool that has been licensed to a private company - *Raptor: An Enterprise Knowledge Discovery Engine (Version 2.0)*
- Met with sponsors to discuss project requirements
- Became proficient in C#, Java, and MS SQL

GE Consumer and Industrial, Louisville, KY (Jan. 2008 – May 2008) - Co-op

- Managed cost take out projects on sourced products
- Checked supplier and in house test data for conformance to product specification
- Interfaced with marketing, service, quality, and suppliers

Honeywell Aerospace, Clearwater, FL (Jan. 2007 – May 2007) - Co-op

- Ran/Built simulations in Matlab and Simulink
- Helped rewrite/modify process for testing a product
- Identified error in product code and analyzed its impact

HONORS

- Outstanding GTA - EECS Department (2012-2013)
- J. Wallace & Katie Dean Graduate Fellowship (2010-2014)
- Billy and Sylvia Moore Scholarship (2008-2010)
- Charles and Martha Sprankle Scholarship (2006-2007)
- Alumni Valedictorian Scholarship (2005-2007)
- James L. Howard Scholarship (2005-2006)

ACTIVITIES

- Eta Kappa Nu – Vice President (2009-2010), Corresponding Secretary (2010-2012)
- IEEE – Student Member
- Tau Beta Pi - Member

PUBLICATIONS

- S. Young, A. Davis, A. Mishtal, I. Arel, "Hierarchical spatiotemporal feature extraction using recurrent online clustering", *Pattern Recognition Letters*, Volume 37, Pages 115-123, Feb. 2014
- J. Lu, S. Young, I. Arel, J. Holleman, "A 1~TOPS/W Analog Deep Machine-Learning Engine with Floating-Gate Storage in 0.13um CMOS" *IEEE International Solid-State Circuits Conference, Digest of Technical Papers*, Feb. 2014.
- S. Young, J. Lu, J. Holleman, I. Arel, "On the Impact of Approximate Computation in an Analog DeSTIN Architecture," *Neural Networks and Learning Systems, IEEE Transactions on* , vol.PP, no.99, pp. 1, Oct. 2013
- J. Lu, S. Young, I. Arel, J. Holleman, "An Analog Online Clustering Circuit in 130nm CMOS," *in Proc. IEEE Asian Solid-State Circuits Conference* , Nov. 2013
- S. Young, I. Arel, "Recurrent Clustering for Unsupervised Feature Extraction with Application to Sequence Detection," *Machine Learning and Applications (ICMLA), 2012 11th International Conference on* , vol.2, no., pp.54,55, 12-15 Dec. 2012
- T. Karnowski, I. Arel, S. Young, "Modeling Temporal Dynamics with Function Approximation in Deep Spatio-Temporal Inference Network," *in Proc. 2nd International Conference on Biologically Inspired Cognitive Architectures (BICA)* , Nov. 2011
- S. Young, I. Arel, T. Karnowski, D. Rose, "A Fast and Stable Incremental Clustering Algorithm," *Information Technology: New Generations (ITNG), 2010 Seventh International Conference on* , vol., no., pp.204,209, 12-14 April 2010
- S. Young, I. Arel, O. Arazi, "Pi-PIFO: A Scalable Pipelined PIFO Memory Management Architecture," *Proc. of the 10th IEEE International Conference on Telecommunications* , June 2009