

$$\hat{x} = e^{A_2 t_2} (A_1 - A_2) e^{A_1 t_1} X_0 \hat{t} + e^{A_2 t_2} A_1^{-1} (e^{A_1 t_1} A_1 \hat{t}) B_1 u - e^{A_2 t_2} A_2 \hat{t} A_1^{-1} (e^{A_1 t_1} + \underline{e^{A_1 t_1} A_1 \hat{t}} - I) B_1 u - A_2^{-1} (e^{A_2 t_2} A_2 \hat{t}) B_2 u$$

Note: $A e^{At} \Rightarrow e^{At} A$ $\left(A \sum_{k=0}^{\infty} \frac{A^k}{k!} = \left(\sum_{k=0}^{\infty} \frac{A^k}{k!} \right) A \right)$

$$\hat{x} = \hat{t} e^{A_2 t_2} \left[(A_1 - A_2) e^{A_1 t_1} X_0 + \cancel{A_1^{-1} e^{A_1 t_1} A_1} B_1 u - A_2 A_1^{-1} (e^{A_1 t_1} - I) B_1 u - \cancel{A_2 A_1^{-1} (e^{A_1 t_1} A_1 \hat{t})} B_1 u - \cancel{A_2^{-1} A_2} B_2 u \right]$$

By 1st order expansion/linearization

$$\hat{x} = \hat{t} e^{A_2 t_2} \left[(A_1 - A_2) e^{A_1 t_1} X_0 + \underline{A_1^{-1} (e^{A_1 t_1} - I) B_1 u + B_1 u} - A_2 A_1^{-1} (e^{A_1 t_1} - I) B_1 u - B_2 u \right]$$

$$\hat{x} = \hat{t} e^{A_2 t_2} \left[(A_1 - A_2) e^{A_1 t_1} X_0 + (A_1 - A_2) \left[A_1^{-1} (e^{A_1 t_1} - I) B_1 u \right] + (B_1 - B_2) u \right]$$

$$\hat{x} = \hat{t} e^{A_2 t_2} \left[(A_1 - A_2) \left(e^{A_1 t_1} X_0 + A_1^{-1} (e^{A_1 t_1} - I) B_1 u \right) + (B_1 - B_2) u \right]$$

$$\frac{d\hat{x}}{dt} = e^{A_2 t_2} \left[(A_1 - A_2) X_p + (B_1 - B_2) u \right]$$

$$\Gamma = e^{A_2 t_2} \left[(A_1 - A_2) X_p + (B_1 - B_2) u \right] \Gamma_s - \frac{d\hat{x}}{dt}$$

$X(DT_s) \equiv X_p$

unit conversion



Converter Small Signal Model

Expedited derivation:

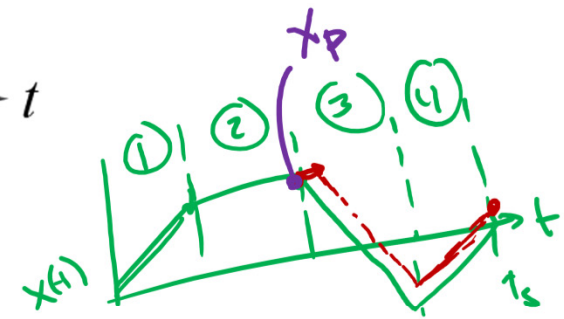
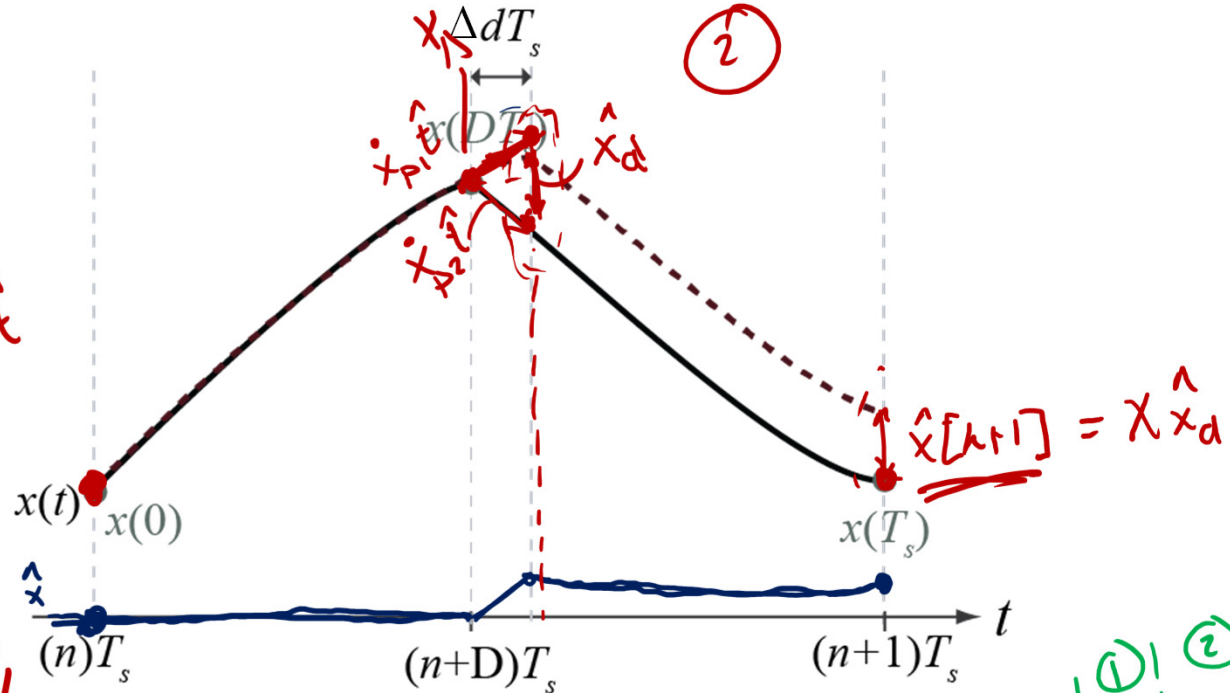
$$\begin{aligned} \hat{x}_d &= [A_1 x_p + B_1 u] \hat{t} \\ &\quad - [A_2 x_p + B_2 u] \hat{t} \\ &= [(A_1 - A_2) x_p + (B_1 - B_2) u] \hat{t} \end{aligned}$$

$$\hat{x}[k+1] = e^{A_2 t_2} \hat{x}_d$$

$$\Gamma = e^{A_2 t_2} [(A_1 - A_2) x_p + (B_1 - B_2) u]$$

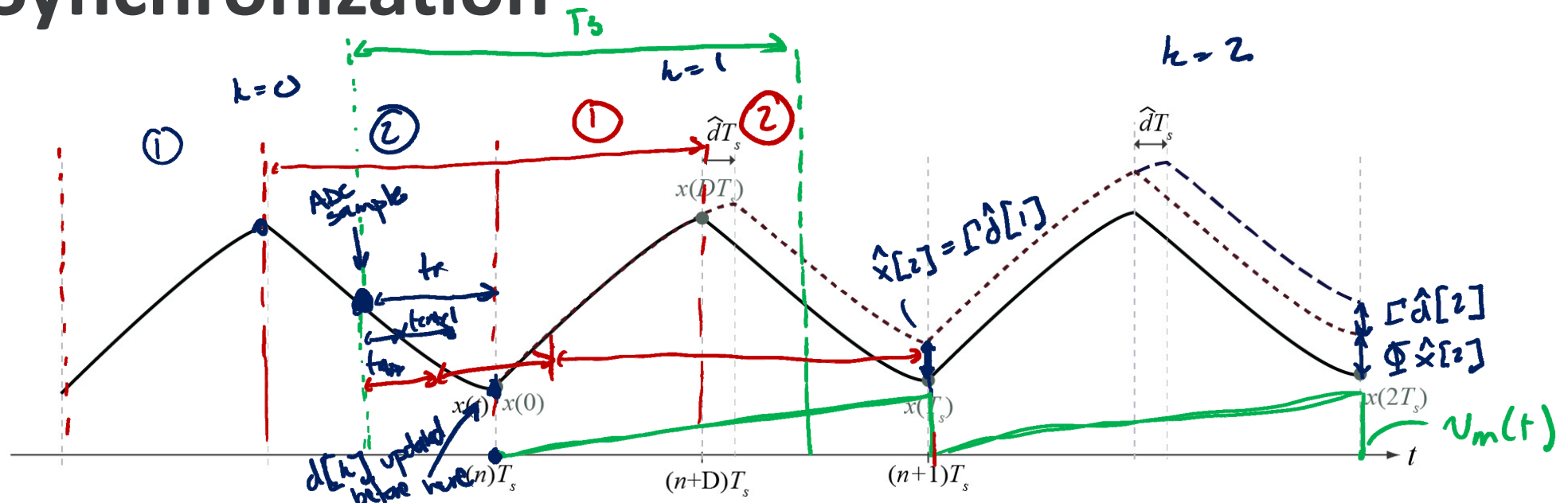
or solely increase t_1 without decreasing t_2 (variable f_s modulation)

$$\Gamma = e^{A_2 t_2} [A_1 x_p + B_1 u]$$



$$\Gamma = e^{A_4 t_4} e^{A_3 t_3} [(A_2 - A_3) x_p + (B_2 - B_3) u]$$

ADC Synchronization



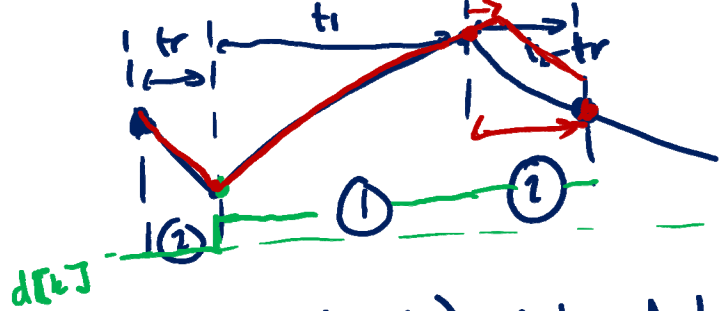
$$\hat{\hat{x}}[k+1] = \bar{\Phi} \hat{x}[k] + \Psi \hat{u}[k] + \Gamma \hat{d}[k]$$

t_r is time between ADC sample & next PWM update

t_{ADC} → ADC conversion time

t_{ctrl} → control delay to compute next $d[k]$

as long as $t_r \geq t_{ADC} + t_{ctrl}$, this works, otherwise additional T_s delay



$$\left\{ \begin{aligned} \Phi &= e^{A_2(t_2 - t_r)} e^{A_1 t_1} e^{A_2 t_r} \\ \Gamma &= e^{A_1(t_2 - t_r)} \left[(A_1 - A_2) X_p + (B_1 - B_2) u \right] T_s \\ \Psi &= \sum \Pi (\dots) \end{aligned} \right.$$

mode of plant with
ADC, PWM sampling & delay
included