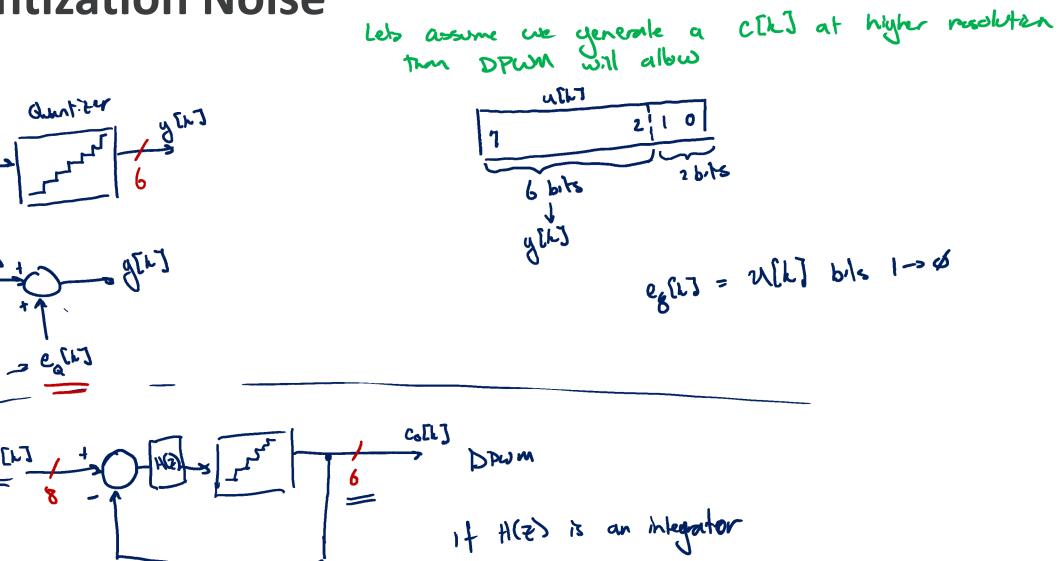
Quantization Noise

NEL

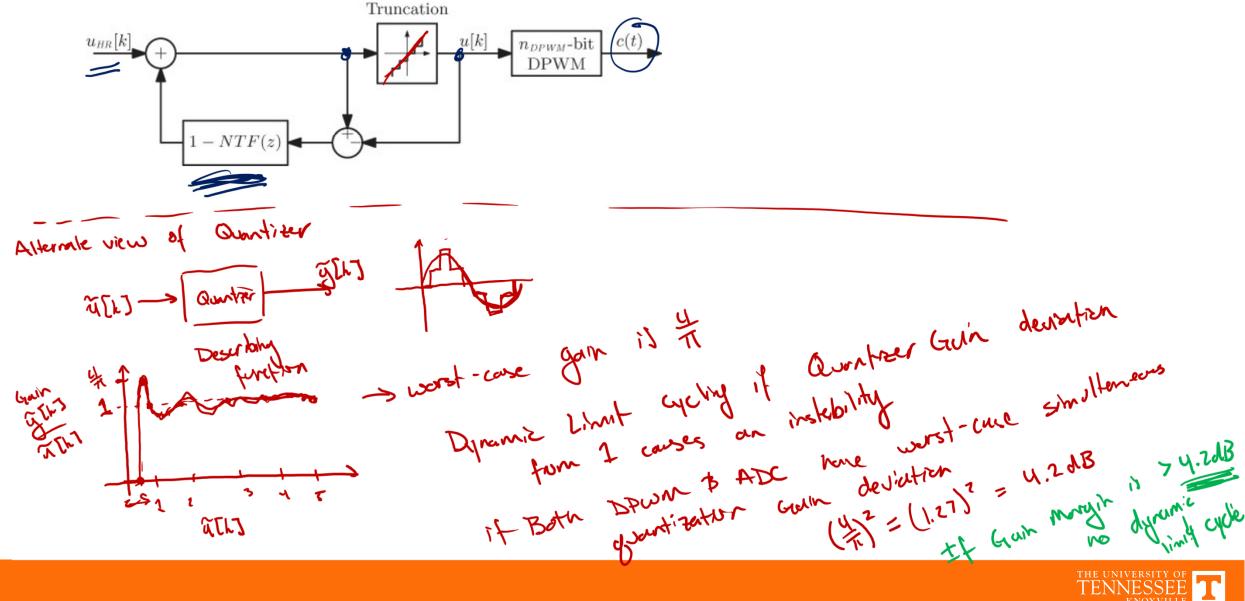
ults

C'[r]



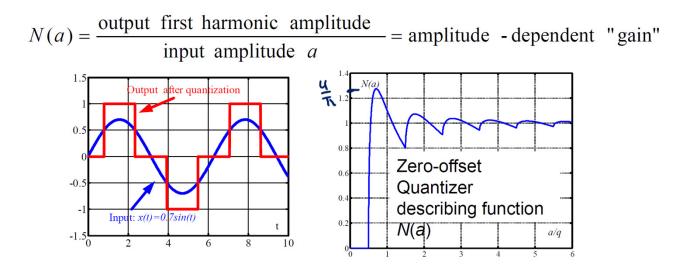


ΣΔ Modulation



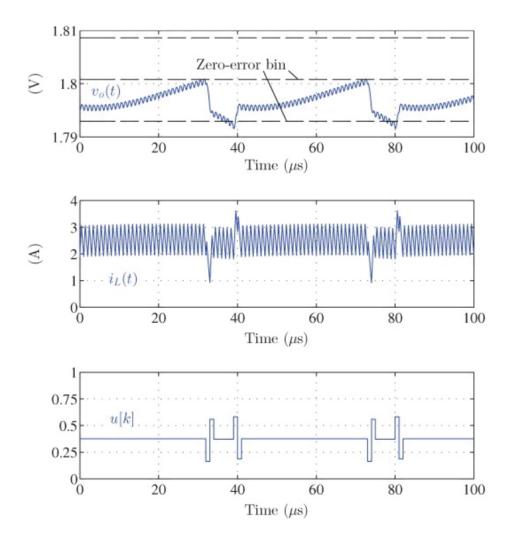


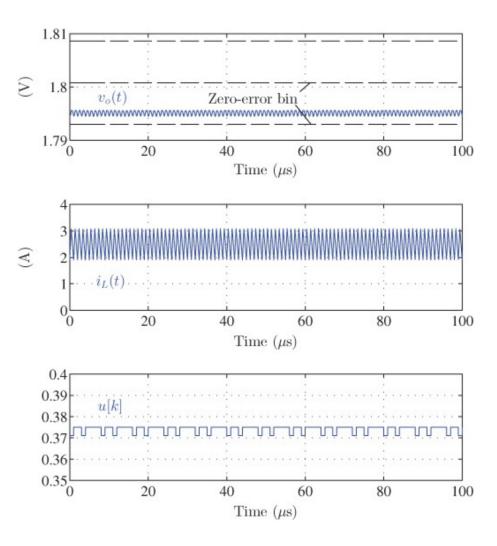
Dynamic Limit Cycling Condition





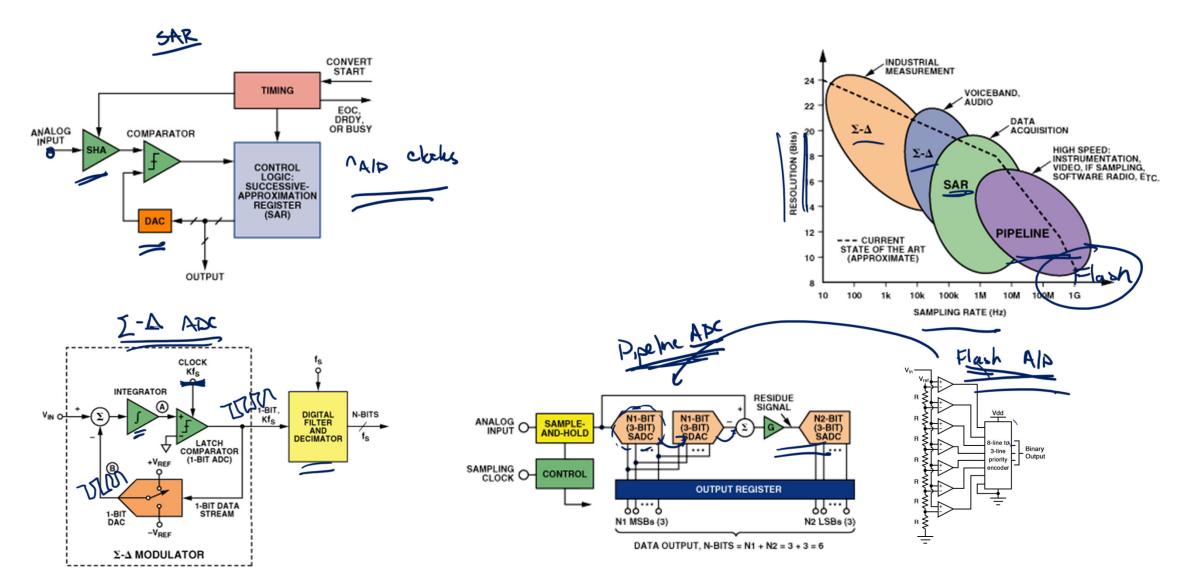
H.Peng, D.Maksimovic, A.Prodic, E.Alarcon, "Modeling of quantization effects in digitally controlled DC-DC converters," IEEE PESC 2004, pp.4312-4318.







ADC implementation





Z-transform

$$Z\{h\} \triangleq \sum_{k=0}^{+\infty} h[k]z^{-k} = H(z)$$

Properties:

- 1. Linearity
- 2. Delay: z^{-1} represents a one-sample delay
- 3. Discrete convolution of two signals is multiplication of their ztransforms
- 4. The frequency response of any stable system is equal to H(z) evaluated with $z=e^{j\theta}$, $\theta = \omega T_s \rightarrow \tau_s$ is sample, period



Pole Mapping

