

# Limitations and Discussion

steady-state

$$x_{ss} = X_0 = \left( I - \prod_{i=k}^0 e^{A_i t_i} \right)^{-1} \sum_{i=1}^k \left( \prod_{j=k}^{i+1} e^{A_j t_j} \right) A_i^{-1} (e^{A_i t_i} - I) B_i u_i$$

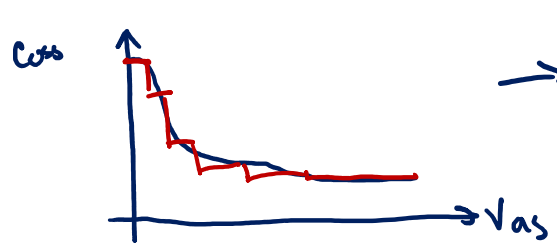
Gives steady-state solution to any arbitrary periodically switched system where:

- 1) Valid circuit which possesses a periodic steady-state solution
- 2)  $u(t) \approx u_i$  within one subinterval
- 3) All  $A_i$  nonsingular
- 4) Within any subinterval, we need to be able to model circuit w/ LTI equivalent
- 5) All  $A_i, B_i, t_i$  are known

Discuss

- 1) Satisfied for circuits of interest
- 2) Static or quasi-static inputs must be true for converter to respond  
Alternative is to model the quickly-varying input as an internal waveform
- 3) We will develop methods next

④ Nonlinear elements like  $\cos(v_{as})$ ,  $R_{ac}(f)$ ,  $P_{fe}$  cannot be included in  $A_i \neq B_i$   
 can model with piecewise linear elements



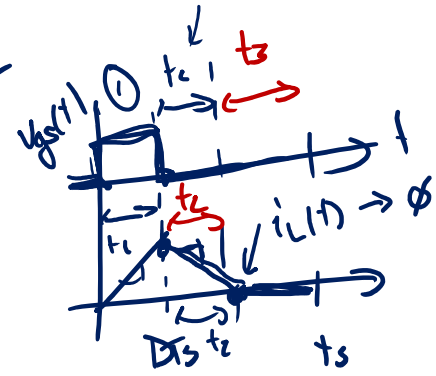
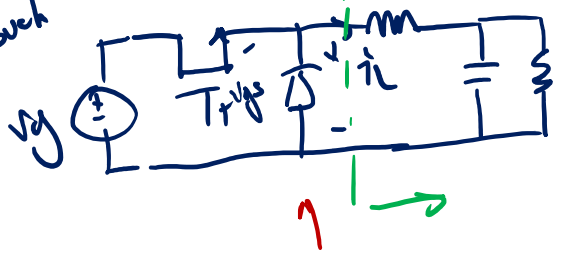
→ can add additional  $A_i \neq B_i$

High- $\rightarrow$  approximation or otherwise just neglect & calculate afterwards

⑤ Can solve LTI equivalents for any  $A_i \neq B_i$   
 for  $t_i \rightarrow$  known for active time-dependent switching

"state-dependent" switching

e.g. DCM Buck



$$\dot{x}(t) = \underbrace{A(x, t)}_{\substack{\text{time-varying} \\ \text{with state-dependent}}} x(t) + \underbrace{B(x, t)}_{\substack{\text{nonlinear} \\ \text{switching}}} u(t)$$

# Topological Time Invariance (HW2)

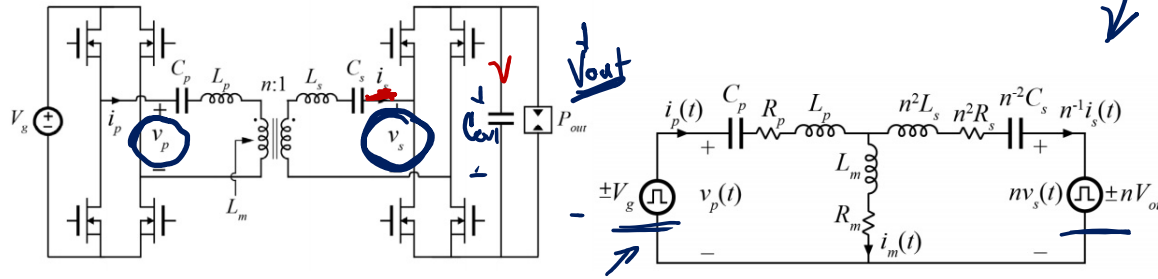


Fig. 1: Series Resonant Converter

Fig. 2: Time-Invariant Circuit Model of SRC

if  $u = [v_g \quad v_{out}]^T$ , All  $A_i$  will be the same  $\neq B_i$  differ only by  $\pm$  signs

Alternately, if we pick  $u(t) = [v_p(t) \quad v_s(t)]^T$

$A_i = A \neq B_i = B$   
Not time-varying

can model as a completely LTI system  
 $\dot{x} = Ax(t) + Bu(t)$

# Augmented State Space Approach

$$x(t) = e^{At}x_0 + \int_0^t e^{A(t-\tau)}Bu(\tau)d\tau \quad \longrightarrow \quad x(t) = e^{At}x_0 + \underline{A^{-1}(e^{At} - I)Bu}$$

Need a new way to deal with the convolution integral

*Handwritten notes:*  $u_i$  with a checkmark and an arrow pointing to the input  $u(\tau)$  in the integral term.

# Non-invertible A Matrices

$$C_i \rightarrow \begin{bmatrix} A_i & B_i \\ \emptyset & \emptyset \end{bmatrix} \rightarrow e^{C_i t} = \begin{bmatrix} e^{A_i t} & \int_0^t e^{A_i(t-\tau)} B_i \\ \emptyset & I \end{bmatrix}$$

$$C = \begin{bmatrix} \boxed{A_1} & B_1 & C_1 & D_1 \\ 0 & \boxed{A_2} & B_2 & C_2 \\ 0 & 0 & A_3 & B_3 \\ 0 & 0 & 0 & A_4 \end{bmatrix} \begin{matrix} \} n_1 \\ \} n_2 \\ \} n_3 \\ \} n_4 \end{matrix}$$

$\underbrace{\hspace{1.5cm}}_{n_1} \quad \underbrace{\hspace{1.5cm}}_{n_2} \quad \underbrace{\hspace{1.5cm}}_{n_3} \quad \underbrace{\hspace{1.5cm}}_{n_4}$

$$e^{Ct} = \begin{bmatrix} \boxed{F_1(t)} & G_1(t) & H_1(t) & K_1(t) \\ 0 & \boxed{F_2(t)} & G_2(t) & H_2(t) \\ 0 & 0 & F_3(t) & G_3(t) \\ 0 & 0 & 0 & F_4(t) \end{bmatrix}$$

$$F_j(t) = e^{A_j t}, \quad j=1,2,3,4$$

$$G_j(t) = \int_0^t e^{A_j(t-s)} B_j e^{A_{j+1}s} ds, \quad j=1,2,3$$

$$H_j(t) = \int_0^t e^{A_j(t-s)} C_j e^{A_{j+2}s} ds$$

$$+ \int_0^t \int_0^s e^{A_j(t-s)} B_j e^{A_{j+1}(s-r)} \cdot B_{j+1} e^{A_{j+2}r} dr ds, \quad j=1,2$$

$$K_1(t) = \int_0^t e^{A_1(t-s)} D_1 e^{A_4 s} ds + \int_0^t \int_0^s e^{A_1(t-s)} [C_1 e^{A_3(s-r)} B_3 + B_1 e^{A_2(s-r)} C_2] e^{A_4 r} dr ds$$

$$+ \int_0^t \int_0^s \int_0^r e^{A_1(t-s)} B_1 e^{A_2(s-r)} B_2 e^{A_3(r-w)} B_3 e^{A_4 w} dw dr ds.$$

Augmented circuit description:

$$\tilde{A} = \begin{matrix} n_s & p_i \\ \tilde{x} & \end{matrix} \begin{bmatrix} A_i & B_i \\ \emptyset & \emptyset \end{bmatrix} \begin{matrix} n_s \\ p_i \end{matrix} \rightarrow \text{Augmented state matrix}$$

$$\dot{\tilde{x}} = \tilde{A} \tilde{x} \rightarrow \text{homogeneous system}$$

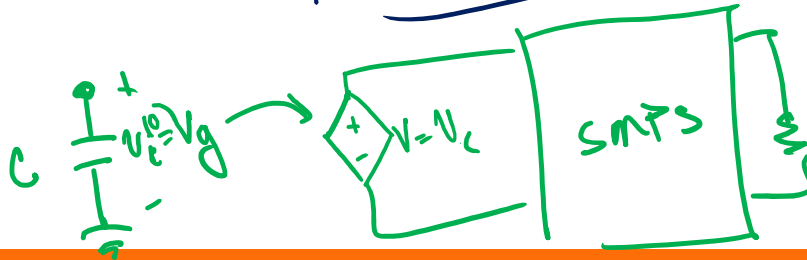
$$\tilde{x} = \begin{bmatrix} x \\ u \end{bmatrix}$$

$$\dot{\tilde{x}} = \begin{bmatrix} \dot{x} \\ \dot{u} \end{bmatrix} = \begin{bmatrix} A_i x + B_i u \\ \emptyset \end{bmatrix}$$

solution

$$\tilde{x}(t) = e^{\tilde{A}t} \tilde{x}_0$$

$u = V_g$ ,  $x \rightarrow$  actual circuit states



$$u = [V_g] \quad \tilde{x} = \begin{bmatrix} x \\ V_c \end{bmatrix} \quad A \rightarrow \tilde{A} = \begin{bmatrix} A_i & B_i \\ \emptyset & \emptyset \end{bmatrix}$$