

Inductance: Review

We know inductor has circuit behavior $V_1 = L_1 \frac{di_1}{dt}$



for an inductor

$$\Phi = \Phi_r$$

$$J_1 = \alpha \frac{d\Phi}{dt}$$

= L for single-turn

For an N -turn inductor

$$\Phi = N a i_1$$



$$V_1 = N \frac{d\Phi}{dt}$$

$$N_1 = N^2 \alpha \frac{d i_1}{d t}$$

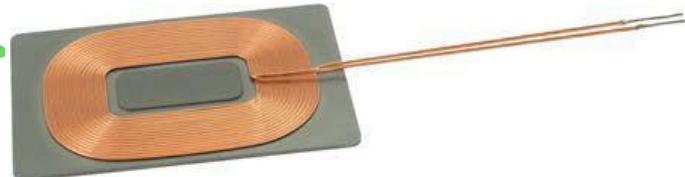
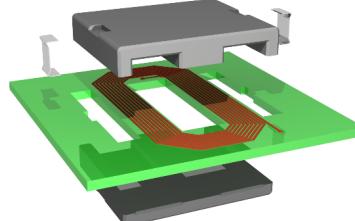
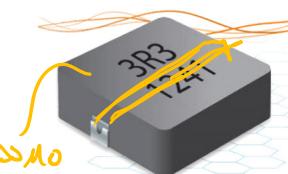
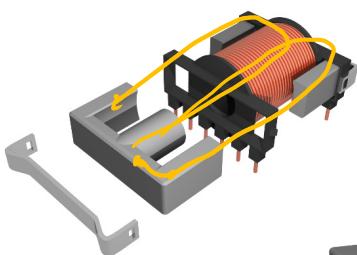
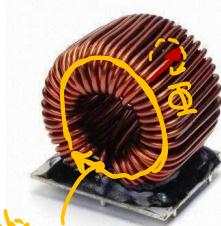
Amperes law
total flux
 $\oint B \cdot d\ell = \mu_0 I_{enc}$

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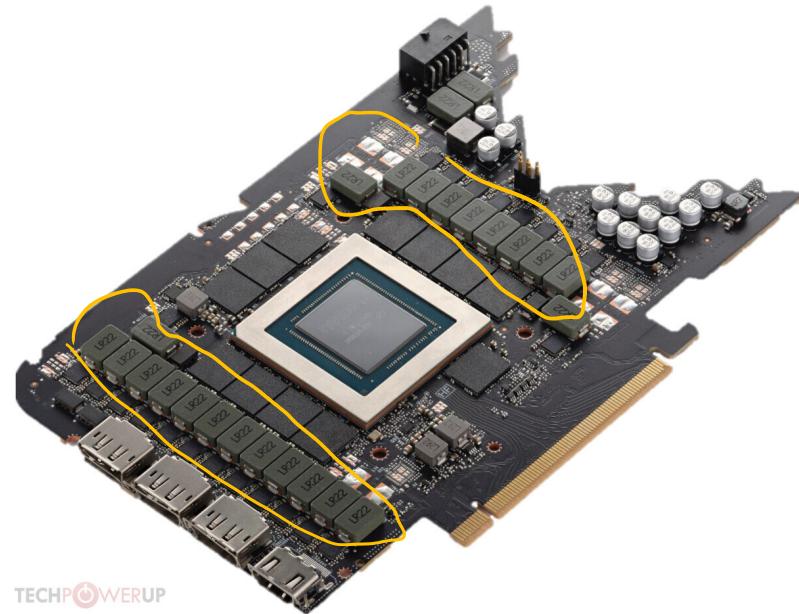
parameter depends on geometry & physical constants

Faraday's law
 $V_1 = \frac{d\Phi_r}{dt}$

Some Inductor Examples

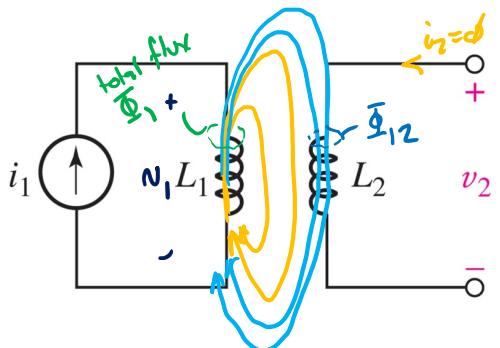


Example Desktop Motherboard and GPU



Magnetic Coupling

flux from one inductor goes through coils of another inductor



Case 1: $i_1 > \phi$ $i_2 = d\phi/dt$

L₁ behaves as before

$$\Phi_1 = \alpha_1 N_1 i_1$$

$$v_1 = N_1 \frac{d\Phi_1}{dt}$$

$$v_1 = \alpha_1 N_1^2 \frac{di_1}{dt}$$

$$L_1 = \alpha_1 N_1^2$$

Because

Φ_{12} flows through L₂

$$v_2 = N_2 \frac{d\Phi_{12}}{dt}$$

$$\Phi_{12} = \alpha_{12} N_1 i_1$$

→

$$v_2 = \alpha_{12} N_1 N_2 \frac{di_1}{dt}$$

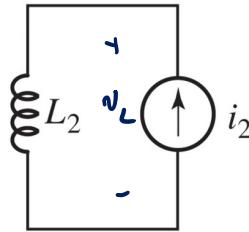
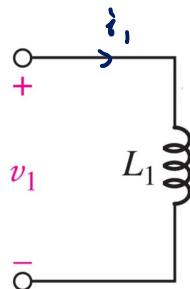
$$M = \alpha_{12} N_1 N_2$$

mutual Inductance

$$v_1 = L_1 \frac{di_1}{dt}$$

$$N_2 = M \frac{di_1}{dt}$$

Magnetic Coupling



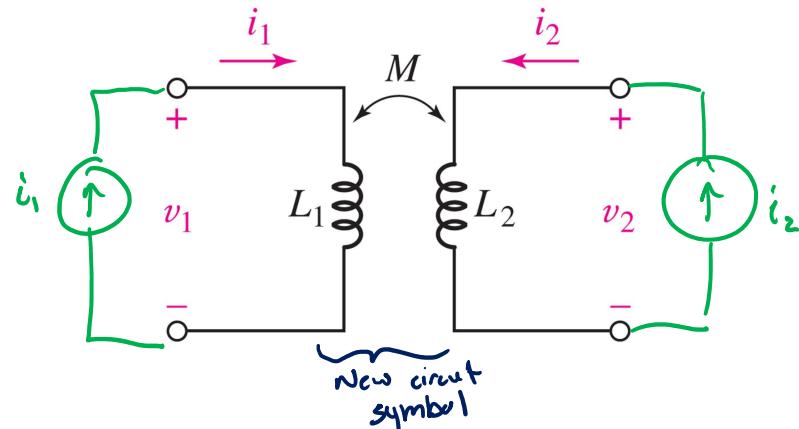
Case 2: $i_1 = \phi$, $i_2 \neq \phi$

Same circuit just mirrored

$$\left\{ \begin{array}{l} v_L = L_2 \frac{di_2}{dt} \\ v_1 = M \frac{di_2}{dt} \end{array} \right.$$

$\alpha_{12} = \alpha_{21} \rightarrow$ so M is the same in both sets of equations

Mutual Inductance



By superposition

$$\left\{ \begin{array}{l} v_1 = L_1 \frac{di_1}{dt} \pm M \frac{di_2}{dt} \\ v_2 = \pm M \frac{di_1}{dt} \pm L_2 \frac{di_2}{dt} \end{array} \right.$$

Apply superposition

with $i_2 = \phi$

$$\left\{ \begin{array}{l} v_1 = L_1 \frac{di_1}{dt} \\ v_2 = M \frac{di_1}{dt} \end{array} \right.$$

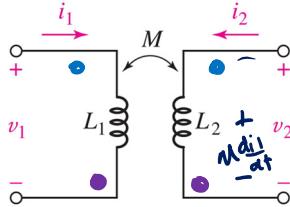
with $i_1 = \phi$

$$\left\{ \begin{array}{l} v_1 = M \frac{di_2}{dt} \\ v_2 = L_2 \frac{di_2}{dt} \end{array} \right.$$

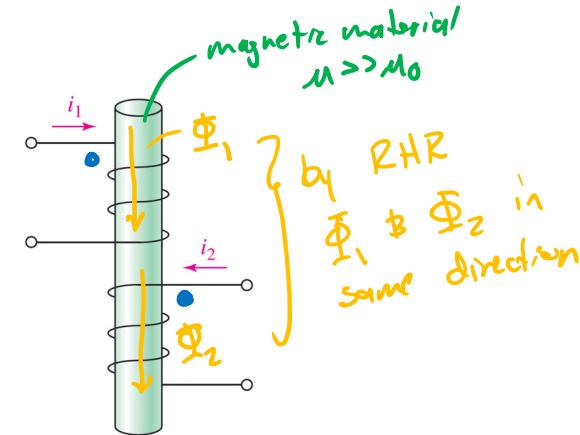
Symbols and Dot Convention

* Make sure to use passive sign convention

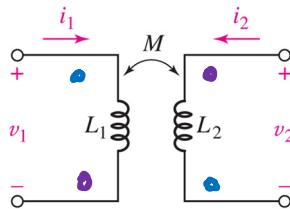
$$\left\{ \begin{array}{l} V_1 = L_1 \frac{di_1}{dt} + M \frac{di_2}{dt} \\ V_2 = M \frac{di_1}{dt} + L_2 \frac{di_2}{dt} \end{array} \right.$$



Physical: If both i_1 & i_2 enter the dotted terminals of L_1 & L_2 , they produce additive flux



$$\left\{ \begin{array}{l} V_1 = L_1 \frac{di_1}{dt} - M \frac{di_2}{dt} \\ V_2 = -M \frac{di_1}{dt} + L_2 \frac{di_2}{dt} \end{array} \right.$$



Circuit: current flowing into the dotted terminal will produce a positive voltage relative to the dotted terminal of the other winding (in open circuit)

