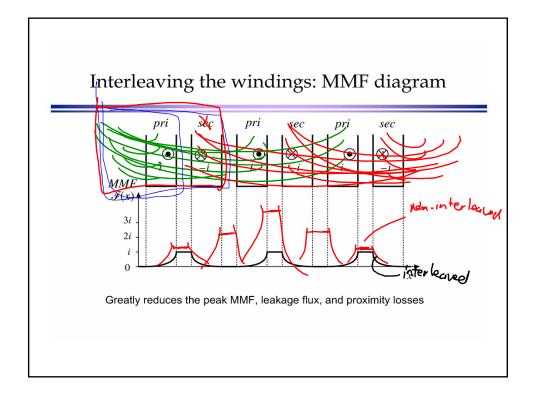
Lecture 24: Inductor Design Procedure

ECE 481: Power Electronics

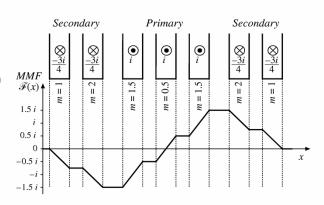
Prof. Daniel Costinett

Department of Electrical Engineering and Computer Science
University of Tennessee Knoxville
Fall 2013



A partially-interleaved transformer

For this example, there are three primary layers and four secondary layers. The MMF diagram contains fractional values.



Discussion: design of winding geometry to minimize proximity loss

- Interleaving windings can significantly reduce the proximity loss when the winding currents are in phase, such as in the transformers of buckderived converters or other converters
- In some converters (such as flyback or SEPIC) the winding currents are
 <u>out of phase</u>, Interleaving then does little to reduce the peak MMF and
 proximity loss. See Vandelac and Ziogas [10].
- For sinusoidal winding currents, there is an optimal conductor thickness near $\phi=1$ that minimizes copper loss.
- Minimize the number of layers. Use a core geometry that maximizes the width ℓ_w of windings.

 Minimize the amount of copper in vicinity of high MMF portions of the windings

(48 diamera)





Litz wire

- A way to increase conductor area while maintaining low proximity losses
- Many strands of small-gauge wire are bundled together and are externally connected in parallel
- Strands are twisted, or transposed, so that each strand passes equally through each position on inside and outside of bundle. This prevents circulation of currents between strands.
- Strand diameter should be sufficiently smaller than skin depth
- The Litz wire bundle itself is composed of multiple layers
- Advantage: when properly sized, can significantly reduce proximity loss
- Disadvantage: increased cost and decreased amount of copper within core window

13.5. Several types of magnetic devices, their *B–H* loops, and core vs. copper loss

A key design decision: the choice of maximum operating flux density ${\cal B}_{\it max}$

- Choose B_{max} to avoid saturation of core, or
- Further reduce B_{\max} , to reduce core losses

Different design procedures are employed in the two cases.

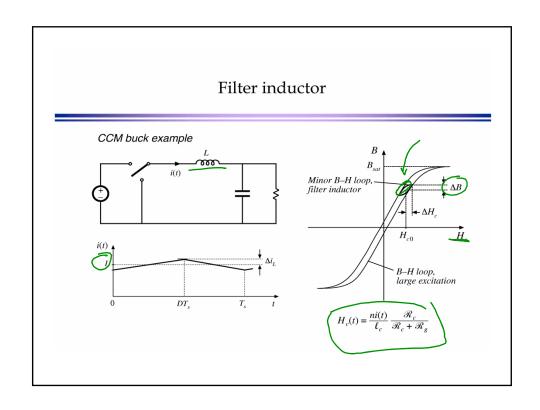
Types of magnetic devices:

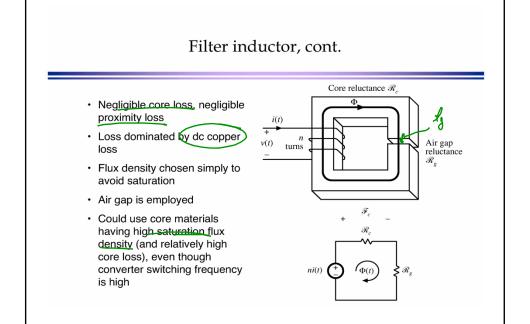
Filter inductor AC inductor

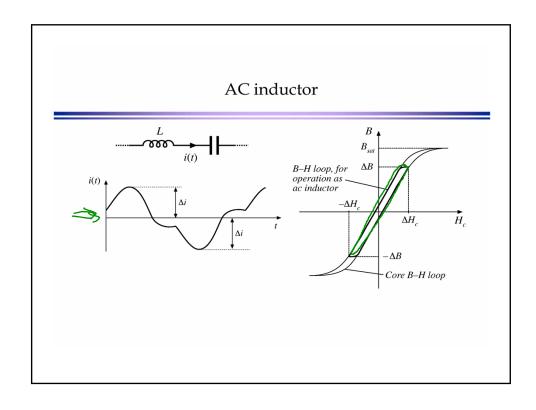
Conventional transformer Coupled inductor V

Flyback transformer SEPIC transformer

Magnetic amplifier Saturable reactor

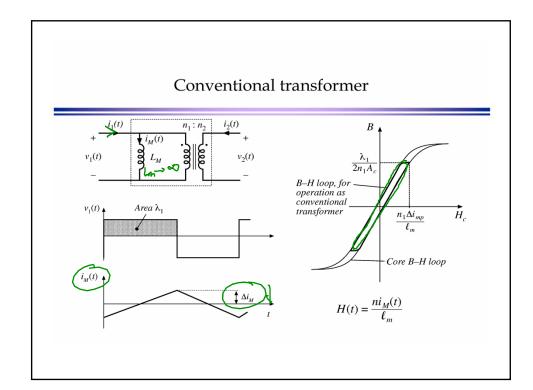






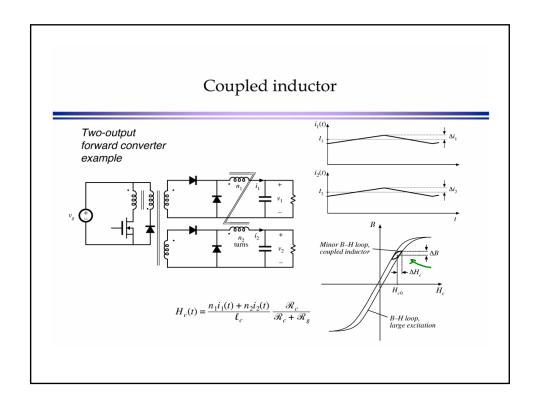
AC inductor, cont.

- · Core loss, copper loss, proximity loss are all significant
- An air gap is employed
- Flux density is chosen to reduce core loss
- A high-frequency material (ferrite) must be employed



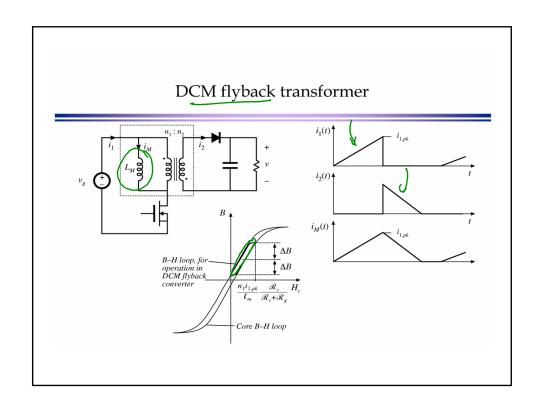
Conventional transformer, cont.

- Core loss, copper loss, and proximity loss are usually significant
- No air gap is employed
- · Flux density is chosen to reduce core loss
- A high frequency material (ferrite) must be employed



Coupled inductor, cont.

- · A filter inductor having multiple windings
- · Air gap is employed
- · Core loss and proximity loss usually not significant
- · Flux density chosen to avoid saturation
- · Low-frequency core material can be employed



DCM flyback transformer, cont.



- Core loss, copper loss, proximity loss are significant
- Flux density is chosen to reduce core loss
- Air gap is employed
- A high-frequency core material (ferrite) must be used

Chapter 14 Inductor Design

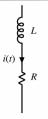
- 14.1 Filter inductor design constraints
- 14.2 A step-by-step design procedure
- 14.3 Multiple-winding magnetics design using the K_g method
- 14.4 Examples
- 14.5 Summary of key points

Fundamentals of Power Electronics

1

Chapter 14: Inductor design

14.1 Filter inductor design constraints



Objective:

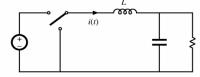
Design inductor having a given inductance L,

which carries worst-case current $I_{\it max}$ without saturating,

and which has a given <u>winding resistance</u> *R*, or, equivalently, exhibits a worst-case copper loss of

$$P_{cu} = I_{rms}^{2} R$$

Example: filter inductor in CCM buck converter



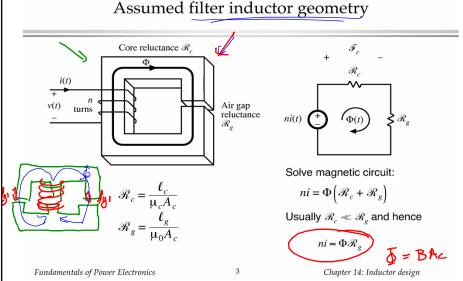
 $\begin{matrix} i(t) \\ I \\ 0 \end{matrix} \qquad \begin{matrix} DT_s \\ T_s \end{matrix} \qquad \begin{matrix} \Delta i_L \\ T_s \end{matrix}$

Fundamentals of Power Electronics

2

Chapter 14: Inductor design

Assumed filter inductor geometry



Constraint: maximum flux density 14.1.1

Given a peak winding current I_{max} , it is desired to operate the core flux density at a peak value B_{max} . The value of B_{max} is chosen to be less than the worst-case saturation flux density B_{sat} of the core material.

From solution of magnetic circuit:

Let
$$I = I_{max}$$
 and $B = B_{max}$:
$$nI_{max} = B_{max} A_c \mathcal{R}_g = B_{max} \frac{\ell_g}{\mu_0}$$

This is constraint #1. The turns ratio n and air gap length t_g are unknown.

Fundamentals of Power Electronics

14.1.2 Constraint: inductance

Must obtain specified inductance L. We know that the inductance is

$$L = \frac{n^2}{\mathcal{R}_g} = \frac{\mu_0 A_c n^2}{\ell_g}$$

This is constraint #2. The turns ratio n, gore area A_c , and air gap length ℓ_g are unknown.

Fundamentals of Power Electronics

Chapter 14: Inductor design

14.1.3 Constraint: winding area

Wire must fit through core window (i.e., hole in center of core)

wire bare area

Total area of copper in window:

 nA_w

 K_uW_A

Area available for winding conductors:

Third design constraint: $K_u W_A \ge n A_{\underline{w}}$

Fundamentals of Power Electronics

OLKu C1

 $\begin{array}{c} \text{core window} \\ \text{area} \ W_A \end{array}$

core

The window utilization factor K_u

also called the "fill factor"

 K_u is the fraction of the core window area that is filled by copper Mechanisms that cause K_u to be less than 1:

- Round wire does not pack perfectly, which reduces K_u by a factor of 0.7 to 0.55 depending on winding technique
- Insulation reduces K_u by a factor of 0.95 to 0.65, depending on wire size and type of insulation
- · Bobbin uses some window area
- · Additional insulation may be required between windings

Typical values of K_{ij} :

0.5 for simple low-voltage inductor

0.25 to 0.3 for off-line transformer

0.05 to 0.2 for high-voltage transformer (multiple kV)

0.65 for low-voltage foil-winding inductor

Fundamentals of Power Electronics

7

Chapter 14: Inductor design

14.1.4 Winding resistance

The resistance of the winding is

$$\underline{R} = \rho \, \frac{\ell_b}{A_W}$$

where ρ is the resistivity of the conductor material, ℓ_b is the length of the wire, and A_W is the wire bare area. The resistivity of copper at room temperature is $1.724\cdot 10^{-6}\,\Omega\text{-cm}$. The length of the wire comprising an n-turn winding cap be expressed as

$$\ell_b = n(MLT)$$

where (MLT) is the mean-length-per-turn of the winding. The mean-length-per-turn is a function of the core geometry. The above equations can be combined to obtain the fourth constraint:

$$R = \rho$$
 A_W
Select

 $Fundamentals\ of\ Power\ Electronics$

14.1.5 The core geometrical constant K_g

The four constraints:

The four constraints.

(1)
$$nI_{max} = B_{max} A_c \mathcal{R}_g = B_{max} \frac{\ell_g}{\mu_0}$$
 $L = \frac{n^2}{\mathcal{R}_g} = \frac{\mu_0 A_c n^2}{\ell_g}$ (3)

(2) $K_u W_A \ge nA_W$ $R = \rho \frac{n (MLT)}{A_W}$ (4)

$$L = \frac{n^2}{\mathcal{R}_o} = \frac{\mu_0 A_c n^2}{\ell_o} \quad (3)$$

$$(1) \quad K_u W_A \ge nA_v$$

$$R = \rho \, \frac{n \, (MLT)}{A_W} \quad \text{(4)}$$

These equations involve the quantities

 A_c , W_A , and MLT, which are functions of the core geometry,

 I_{max} , B_{max} , μ_{Ω} , L, K_{u} , R, and Ω , which are given specifications or other known quantities, and

 n, ℓ_{∞} and A_{w} which are unknowns.

Eliminate the three unknowns, leading to a single equation involving the remaining quantities.

Fundamentals of Power Electronics

Chapter 14: Inductor design

Core geometrical constant K_g

Elimination of n, ℓ_{o} , and A_{W} leads to

$$\frac{A_c^2 W_A}{(MLT)} \ge \frac{\rho L^2 I_{max}^2}{B_{max}^2 R K_u}$$

- · Right-hand side: specifications or other known quantities
- · Left-hand side: function of only core geometry

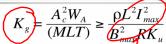
So we must choose a core whose geometry satisfies the above equation.

The core geometrical constant K_{g} is defined as

$$K_g = \frac{A_c^2 W_A}{(MLT)}$$

Fundamentals of Power Electronics

Discussion



 K_a is a figure-of-merit that describes the effective electrical size of magnetic cores, in applications where the following quantities are specified:

→ Copper loss

Maximum flux density

How specifications affect the core size:

A smaller core can be used by increasing

 B_{max} \Rightarrow use core material having higher B_{sat} \Rightarrow $B = A_{c}$ A_{c} A_{c

How the core geometry affects electrical capabilities:

A larger K_g can be obtained by increase of

 $A_c \Rightarrow$ more iron core material, or

 $W_A \Rightarrow$ larger window and more copper

Fundamentals of Power Electronics

Chapter 14: Inductor design

14.2 A step-by-step procedure

The following quantities are specified, using the units noted:

Wire resistivity Peak winding current

Inductance

Winding resistance Winding fill factor

Core maximum flux density

The core dimensions are expressed in cm:

Core cross-sectional area (cm²) Core window area W. (cm²) Mean length per turn MLT

The use of centimeters rather than meters requires that appropriate factors be added to the design equations.

Determine core size



Choose a core which is large enough to satisfy this inequality (see Appendix D for magnetics design tables).

Note the values of A_c , W_A , and MLT for this core.

Fundamentals of Power Electronics

13

Chapter 14: Inductor design

Determine air gap length

$$\ell_g = \frac{\mu_0 L I_{max}^2}{B_{max}^2 A_c} 10^4$$
 (m)

with A_c expressed in cm². μ_0 = $4\pi 10^{-7}\,H/m.$

The air gap length is given in meters.

The value expressed above is approximate, and neglects fringing flux and other nonidealities.

 $Fundamentals\ of\ Power\ Electronics$

14



Core manufacturers sell gapped cores. Rather than specifying the air gap length, the equivalent quantity A_L is used.

 A_L is equal to the inductance, in $\rm mH,$ obtained with a winding of 1000 turns.

When ${\cal A}_L$ is specified, it is the core manufacturer's responsibility to obtain the correct gap length.

The required A_L is given by:

$$A_L = \frac{10B_{max}^2 A_c^2}{LI_{max}^2} \qquad \text{(mH/1000 turns)} \qquad \begin{array}{c} \textit{Units:} \\ A_c & \text{cm}^2, \\ L & \text{Henries,} \\ B_{max} & \text{Tesla.} \end{array}$$

$$L = A_L n^2 10^{-9} \quad \text{(Henries)}$$

Fundamentals of Power Electronics

15

Chapter 14: Inductor design

Determine number of turns *n*

$$n = \frac{LI_{max}}{B_{max}A_c} \quad 10^4$$

Evaluate wire size

$$A_W \le \frac{K_u W_A}{n} \quad \text{(cm}^2)$$

Select wire with bare copper area ${\cal A}_W$ less than or equal to this value. An American Wire Gauge table is included in Appendix D.

As a check, the winding resistance can be computed:

$$R = \frac{\rho n \, (MLT)}{A_w} \quad (\Omega$$

Fundamentals of Power Electronics

17

Chapter 14: Inductor design

14.3 Multiple-winding magnetics design using the K_g method

The K_g design method can be extended to multiplewinding magnetic elements such as transformers and coupled inductors.

A

This method is applicable when

- Copper loss dominates the total loss (i.e. core loss is ignored), or
- The maximum flux density B_{max} is a specification rather than a quantity to be optimized

To do this, we must

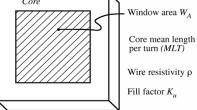
- Find how to allocate the window area between the windings
- Generalize the step-by-step design procedure

 $Fundamentals\ of\ Power\ Electronics$

14.3.1 Window area allocation

 $\begin{tabular}{ll} \textbf{Given:} & application with k windings having known rms currents and desired turns ratios \end{tabular}$

$$\frac{v_1(t)}{n_1} = \frac{v_2(t)}{n_2} = \dots = \frac{v_k(t)}{n_k}$$



Fundamentals of Power Electronics

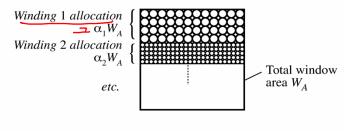
 ${\it Q:}$ how should the window area $W_{\!\scriptscriptstyle A}$ be allocated among the windings?

Chapter 14: Inductor design

rms current

Allocation of winding area

19



 $0 < \alpha_j < 1$ $\alpha_1 + \alpha_2 + \dots + \alpha_k = 1$

 $Fundamentals\ of\ Power\ Electronics$

20

Copper loss in winding j

Copper loss (not accounting for proximity loss) is

$$P_{cu,j} = I_j^2 R_j$$

Resistance of winding j is

$$R_j = \rho \frac{\ell_j}{A_{W,j}}$$

$$\ell_j = n_j \, (MLT)$$

length of wire, winding j

$$A_{W,j} = \frac{W_A K_u \alpha_j}{n_j}$$
 wire area, winding j

$$R_j = \rho \, \frac{n_j^2 (MLT)}{W_A K_u \alpha_j}$$

$$P_{cu,j} = \rho \frac{n_j^2(MLT)}{W_A K_u \alpha_j} \qquad P_{cu,j} = \frac{n_j^2 i_j^2 \rho(MLT)}{W_A K_u \alpha_j}$$

Fundamentals of Power Electronics

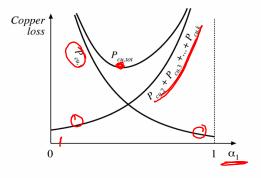
Chapter 14: Inductor design

Total copper loss of transformer

Sum previous expression over all windings:
$$P_{cu,tot} = P_{cu,1} + P_{cu,2} + \dots + P_{cu,k} = \frac{\rho (MLT)}{W_A K_u} \sum_{j=1}^k \left(\frac{n_j^2 I_j^2}{\alpha_j} \right)$$

Need to select values for $\alpha_1, \alpha_2, ..., \alpha_k$ such that the total copper loss

Variation of copper losses with α_1



For α_1 = 0: wire of winding 1 has zero area. $P_{cu,1}$ tends to infinity

For α_1 = 1: wires of remaining windings have zero area. Their copper losses tend to infinity

There is a choice of $\alpha_{\rm l}$ that minimizes the total copper loss

Fundamentals of Power Electronics

23

Chapter 14: Inductor design

Method of Lagrange multipliers

to minimize total copper loss

Minimize the function

$$P_{cu,tot} = P_{cu,1} + P_{cu,2} + \dots + P_{cu,k} = \frac{\rho (MLT)}{W_A K_u} \sum_{j=1}^{k} \binom{n_j^2 I_j^2}{\alpha_j}$$

subject to the constraint

$$\alpha_1 + \alpha_2 + \cdots + \alpha_k = 1$$

Define the function

$$f(\alpha_1,\alpha_2,\cdots,\alpha_k,\xi) = P_{cu,tot}(\alpha_1,\alpha_2,\cdots,\alpha_k) + \xi \, g(\alpha_1,\alpha_2,\cdots,\alpha_k)$$

where

$$g(\alpha_1, \alpha_2, \dots, \alpha_k) = 1 - \sum_{j=1}^k \alpha_j$$

is the constraint that must equal zero and $\boldsymbol{\xi}$ is the Lagrange multiplier

Fundamentals of Power Electronics

24

Lagrange multipliers

continued

Optimum point is solution of the system of equations

$$\begin{split} \frac{\partial f(\alpha_1,\alpha_2,\cdots,\alpha_k,\xi)}{\partial \alpha_1} &= 0 \\ \frac{\partial f(\alpha_1,\alpha_2,\cdots,\alpha_k,\xi)}{\partial \alpha_2} &= 0 \\ &\vdots \\ \frac{\partial f(\alpha_1,\alpha_2,\cdots,\alpha_k,\xi)}{\partial \alpha_k} &= 0 \\ \frac{\partial f(\alpha_1,\alpha_2,\cdots,\alpha_k,\xi)}{\partial \xi} &= 0 \end{split}$$

Hesuit:

$$\xi = \frac{\rho (MLT)}{W_A K_u} \left(\sum_{j=1}^k n_j I_j \right)^2 = P_{cu,tot}$$

$$\alpha_m = \frac{n_m I_m}{\sum_{n=1}^{\infty} n_j I_j}$$

An alternate form:

$$\alpha_m = \frac{V_m I_m}{\sum_{m=1}^{\infty} V_j I_j}$$

Fundamentals of Power Electronics

25

Chapter 14: Inductor design

Interpretation of result

$$\alpha_m = \frac{V_m I_m}{\sum_{m=1}^{\infty} V_j I_j}$$

Apparent power in winding j is

$$V_jI_j$$

where

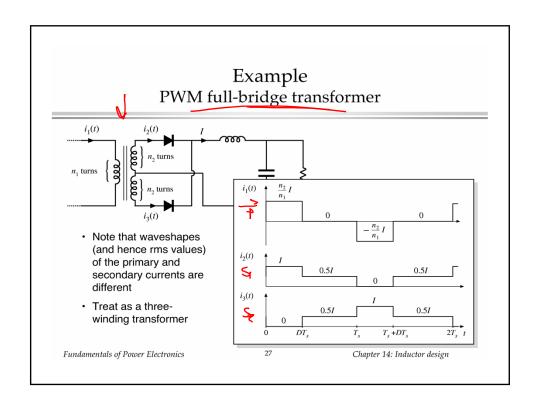
 V_j is the rms or peak applied voltage

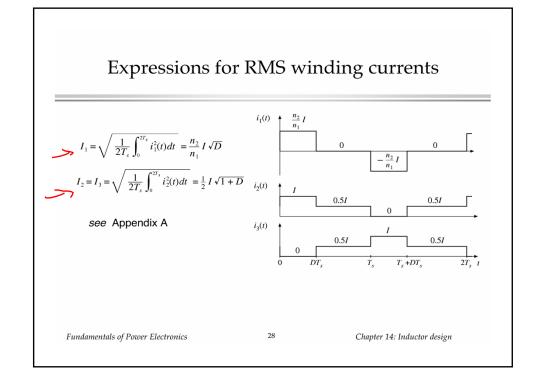
 I_i is the rms current

Window area should be allocated according to the apparent powers of the windings

Fundamentals of Power Electronics

20





Allocation of window area:

$$\alpha_m = \frac{V_m I_m}{\sum_{n=1}^{\infty} V_j I_j}$$

Plug in rms current expressions. Result:

$$\alpha_1 = \frac{1}{\left(1 + \sqrt{\frac{1+D}{D}}\right)}$$

Fraction of window area allocated to primary winding

$$\alpha_2 = \alpha_3 = \frac{1}{2} \frac{1}{\left(1 + \sqrt{\frac{D}{1+D}}\right)}$$

Fraction of window area allocated to each secondary winding

Fundamentals of Power Electronics

29

Chapter 14: Inductor design

Numerical example

Suppose that we decide to optimize the transformer design at the worst-case operating point D=0.75. Then we obtain

$$\alpha_1 = 0.396$$

$$\alpha_2 = 0.302$$

$$\alpha_3 = 0.302$$

The total copper loss is then given by

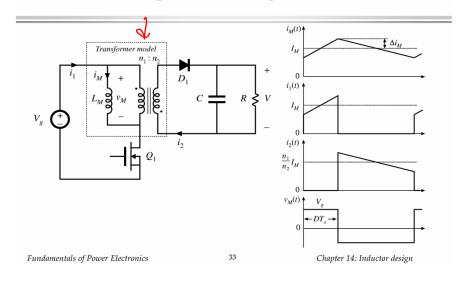
$$P_{cu,tot} = \frac{\rho(MLT)}{W_A K_u} \left(\sum_{j=1}^{3} n_j I_j \right)^2$$

$$= \frac{\rho(MLT) n_2^2 I^2}{W_A K_u} \left(1 + 2D + 2\sqrt{D(1+D)} \right)$$

Fundamentals of Power Electronics

30





Specifications

Input voltage $V_g = 200 \text{ V}$ Output (full load) 20 V at 5 A

Switching frequency 150 kHz

Magnetizing current ripple 20% of dc magnetizing current

Chapter 14: Inductor design

Duty cycle D = 0.4

Turns ratio $n_2/n_1 = 0.15$

Maximum flux density $B_{max} = 0.25 \text{ T}$

Fundamentals of Power Electronics 34

Basic converter calculations

Components of magnetizing current, referred to primary:

$$I_M = \left(\frac{n_2}{n_1}\right) \frac{1}{D'} \frac{V}{R} = 1.25 \text{ A}$$

$$\Delta i_M = \left(20\%\right)I_M = 0.25~\mathrm{A}$$

$$I_{M,max} = I_M + \Delta i_M = 1.5 \text{ A}$$

Choose magnetizing inductance:

$$L_M = \frac{V_g DT_s}{2\Delta i_M}$$
$$= 1.07 \text{ mH}$$

RMS winding currents:

$$I_1 = I_M \sqrt{D} \sqrt{1 + \frac{1}{3} \left(\frac{\Delta i_M}{I_M}\right)^2} = 0.796 \text{ A}$$

$$I_2 = \frac{n_1}{n_2} I_M \sqrt{D'} \sqrt{1 + \frac{1}{3} \left(\frac{\Delta i_M}{I_M}\right)^2} = 6.50 \text{ A}$$

$$I_{tot} = I_1 + \frac{n_2}{n_1} I_2 = 1.77 \text{ A}$$

Fundamentals of Power Electronics

3

Chapter 14: Inductor design

Choose core size

$$K_g \ge \frac{\rho L_M^2 I_{tot}^2 I_{M,max}^2}{B_{max}^2 P_{cu} K_u} 10^8$$

$$= \frac{\left(1.724 \cdot 10^{-6} \Omega - \text{cm}\right) \left(1.07 \cdot 10^{-3} \text{ H}\right)^2 \left(1.77 \text{ A}\right)^2 \left(1.5 \text{ A}\right)^2}{\left(0.25 \text{ T}\right)^2 \left(1.5 \text{ W}\right) \left(0.3\right)} 10^8$$

$$= 0.049 \text{ cm}^5$$

The smallest EE core that satisfies this inequality (Appendix D) is the EE30.



Fundamentals of Power Electronics

57

Chapter 14: Inductor design

Choose air gap and turns

$$\frac{\ell_{\nu}}{B_{max}^{2}A_{c}} \frac{\mu_{0}L_{M}I_{M,max}^{2}}{B_{max}^{2}A_{c}} 10^{4}$$

$$= \frac{\left(4\pi \cdot 10^{-7}\text{H/m}\right)\left(1.07 \cdot 10^{-3}\text{ H}\right)\left(1.5\text{ A}\right)^{2}}{\left(0.25\text{ T}\right)^{2}\left(1.09\text{ cm}^{2}\right)} 10^{4}$$

$$= 0.44 \text{ mm}$$

$$n_{1} = \frac{L_{M}I_{M,max}}{B_{max}A_{c}} 10^{4} \qquad \qquad n_{2} = \left(\frac{n_{2}}{n_{1}}\right)n_{1}$$

$$= \frac{\left(1.07 \cdot 10^{-3} \text{ H}\right)\left(1.5 \text{ A}\right)}{\left(0.25 \text{ T}\right)\left(1.09 \text{ cm}^{2}\right)} 10^{4} \qquad \qquad = \left(0.15\right) 59$$

$$= 58.7 \text{ turns}$$
Round to $n_{1} = 59$

$$n_{2} = 9$$

Fundamentals of Power Electronics

37

Chapter 14: Inductor design

Wire gauges

$$\alpha_{1} = \frac{I_{1}}{I_{tot}} = \frac{(0.796 \text{ A})}{(1.77 \text{ A})} = 0.45$$

$$\alpha_{2} = \frac{n_{2}I_{2}}{n_{1}I_{tot}} = \frac{(9)(6.5 \text{ A})}{(59)(1.77 \text{ A})} = 0.55$$

$$A_{W1} \le \frac{\alpha_{1}K_{u}W_{A}}{n_{1}} = \frac{1.09 \cdot 10^{-3} \text{ cm}^{2}}{n_{2}} \qquad \text{use #28 AWG}$$

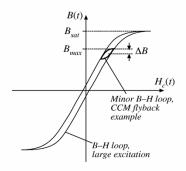
$$A_{W2} \le \frac{\alpha_{2}K_{u}W_{A}}{n_{2}} = 8.88 \cdot 10^{-3} \text{ cm}^{2} \qquad \text{use #19 AWG}$$

 $Fundamentals\ of\ Power\ Electronics$

38

Core loss CCM flyback example

B-H loop for this application:

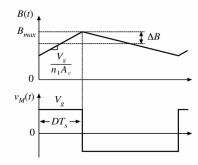


B(t) vs. applied voltage, from Faraday's law:

$$\frac{dB(t)}{dt} = \frac{v_M(t)}{n_1 A_c}$$

Fundamentals of Power Electronics

The relevant waveforms:



For the first subinterval:

$$\frac{dB(t)}{dt} = \frac{V_g}{n_1 A_g}$$

Chapter 14: Inductor design

Calculation of ac flux density to the checked and core loss

Solve for ΔB :

we for
$$\Delta B$$
:
$$\Delta B = \left(\frac{V_g}{n_1 A_c}\right) (DT_s) = \sqrt{\frac{1}{n_1 A_c}}$$

Plug in values for flyback example:

$$\Delta B = \frac{(200 \text{ V})(0.4)(6.67 \text{ }\mu\text{s})}{2(59)(1.09 \text{ cm}^2)} 10^4$$
$$= 0.041 \text{ T}$$

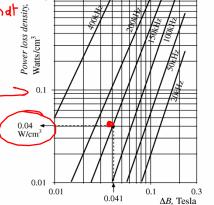
From manufacturer's plot of core loss (at left), the power loss density is $0.04~\rm W/cm^3$. Hence core loss is

$$P_{fe} = (0.04 \text{ W/cm}^3)(A_e \ell_m)$$

$$= (0.04 \text{ W/cm}^3)(1.09 \text{ cm}^2)(5.77 \text{ cm})$$

$$= 0.25 \text{ W}$$

Fundamentals of Power Electronics



Comparison of core and copper loss

- Copper loss is 1.5 W
 - does not include proximity losses, which could substantially increase total copper loss
- · Core loss is 0.25 W
 - Core loss is small because ripple and ΔB are small
 - It is not a bad approximation to ignore core losses for ferrite in CCM filter inductors
 - Could consider use of a less expensive core material having higher core loss
 - Neglecting core loss is a reasonable approximation for this application
- Design is dominated by copper loss
 - The dominant constraint on flux density is saturation of the core, rather than core loss

Fundamentals of Power Electronics

41

Chapter 14: Inductor design

14.5 Summary of key points

- A variety of magnetic devices are commonly used in switching converters. These devices differ in their core flux density variations, as well as in the magnitudes of the ac winding currents. When the flux density variations are small, core loss can be neglected. Alternatively, a low-frequency material can be used, having higher saturation flux density.
- 2. The core geometrical constant K_g is a measure of the magnetic size of a core, for applications in which copper loss is dominant. In the K_g design method, flux density and total copper loss are specified.

 $Fundamentals\ of\ Power\ Electronics$