Lecture 2: Volt-Second and Capacitor Charge Balance

ECE 481: Power Electronics

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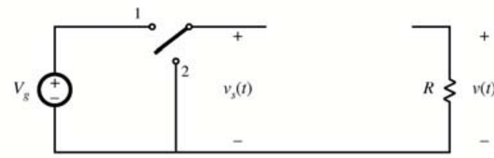
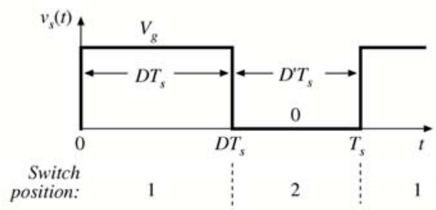
Chapter 2
Principles of Steady-State Converter Analysis

2.1 Introduction
Buck converter

SPDT switch changes dc component



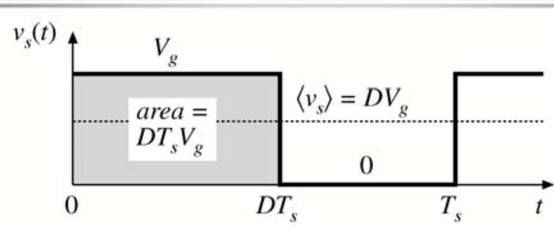
Switch output voltage waveform



Duty cycle D:
$0 \le D \le 1$

complement D':
$D' = 1 - D$

Dc component of switch output voltage

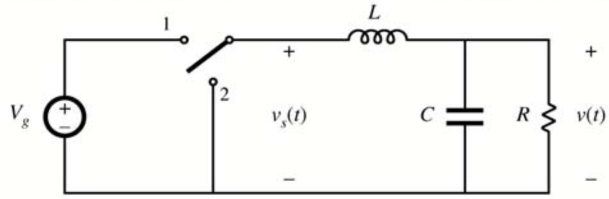


Fourier analysis: Dc component = average value

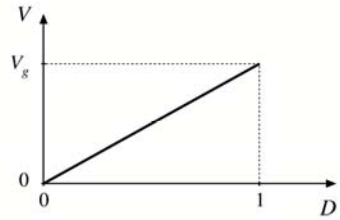
$$\langle v_s \rangle = \frac{1}{T_s}\int_0^{T_s} v_s(t)\, dt$$

$$\langle v_s \rangle = \frac{1}{T_s}(DT_sV_g) = DV_g$$

Insertion of low-pass filter to remove switching harmonics and pass only dc component

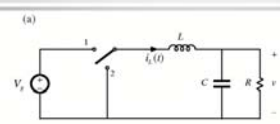


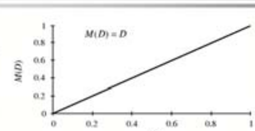
$$v \approx \langle v_s \rangle = DV_g$$



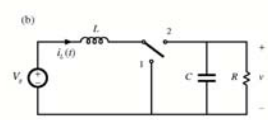
Three basic dc-dc converters

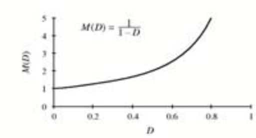
Buck



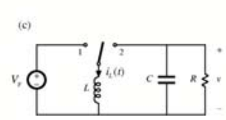


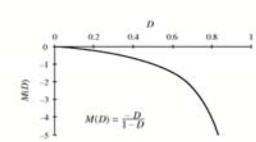
Boost





Buck-boost





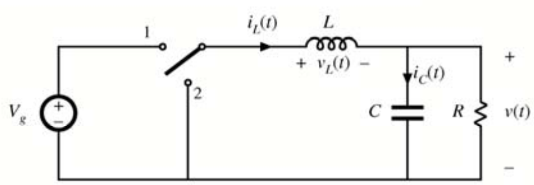
Objectives of this chapter

- Develop techniques for easily determining output voltage of an arbitrary converter circuit
- Derive the principles of *inductor volt-second balance* and *capacitor charge (amp-second) balance*
- Introduce the key *small ripple approximation*
- Develop simple methods for selecting filter element values
- Illustrate via examples

2.2. Inductor volt-second balance, capacitor charge balance, and the small ripple approximation

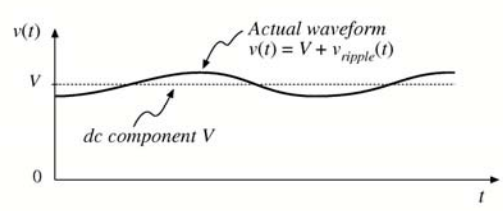
Actual output voltage waveform, buck converter

Buck converter containing practical low-pass filter



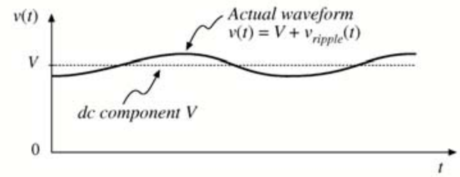
Actual output voltage waveform

$v(t) = V + v_{ripple}(t)$



The small ripple approximation

$$v(t) = V + v_{ripple}(t)$$

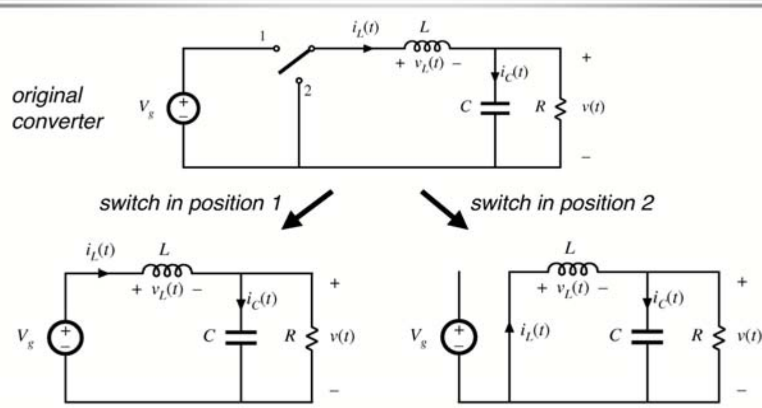


In a well-designed converter, the output voltage ripple is small. Hence, the waveforms can be easily determined by ignoring the ripple:

$$\left\| v_{ripple} \right\| \ll V$$

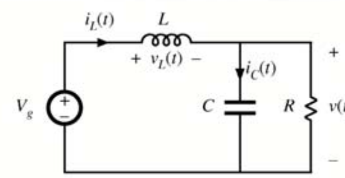
$$v(t) \approx V$$

Buck converter analysis: inductor current waveform



Inductor voltage and current
Subinterval 1: switch in position 1

Inductor voltage

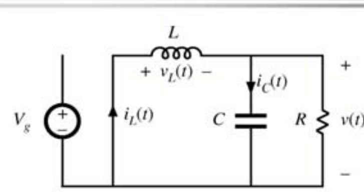


Small ripple approximation:

Solve for the slope:

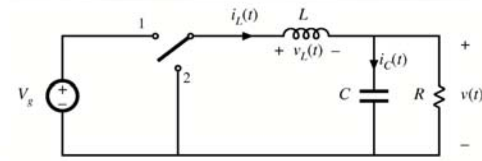
Inductor voltage and current
Subinterval 2: switch in position 2

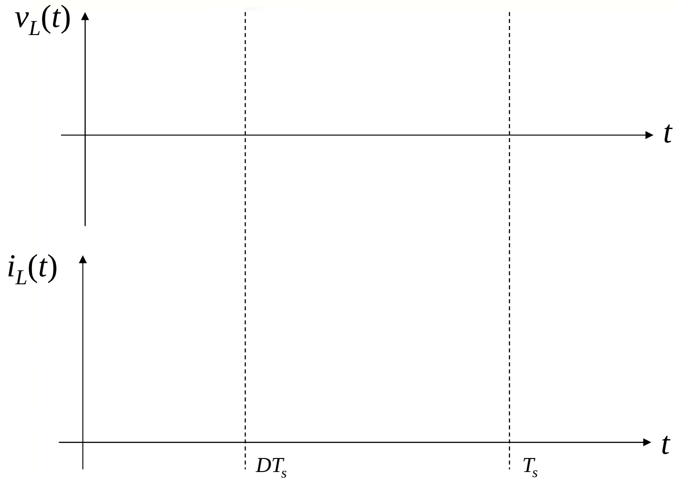
Inductor voltage



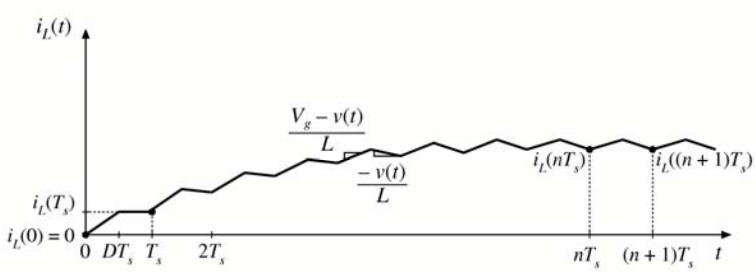
Small ripple approximation:

Inductor Voltage and Current Waveforms





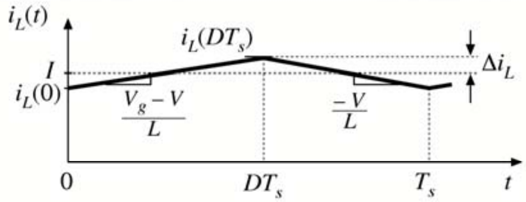
Inductor current waveform during turn-on transient



When the converter operates in equilibrium:

$$i_L((n+1)T_s) = i_L(nT_s)$$

Determination of inductor current ripple magnitude



$$(\text{change in } i_L) = (\text{slope})(\text{length of subinterval})$$

$$\left(2\Delta i_L\right) = \left(\frac{V_g - V}{L}\right)\left(DT_s\right)$$

$$\Rightarrow \quad \Delta i_L = \frac{V_g - V}{2L} DT_s \qquad L = \frac{V_g - V}{2\Delta i_L} DT_s$$

The principle of inductor volt-second balance: Derivation

Inductor defining relation:

$$v_L(t) = L \frac{di_L(t)}{dt}$$

Integrate over one complete switching period:

$$i_L(T_s) - i_L(0) = \frac{1}{L}\int_0^{T_s} v_L(t)\, dt$$

In periodic steady state, the net change in inductor current is zero:

$$0 = \int_0^{T_s} v_L(t)\, dt$$

Hence, the total area (or volt-seconds) under the inductor voltage waveform is zero whenever the converter operates in steady state.

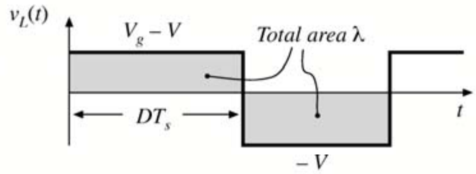
An equivalent form:

$$0 = \frac{1}{T_s}\int_0^{T_s} v_L(t)\, dt = \left\langle v_L \right\rangle$$

The average inductor voltage is zero in steady state.

Inductor volt-second balance: Buck converter example

Inductor voltage waveform, previously derived:



Integral of voltage waveform is area of rectangles:

$$\lambda = \int_0^{T_s} v_L(t)\, dt = (V_g - V)(DT_s) + (-V)(D'T_s)$$

Average voltage is

$$\langle v_L \rangle = \frac{\lambda}{T_s} = D(V_g - V) + D'(-V)$$

Equate to zero and solve for *V:*

$$0 = DV_g - (D + D')V = DV_g - V \qquad \Rightarrow \qquad V = DV_g$$

The principle of capacitor charge balance: Derivation

Capacitor defining relation:

$$i_C(t) = C\,\frac{dv_C(t)}{dt}$$

Integrate over one complete switching period:

$$v_C(T_s) - v_C(0) = \frac{1}{C}\int_0^{T_s} i_C(t)\, dt$$

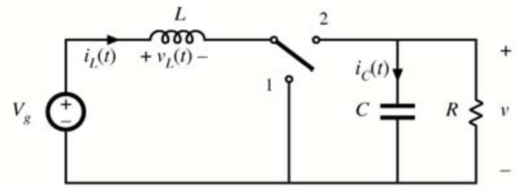
In periodic steady state, the net change in capacitor voltage is zero:

$$0 = \frac{1}{T_s}\int_0^{T_s} i_C(t)\, dt = \langle i_C \rangle$$

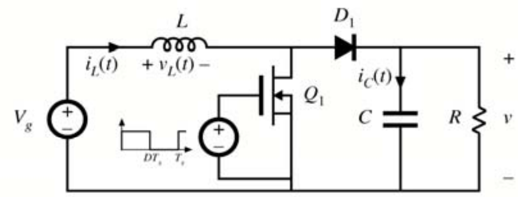
Hence, the total area (or charge) under the capacitor current waveform is zero whenever the converter operates in steady state. The average capacitor current is then zero.

2.3 Boost converter example

Boost converter with ideal switch

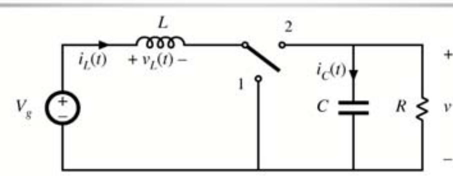


Realization using power MOSFET and diode

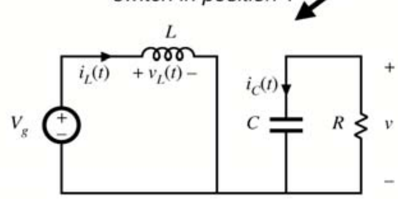


Boost converter analysis

original converter



switch in position 1



switch in position 2