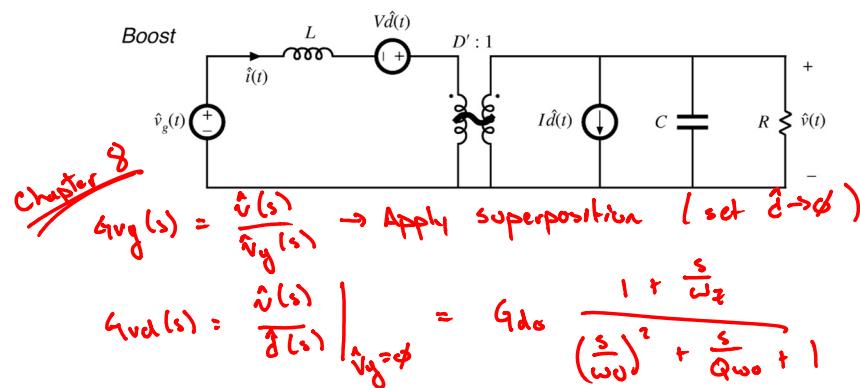


Announcements

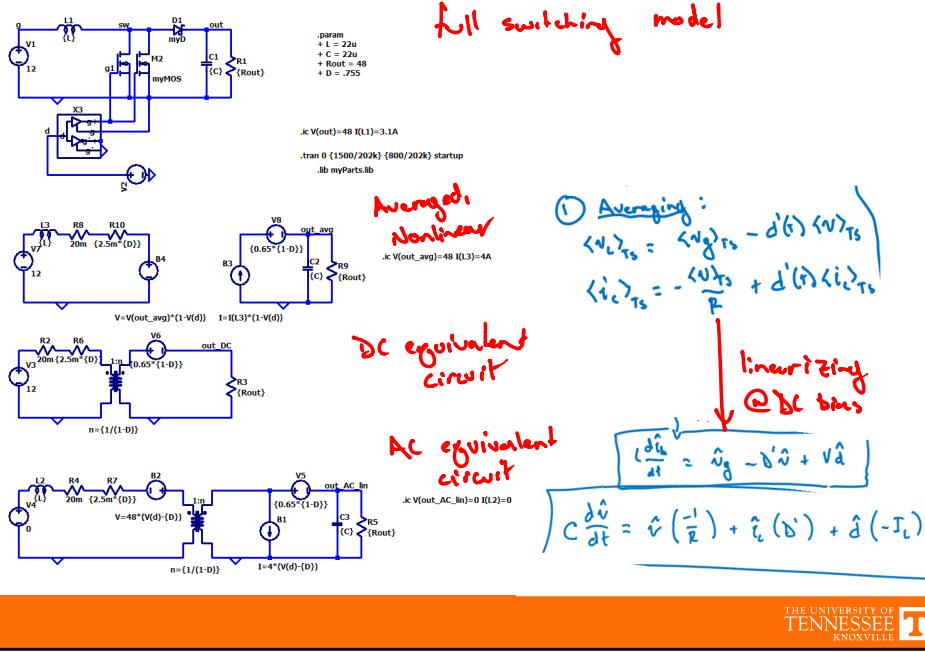
- Midterm due now
- Solutions to be posted on website
- Next Homework Due Friday, October 23rd
- Remote Lecture on Wednesday
- Remote Lectures W/F Oct 21 & 23



Boost Converter Averaged, AC, Linear Circuit Model

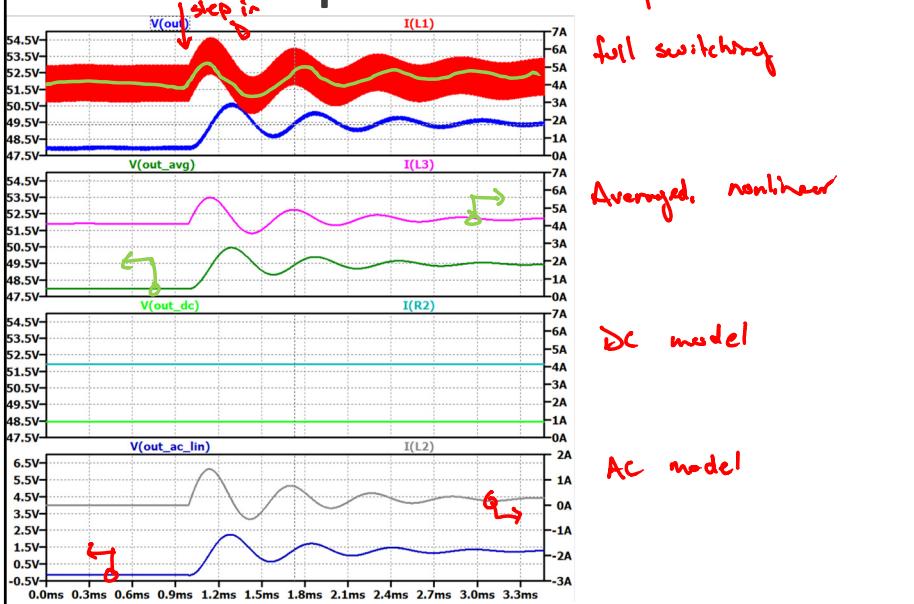


Model Simulation



THE UNIVERSITY OF
TENNESSEE
KNOXVILLE

Model Comparison



THE UNIVERSITY OF
TENNESSEE
KNOXVILLE

Buck Boost AC Modeling

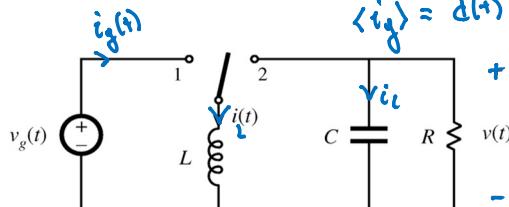
Averaged Equations

$$L \frac{d\langle i_L \rangle}{dt} = d(t) \langle v_g \rangle + d'(t) \langle v \rangle$$

$$C \frac{d\langle v \rangle}{dt} = -\frac{\langle v \rangle}{R} - d'(t) \langle i_L \rangle$$

$$\langle i_g \rangle = d(t) \langle i_L \rangle$$

Buck-boost converter example



Taylor Series Linearization:

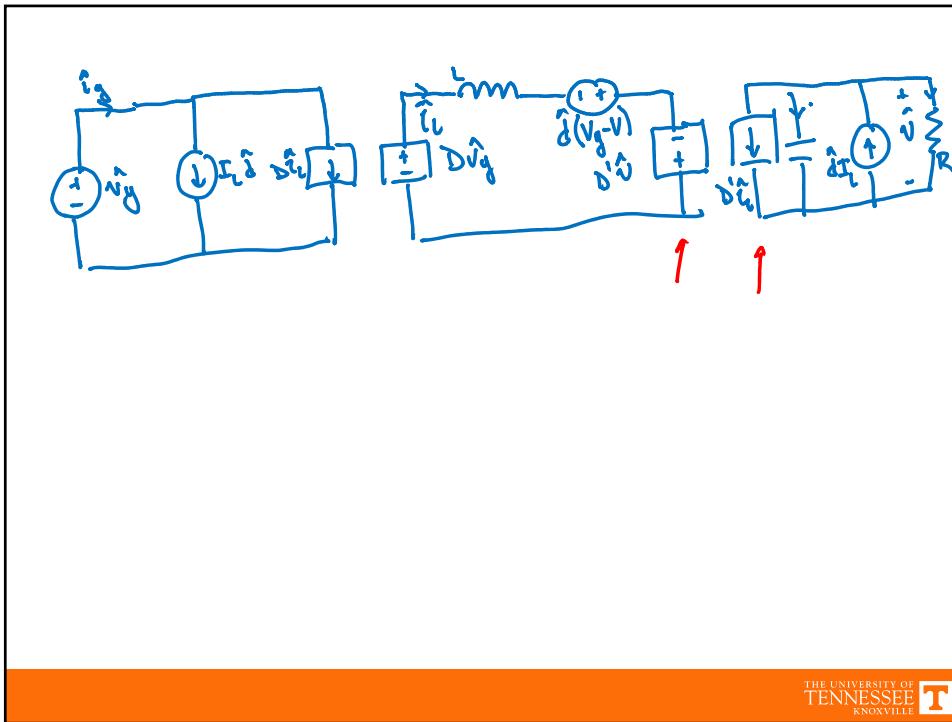
$$L \frac{d\langle i_L \rangle}{dt} = d(t) \langle v_g \rangle + \overset{(1-d(t))}{d'(t)} \langle v \rangle = f(d, v_g, v)$$

$$L \frac{d\hat{i}_L}{dt} = \hat{v}_g \frac{df}{dv_g} + \hat{v} \frac{df}{dv} + \hat{d} \frac{df}{dd}$$

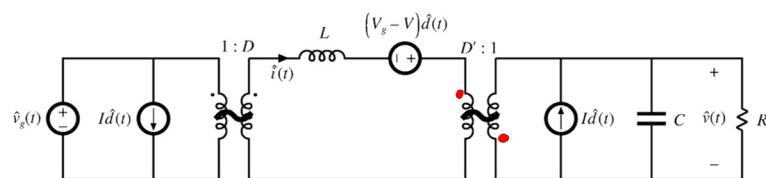
$$2 \quad L \frac{d\hat{i}_L}{dt} = \hat{v}_g D + \hat{v} D' + \hat{d} (\hat{v}_g - \hat{v})$$

$$3 \quad C \frac{d\hat{v}}{dt} = \hat{v} \left(-\frac{1}{R} \right) + \hat{i}_L (-D') + \hat{d} I_L$$

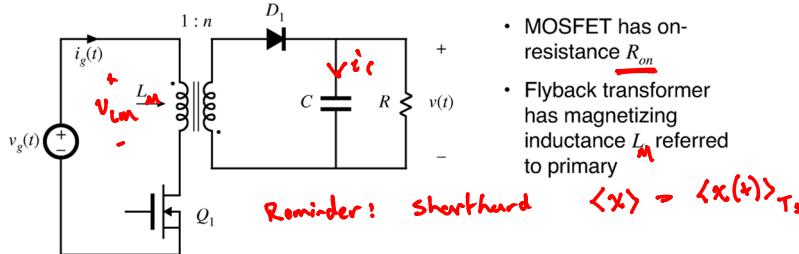
$$1 \quad \hat{i}_g = D \hat{i}_L + I_L \hat{d}$$

THE UNIVERSITY OF
TENNESSEE 
KNOXVILLE

Buck-Boost Small-Signal AC Equivalent Circuit

Buck-boostTHE UNIVERSITY OF
TENNESSEE 
KNOXVILLE

7.2.9: Nonideal Flyback Modeling



- MOSFET has on-resistance R_{on}
- Flyback transformer has magnetizing inductance L_M referred to primary

① Average equations:

$$\langle v_{Lm} \rangle = L \frac{d \langle i_{Lm} \rangle}{dt} = d(t) [\langle v_g \rangle - \langle i_{Lm} \rangle R_{on}] - d'(t) \frac{\langle v \rangle}{n}$$

$$\langle i_c \rangle = C \frac{d \langle v \rangle}{dt} = -\frac{\langle v \rangle}{R} + d'(t) \langle i_{Lm} \rangle \frac{1}{n}$$

$$\langle i_g \rangle = d(t) \langle i_{Lm} \rangle$$

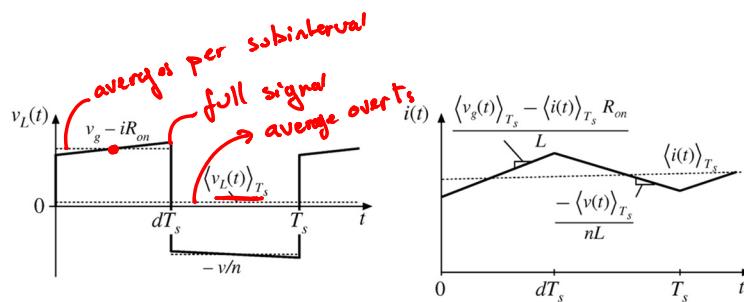
Fundamentals of Power Electronics

47

Chapter 7: AC equivalent circuit modeling



Inductor Waveforms



Average inductor voltage:

$$\langle v_L(t) \rangle_{T_s} = d(t) \left(\langle v_g(t) \rangle_{T_s} - \langle i(t) \rangle_{T_s} R_{on} \right) + d'(t) \left(-\frac{\langle v(t) \rangle_{T_s}}{n} \right)$$

Hence, we can write:

$$L \frac{d \langle i(t) \rangle_{T_s}}{dt} = d(t) \langle v_g(t) \rangle_{T_s} - d(t) \langle i(t) \rangle_{T_s} R_{on} - d'(t) \frac{\langle v(t) \rangle_{T_s}}{n}$$

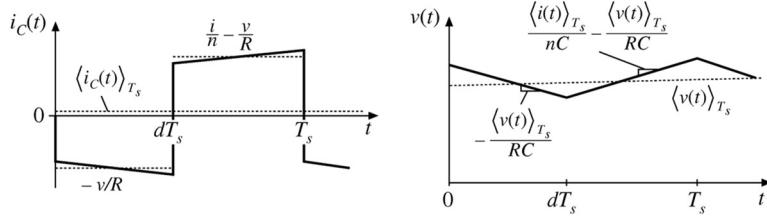
Fundamentals of Power Electronics

51

Chapter 7: AC equivalent circuit modeling



Capacitor Waveforms



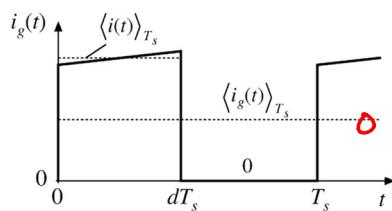
Average capacitor current:

$$\langle i_C(t) \rangle_{T_s} = d(t) \left(\frac{-\langle v(t) \rangle_{T_s}}{R} \right) + d'(t) \left(\frac{\langle i(t) \rangle_{T_s} - \langle v(t) \rangle_{T_s}}{n} \right)$$

Hence, we can write:

$$C \frac{d\langle v(t) \rangle_{T_s}}{dt} = d'(t) \frac{\langle i(t) \rangle_{T_s} - \langle v(t) \rangle_{T_s}}{n}$$

Input Current Waveform



Average input current:

$$\langle i_g(t) \rangle_{T_s} = d(t) \langle i(t) \rangle_{T_s}$$

Flyback Linearization

$$L \frac{d\langle i_{lm} \rangle}{dt} = d(t) [v_g - \langle i_{lm} \rangle R_{on}] - d'(t) \frac{\langle v \rangle}{n}$$

AC: $L \frac{d\hat{i}_{lm}}{dt} = v_g D - \hat{i}_{lm} DR_{on} - \hat{v} \frac{D'}{n} + \hat{d}(v_g + \frac{v}{n} - \hat{i}_{lm} R_{on})$

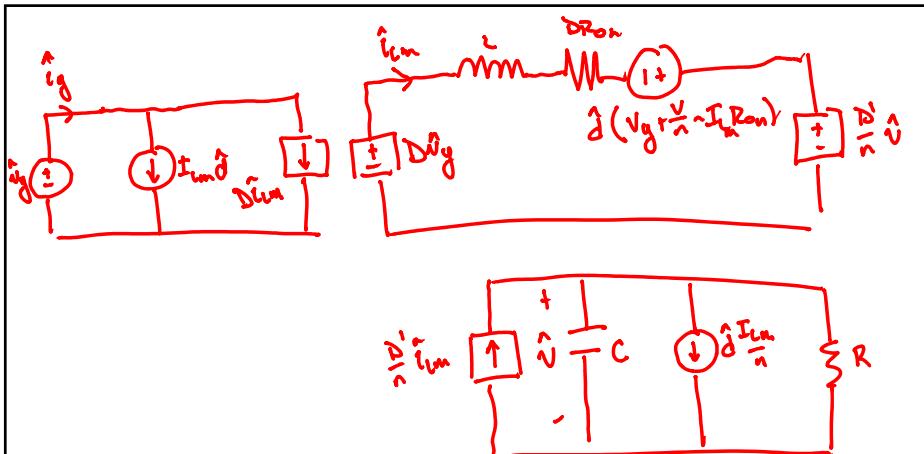
$$C \frac{d\langle v \rangle}{dt} = -\frac{\langle v \rangle}{R} + d'(t) \frac{\langle i_{lm} \rangle}{n}$$

AC: $C \frac{d\hat{v}}{dt} = \hat{v} \left(-\frac{1}{R} \right) + \hat{i}_{lm} \frac{D'}{n} - \hat{d} \frac{\hat{i}_{lm}}{n}$

$$\langle i_g \rangle = d(t) \langle i_{lm} \rangle$$

AC: $\hat{i}_g = \hat{d} \hat{i}_{lm} + \hat{D} \hat{i}_{lm}$

THE UNIVERSITY OF
TENNESSEE 
KNOXVILLE



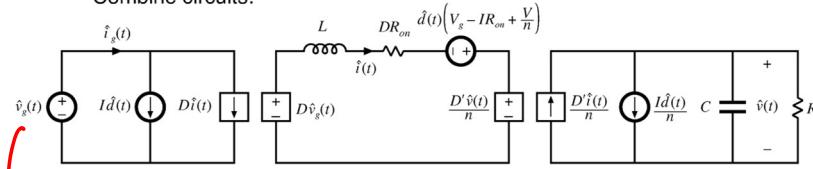
THE UNIVERSITY OF
TENNESSEE 
KNOXVILLE

Flyback Equivalent Circuit Model

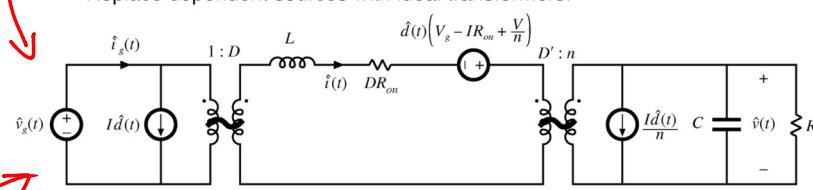


Flyback AC Model

Combine circuits:



Replace dependent sources with ideal transformers:



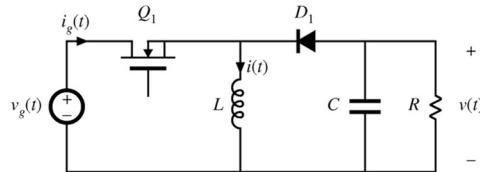


Remainder of Chapter 7

- understand & apply*
- Just exposure*
- 7.1 Introduction
 - 7.2 The basic AC modeling approach
 - 7.3 State-space averaging
 - 7.4 Circuit averaging and averaged switch modeling
 - 7.5 The canonical circuit model
 - 7.6 Modeling the pulse-width modulator
 - 7.7 Summary of key points



7.3: State Space Modeling of Buck Boost



Model nonidealities:

- MOSFET on-resistance R_{on}
- Diode forward voltage drop V_D

state vector

$$\mathbf{x}(t) = \begin{bmatrix} i(t) \\ v(t) \end{bmatrix}$$

input vector

$$\mathbf{u}(t) = \begin{bmatrix} v_g(t) \\ V_D \end{bmatrix}$$

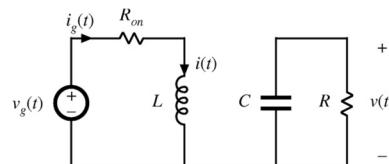
output vector

$$\mathbf{y}(t) = \begin{bmatrix} i_g(t) \end{bmatrix}$$

*whatever you want
to solve*

Model in Subinterval 1

$$\begin{aligned} L \frac{di(t)}{dt} &= v_g(t) - i(t) R_{on} \\ C \frac{dv(t)}{dt} &= -\frac{v(t)}{R} \\ i_g(t) &= i(t) \end{aligned}$$

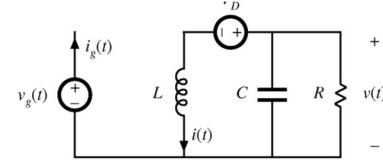


$$\underbrace{\begin{bmatrix} L & 0 \\ 0 & C \end{bmatrix}}_K \underbrace{\frac{d}{dt} \begin{bmatrix} i(t) \\ v(t) \end{bmatrix}}_{\frac{d\mathbf{x}(t)}{dt}} = \underbrace{\begin{bmatrix} -R_{on} & 0 \\ 0 & -\frac{1}{R} \end{bmatrix}}_{A_1} \underbrace{\begin{bmatrix} i(t) \\ v(t) \end{bmatrix}}_{\mathbf{x}(t)} + \underbrace{\begin{bmatrix} 1 & 0 \\ 0 & 0 \end{bmatrix}}_{B_1} \underbrace{\begin{bmatrix} v_g(t) \\ V_D \end{bmatrix}}_{\mathbf{u}(t)}$$

$$\underbrace{\begin{bmatrix} i_g(t) \end{bmatrix}}_{\mathbf{y}(t)} = \underbrace{\begin{bmatrix} 1 & 0 \end{bmatrix}}_{C_1} \underbrace{\begin{bmatrix} i(t) \\ v(t) \end{bmatrix}}_{\mathbf{x}(t)} + \underbrace{\begin{bmatrix} 0 & 0 \end{bmatrix}}_{E_1} \underbrace{\begin{bmatrix} v_g(t) \\ V_D \end{bmatrix}}_{\mathbf{u}(t)}$$

Model in Subinterval 2

$$\begin{aligned} L \frac{di(t)}{dt} &= v(t) - V_D \\ C \frac{dv(t)}{dt} &= -\frac{v(t)}{R} - i(t) \\ i_g(t) &= 0 \end{aligned}$$



$\text{K} \quad \underbrace{\left[\begin{array}{cc} L & 0 \\ 0 & C \end{array} \right] \frac{d}{dt} \left[\begin{array}{c} i(t) \\ v(t) \end{array} \right]}_{\frac{d\mathbf{x}(t)}{dt}} = \underbrace{\left[\begin{array}{cc} 0 & 1 \\ -1 & -\frac{1}{R} \end{array} \right]}_{\mathbf{A}_2} \underbrace{\left[\begin{array}{c} i(t) \\ v(t) \end{array} \right]}_{\mathbf{x}(t)} + \underbrace{\left[\begin{array}{cc} 0 & -1 \\ 0 & 0 \end{array} \right]}_{\mathbf{B}_2} \underbrace{\left[\begin{array}{c} v_g(t) \\ V_D \end{array} \right]}_{\mathbf{u}(t)}$

$$\mathbf{y}(t) = \underbrace{\left[\begin{array}{c} i_g(t) \\ 0 \end{array} \right]}_{\mathbf{C}_2} = \underbrace{\left[\begin{array}{c} 0 \\ 0 \end{array} \right]}_{\mathbf{x}(t)} + \underbrace{\left[\begin{array}{c} i(t) \\ v(t) \end{array} \right]}_{\mathbf{x}(t)} + \underbrace{\left[\begin{array}{c} 0 \\ 0 \end{array} \right]}_{\mathbf{E}_2} \underbrace{\left[\begin{array}{c} v_g(t) \\ V_D \end{array} \right]}_{\mathbf{u}(t)}$$

Fundamentals of Power Electronics

91

Chapter 7: AC equivalent circuit modeling

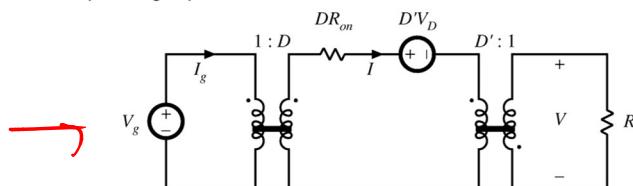
THE UNIVERSITY OF
TENNESSEE
KNOXVILLE

DC Solution

DC state equations:

$$\begin{aligned} \begin{bmatrix} 0 \\ 0 \end{bmatrix} &= \begin{bmatrix} -DR_{on} & D' \\ -D' & -\frac{1}{R} \end{bmatrix} \begin{bmatrix} I \\ V \end{bmatrix} + \begin{bmatrix} D & -D' \\ 0 & 0 \end{bmatrix} \begin{bmatrix} V_g \\ V_D \end{bmatrix} \\ [I_g] &= [D \ 0] \quad \begin{bmatrix} I \\ V \end{bmatrix} + [0 \ 0] \quad \begin{bmatrix} V_g \\ V_D \end{bmatrix} \end{aligned}$$

Corresponding equivalent circuit:



Fundamentals of Power Electronics

94

Chapter 7: AC equivalent circuit modeling

THE UNIVERSITY OF
TENNESSEE
KNOXVILLE

AC Solution

Evaluate matrices in small-signal model:

$$\begin{aligned} (\mathbf{A}_1 - \mathbf{A}_2) \mathbf{X} + (\mathbf{B}_1 - \mathbf{B}_2) \mathbf{U} &= \begin{bmatrix} -V \\ -I \end{bmatrix} + \begin{bmatrix} V_g - IR_{on} + V_D \\ 0 \end{bmatrix} = \begin{bmatrix} V_g - V - IR_{on} + V_D \\ I \end{bmatrix} \\ \textcolor{red}{\rightarrow} (\mathbf{C}_1 - \mathbf{C}_2) \mathbf{X} + (\mathbf{E}_1 - \mathbf{E}_2) \mathbf{U} &= [I] \end{aligned}$$

Small-signal ac state equations:

$$\begin{aligned} \begin{bmatrix} L & 0 \\ 0 & C \end{bmatrix} \frac{d}{dt} \begin{bmatrix} \hat{i}(t) \\ \hat{v}(t) \end{bmatrix} &= \begin{bmatrix} -DR_{on} & D' \\ -D' & -\frac{1}{R} \end{bmatrix} \begin{bmatrix} \hat{i}(t) \\ \hat{v}(t) \end{bmatrix} + \begin{bmatrix} D & -D' \\ 0 & 0 \end{bmatrix} \begin{bmatrix} \hat{v}_g(t) \\ \hat{v}_D(t) \end{bmatrix} + \begin{bmatrix} V_g - V - IR_{on} + V_D \\ I \end{bmatrix} \hat{d}(t) \\ \textcolor{red}{\curvearrowleft} \begin{bmatrix} \hat{i}_g(t) \end{bmatrix} &= \begin{bmatrix} D & 0 \\ 0 & 0 \end{bmatrix} \begin{bmatrix} \hat{i}(t) \\ \hat{v}(t) \end{bmatrix} + \begin{bmatrix} 0 & 0 \\ 0 & 0 \end{bmatrix} \begin{bmatrix} \hat{v}_g(t) \\ \hat{v}_D(t) \end{bmatrix} + \begin{bmatrix} 0 \\ I \end{bmatrix} \hat{d}(t) \end{aligned}$$

Resulting AC Equations

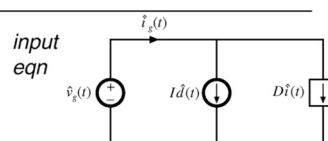
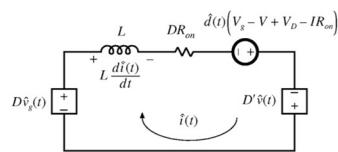
Small-signal ac equations, in scalar form:

$$\begin{aligned} L \frac{d\hat{i}(t)}{dt} &= D' \hat{v}(t) - DR_{on} \hat{i}(t) + D \hat{v}_g(t) + (V_g - V - IR_{on} + V_D) \hat{d}(t) \\ C \frac{d\hat{v}(t)}{dt} &= -D' \hat{i}(t) - \frac{\hat{v}(t)}{R} + I \hat{d}(t) \end{aligned}$$

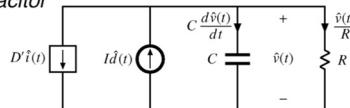
$$\hat{i}_g(t) = D \hat{i}(t) + I \hat{d}(t)$$

Corresponding equivalent circuits:

inductor equation

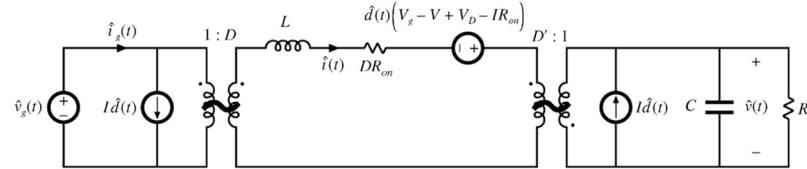


capacitor eqn

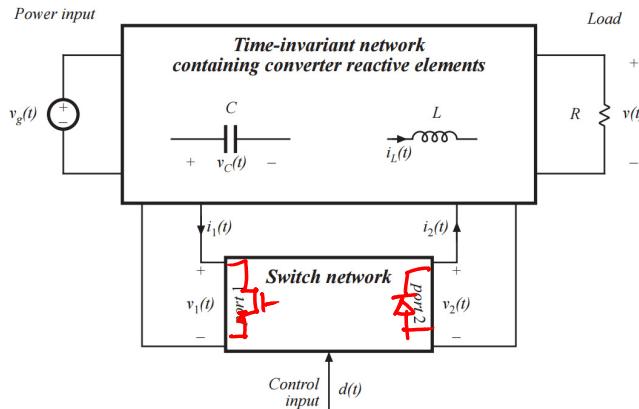


AC Equivalent Circuit Model

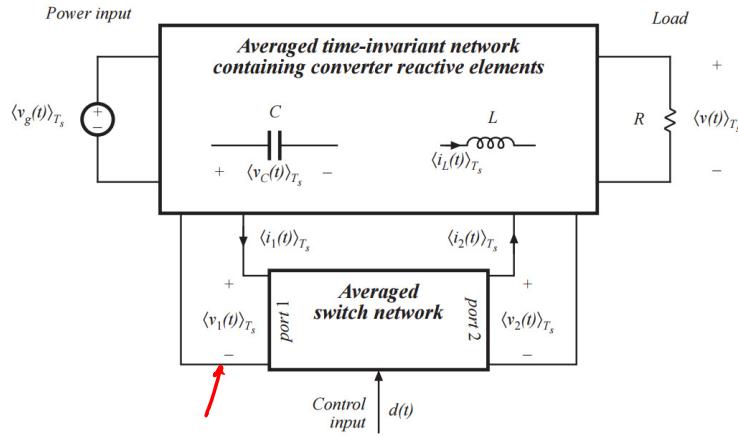
Combine individual circuits to obtain



7.4: Averaged Switch Modeling

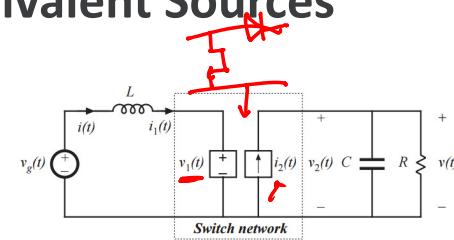
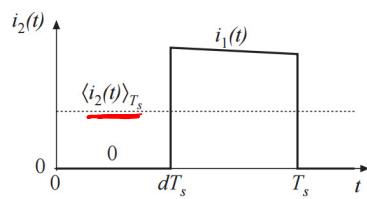
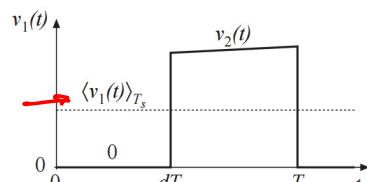


Averaged Switch Network



THE UNIVERSITY OF
TENNESSEE 
KNOXVILLE

Definition of Equivalent Sources



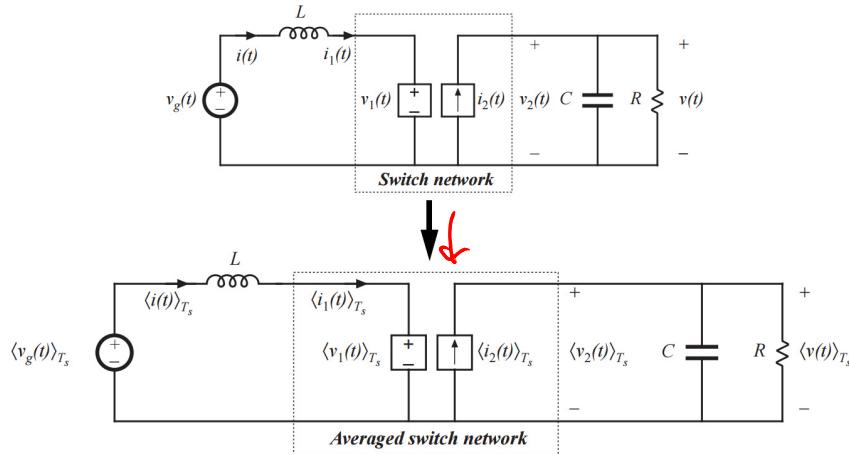
The waveforms of the dependent generators are defined to be identical to the actual terminal waveforms of the switch network.

The circuit is therefore electrical identical to the original converter.

So far, no approximations have been made.

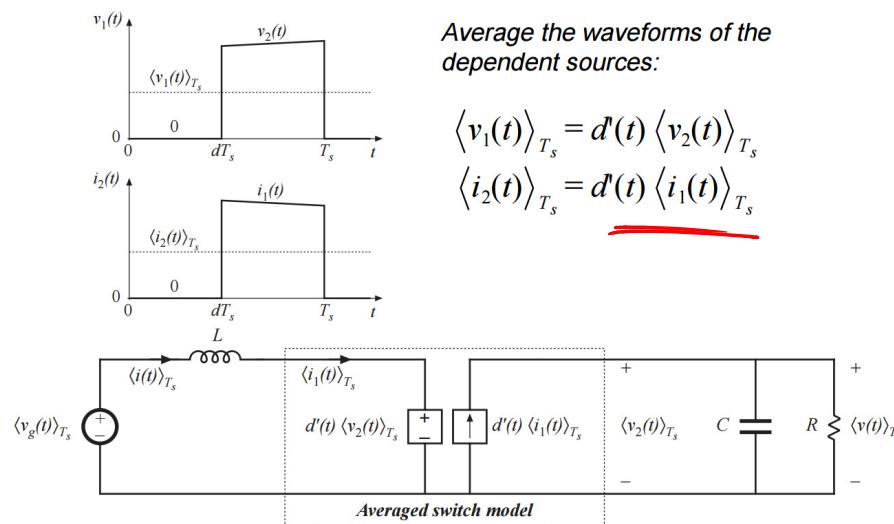
THE UNIVERSITY OF
TENNESSEE 
KNOXVILLE

Switch Averaging



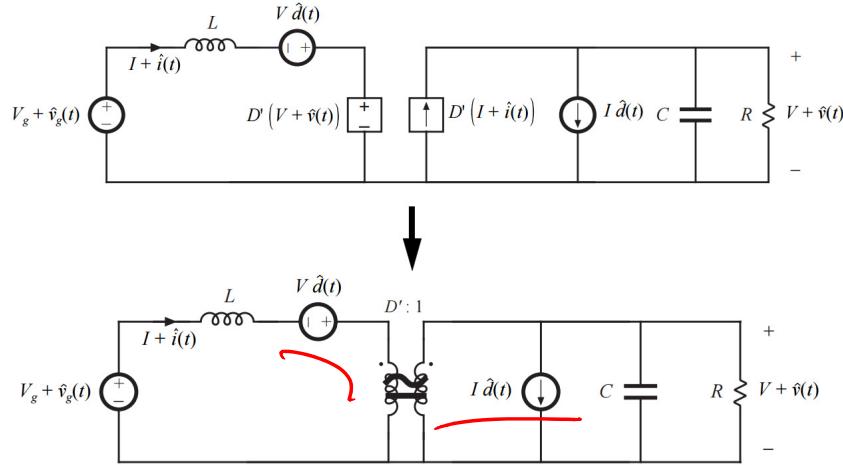
THE UNIVERSITY OF
TENNESSEE 
KNOXVILLE

Computation of Average Values



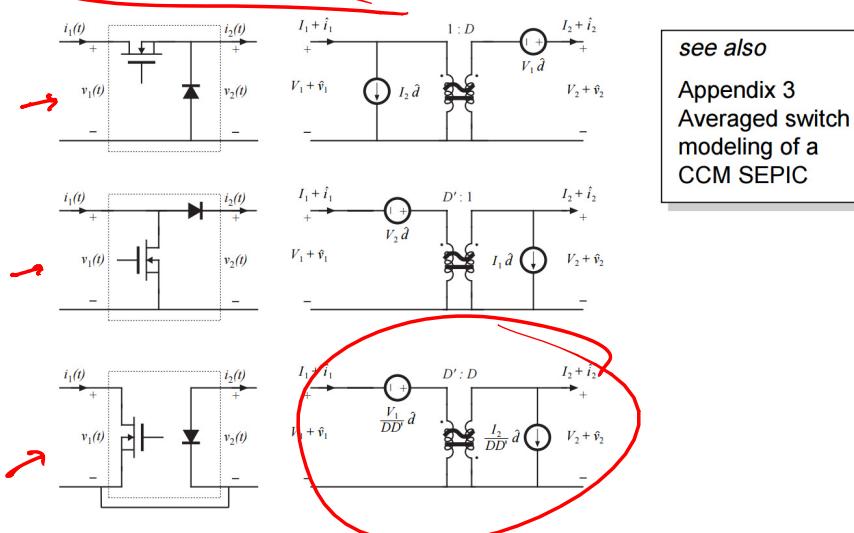
THE UNIVERSITY OF
TENNESSEE 
KNOXVILLE

Linearization of Model



THE UNIVERSITY OF
TENNESSEE 
KNOXVILLE

Averaged Switch Cells



THE UNIVERSITY OF
TENNESSEE 
KNOXVILLE

7.5: Canonical Circuit Model

All PWM CCM dc-dc converters perform the same basic functions:

- Transformation of voltage and current levels, ideally with 100% efficiency
- Low-pass filtering of waveforms
- Control of waveforms by variation of duty cycle

Hence, we expect their equivalent circuit models to be qualitatively similar.

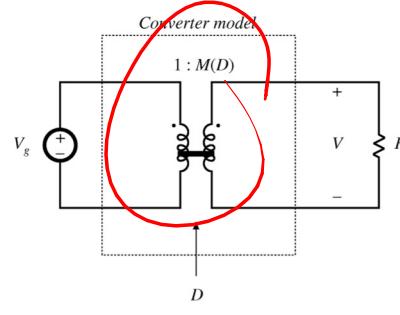
Canonical model:

- A standard form of equivalent circuit model, which represents the above physical properties
- Plug in parameter values for a given specific converter

Development of Canonical Model: DC

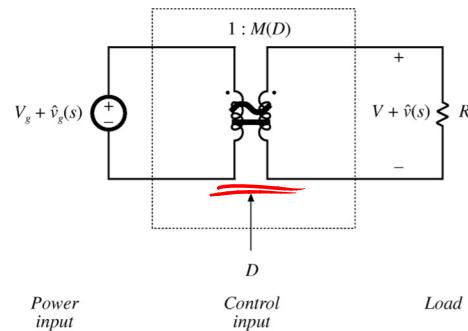
1. Transformation of dc voltage and current levels

- modeled as in Chapter 3 with ideal dc transformer
- effective turns ratio $M(D)$
- can refine dc model by addition of effective loss elements, as in Chapter 3



Direct AC Response

2. Ac variations in $v_g(t)$ induce ac variations in $v(t)$
- these variations are also transformed by the conversion ratio $M(D)$



Fundamentals of Power Electronics

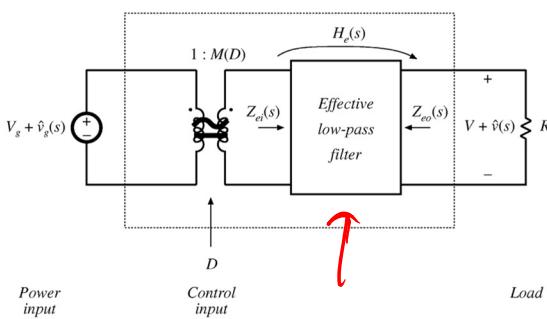
130

Chapter 7: AC equivalent circuit modeling



Filtering Elements

3. Converter must contain an effective low-pass filter characteristic
- necessary to filter switching ripple
 - also filters ac variations
 - effective filter elements may not coincide with actual element values, but can also depend on operating point



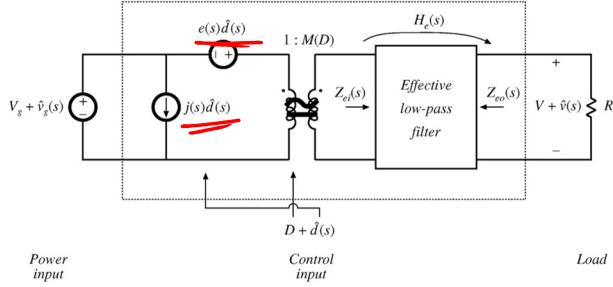
Fundamentals of Power Electronics

131

Chapter 7: AC equivalent circuit modeling

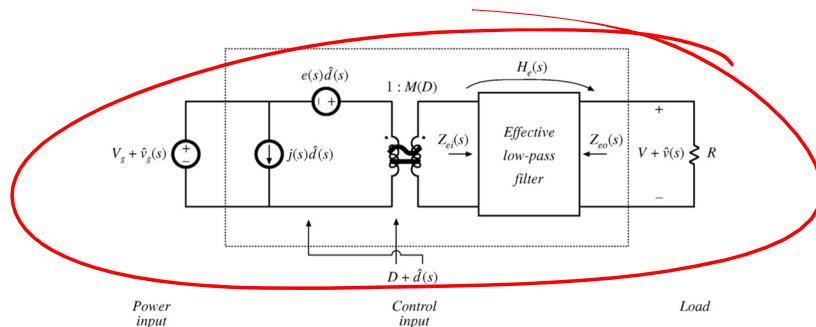


Control Response



4. Control input variations also induce ac variations in converter waveforms
 - Independent sources represent effects of variations in duty cycle
 - Can push all sources to input side as shown. Sources may then become frequency-dependent

Solving Transfer Functions

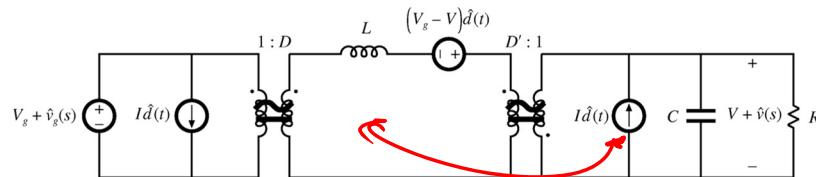


$$\text{Line-to-output transfer function: } G_{vg}(s) = \frac{\hat{v}(s)}{\hat{v}_g(s)} = M(D) H_e(s)$$

$$\text{Control-to-output transfer function: } G_{vd}(s) = \frac{\hat{v}(s)}{\hat{d}(s)} = e(s) M(D) H_e(s)$$

Example: Buck Boost Model

Small-signal ac model of the buck-boost converter



- Push independent sources to input side of transformers
- Push inductor to output side of transformers
- Combine transformers

Fundamentals of Power Electronics

134

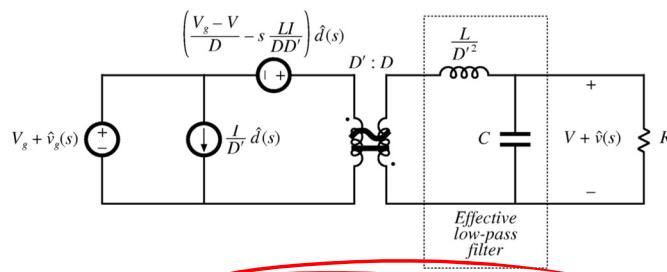
Chapter 7: AC equivalent circuit modeling



Buck-Boost in Canonical Form

Push voltage source through $1:D$ transformer, and combine with existing input-side transformer.

Combine series-connected transformers.



Fundamentals of Power Electronics

139

Chapter 7: AC equivalent circuit modeling



Canonical Form of Basic Converters

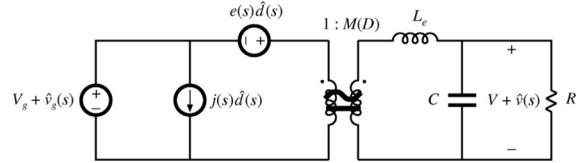


Table 7.1. Canonical model parameters for the ideal buck, boost, and buck-boost converters

Converter	$M(D)$	L_e	$e(s)$	$j(s)$
Buck	D	L	$\frac{V}{D^2}$	$\frac{V}{R}$
Boost	$\frac{1}{D}$	$\frac{L}{D^2}$	$V \left(1 - \frac{sL}{D^2 R}\right)$	$\frac{V}{D^2 R}$
Buck-boost	$-\frac{D}{D}$	$\frac{L}{D^2}$	$-\frac{V}{D^2} \left(1 - \frac{sDL}{D^2 R}\right)$	$-\frac{V}{D^2 R}$

Fundamentals of Power Electronics

141

Chapter 7: AC equivalent circuit modeling