

Selecting Phase Margin

stable for $\phi_m > 0^\circ$

How much phase margin is required?

A small positive phase margin leads to a stable closed-loop system having complex poles near the crossover frequency with high Q . The transient response exhibits overshoot and ringing.

Increasing the phase margin reduces the Q . Obtaining real poles, with no overshoot and ringing, requires a large phase margin.

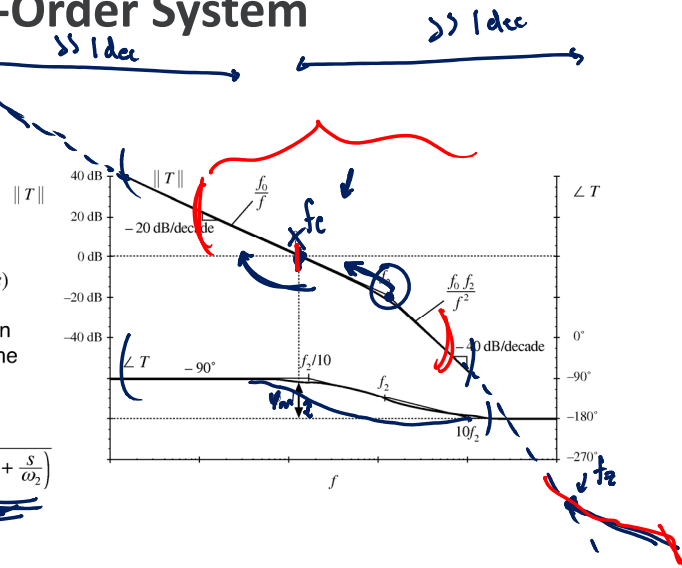
The relation between phase margin and closed-loop Q is quantified in this section.

when stable ($\phi_m > 0^\circ$)
 $\downarrow \phi_m \Rightarrow \uparrow Q_{cl}$ (freq domain)
 $\hookrightarrow \uparrow$ Ringing (time domain)

A Second-Order System

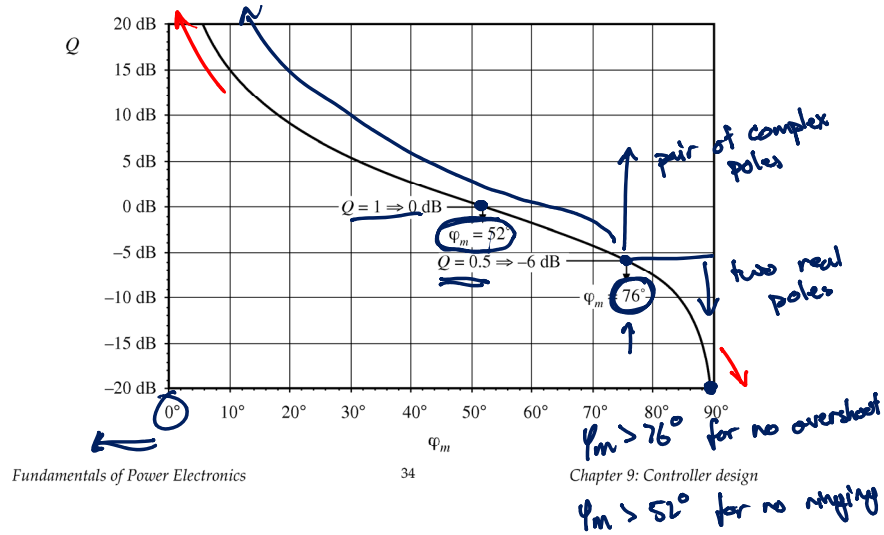
Consider the case where $T(s)$ can be well-approximated in the vicinity of the crossover frequency as

$$T(s) = \frac{1}{\left(\frac{s}{\omega_0}\right)\left(1 + \frac{s}{\omega_2}\right)}$$



Q vs. φ_m

$Q_{cl} \rightarrow Q$ of Closed-loop T_f

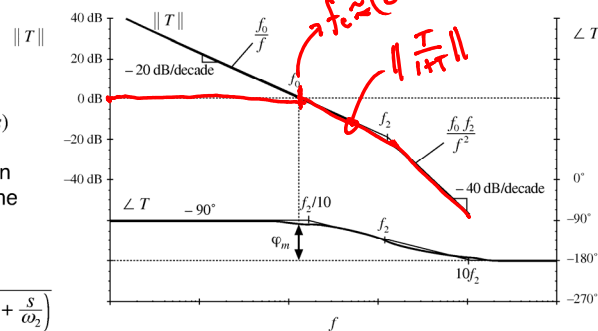


Closed-Loop Bandwidth

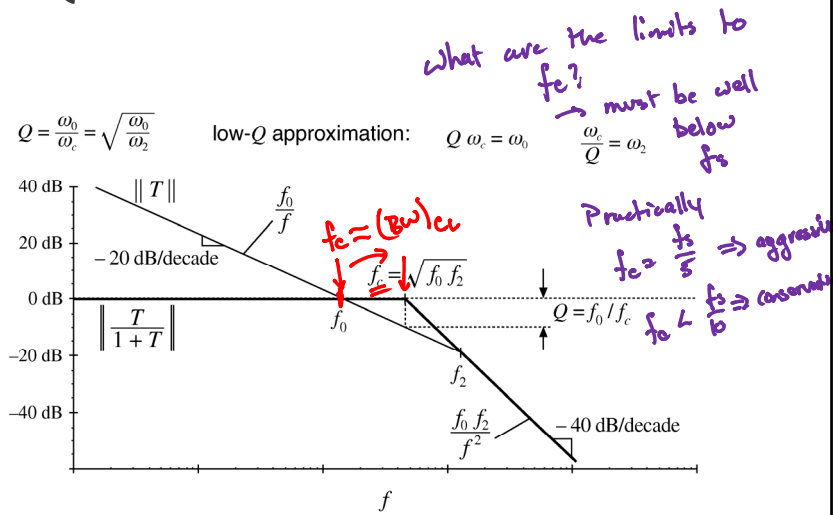
$$\frac{1}{Q_{cl}} = \frac{1}{K} \frac{T}{1+T}$$

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Low-Q Case

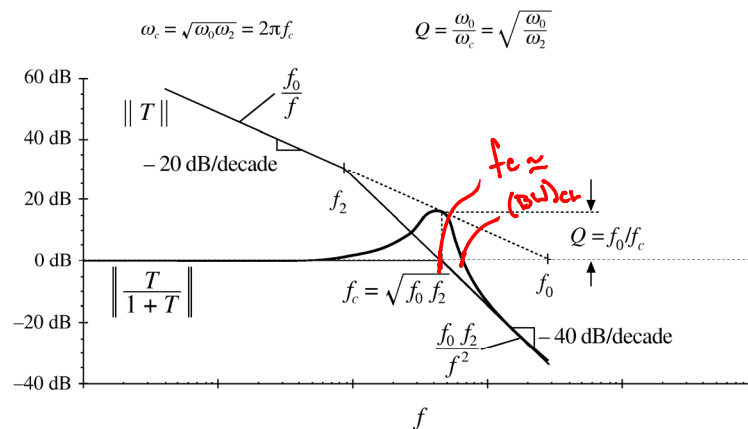


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High-Q Case



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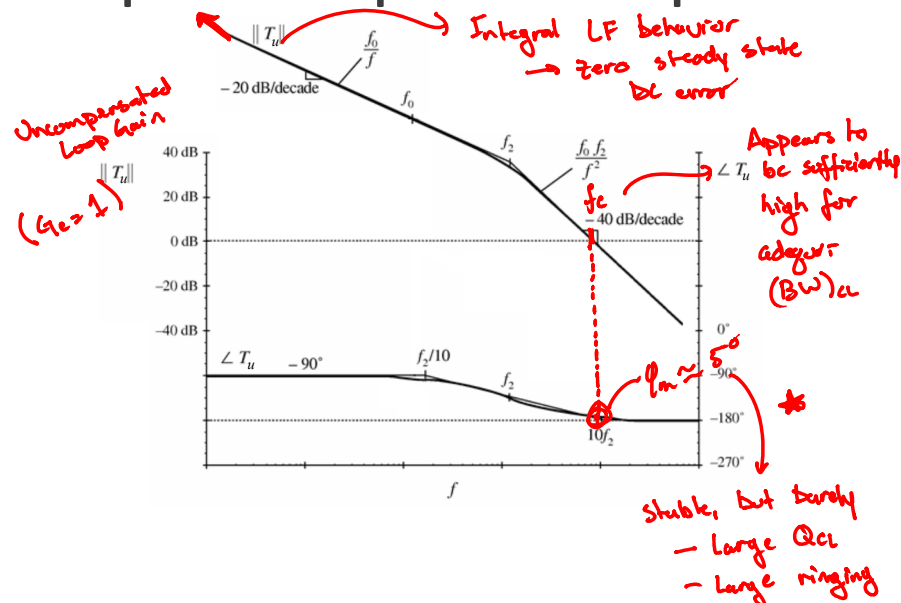
Design Approach

- Assume $G_c(s) = 1$, and plot the resulting uncompensated loop gain $T_u(s)$
- Examine uncompensated loop gain to determine the needs of the compensator
 - Is low-frequency gain amplitude large enough to result in low steady-state error?
 - Is ϕ_m sufficient for stability and requirements on ringing/overshoot?
 - Is f_c high enough for a sufficiently fast response?
- Construct compensator to address shortcomings of $T_u(s)$
 - Use "toolbox" of compensators on following slides

$T(s)$
w/ our designed
 $\rightarrow G_c(s)$

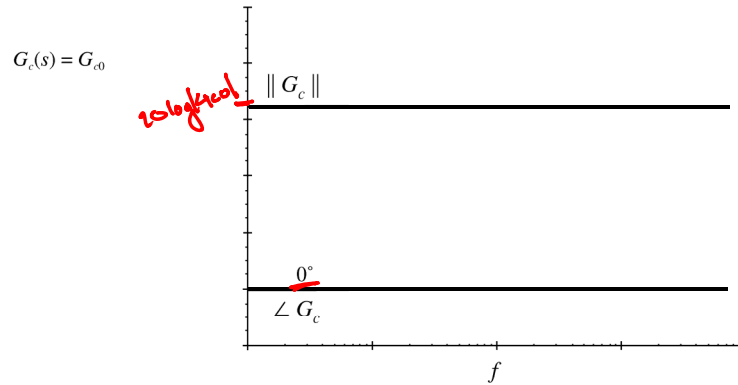
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Example: Uncompensated Loop Gain

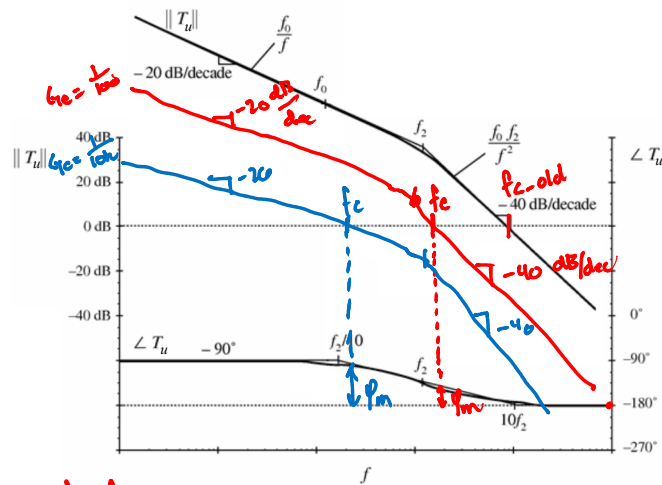


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Proportional (P) Compensator

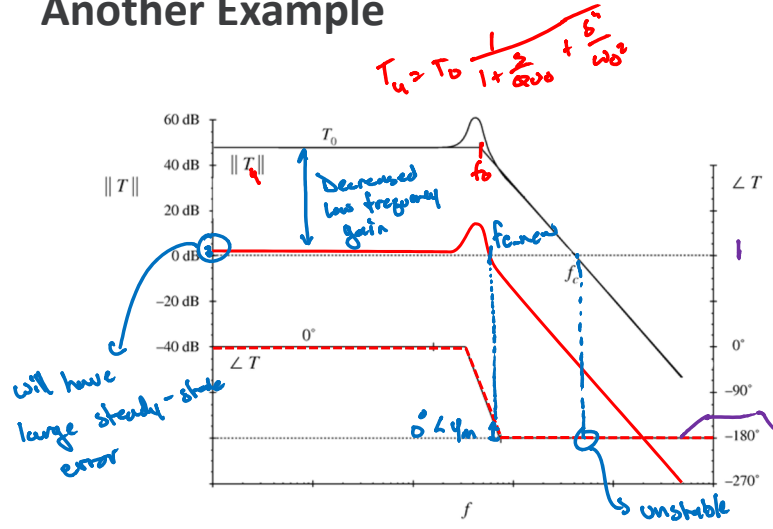


Stabilization by (P) Compensator



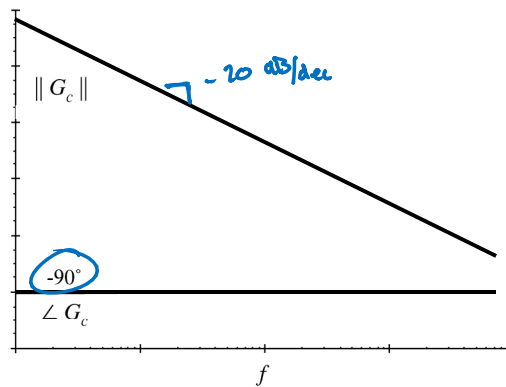
- Improved ϕ_m
- Reduced f_c

Another Example

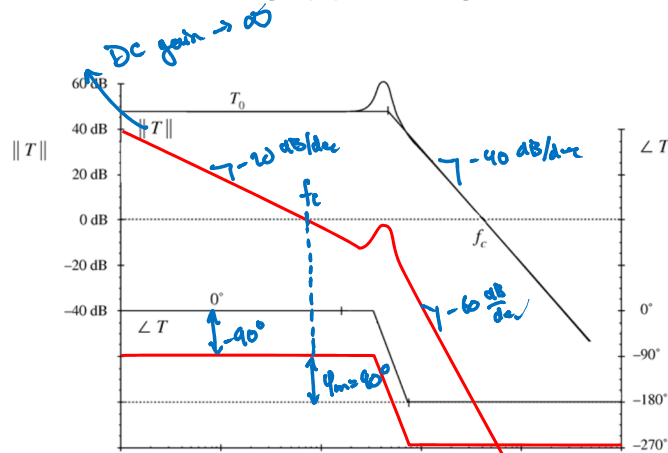


Integral (I) Compensator

$$G_c(s) = \frac{K}{s}$$



Stabilization by (I) Compensator

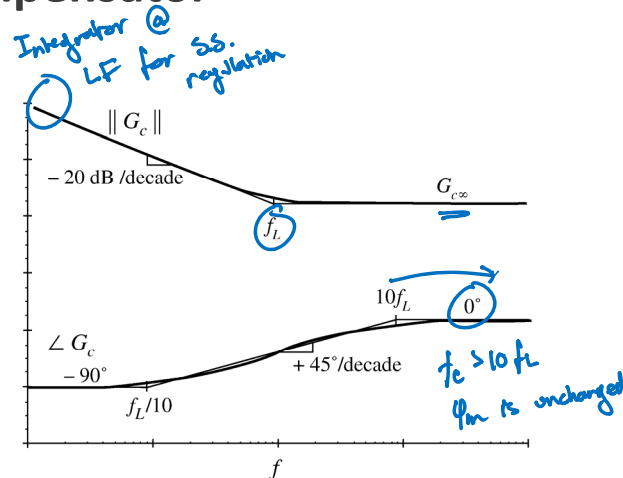


For DC regulation, a pure integral compensator with low enough K_i will usually stabilize a system with zero steady-state DC error (as long as they have 0 dB/dec LF slope)

Lag (PI) Compensator

$$G_c(s) = G_{c\infty} \left(1 + \frac{\omega_L}{s} \right)$$

Improves low-frequency loop gain and regulation



Example Lag Compensator Design

original
(uncompensated)

loop gain is

$$T_u(s) = \frac{T_{u0}}{\left(1 + \frac{s}{\omega_0}\right)}$$

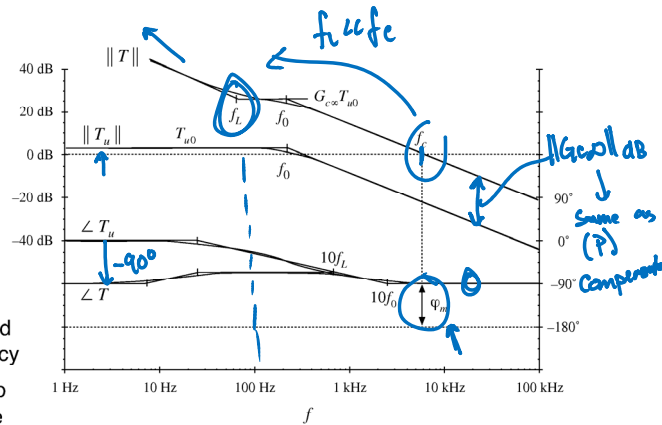
compensator:

$$G_c(s) = G_{c0} \left(1 + \frac{\omega_L}{s}\right)$$

Design strategy:
choose

G_{c0} to obtain desired
crossover frequency

ω_L sufficiently low to
maintain adequate
phase margin



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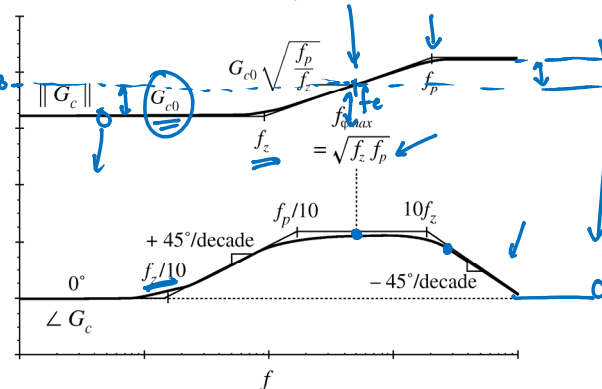
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Lead (PD) Compensator

$$G_c(s) = G_{c0} \left(1 + \frac{s}{\omega_z}\right) \left(1 + \frac{s}{\omega_p}\right)$$

Improves phase
margin

$\omega_z \ll \omega_p$

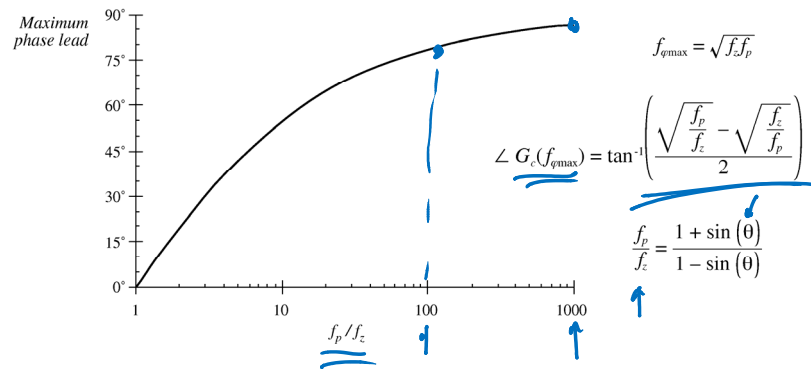


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Maximum Phase Lead



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Lead Compensator Design

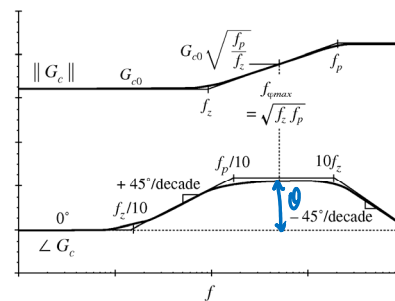
To optimally obtain a compensator phase lead of θ at frequency f_c , the pole and zero frequencies should be chosen as follows:

$$f_z = f_c \sqrt{\frac{1 - \sin(\theta)}{1 + \sin(\theta)}}$$

$$f_p = f_c \sqrt{\frac{1 + \sin(\theta)}{1 - \sin(\theta)}}$$

If it is desired that the magnitude of the compensator gain at f_c be unity, then G_{c0} should be chosen as

$$G_{c0} = \sqrt{\frac{f_z}{f_p}}$$

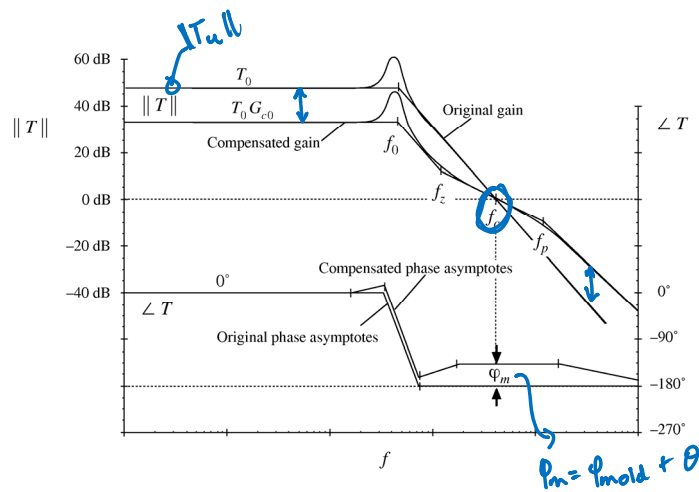


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Example Lead Compensator Design

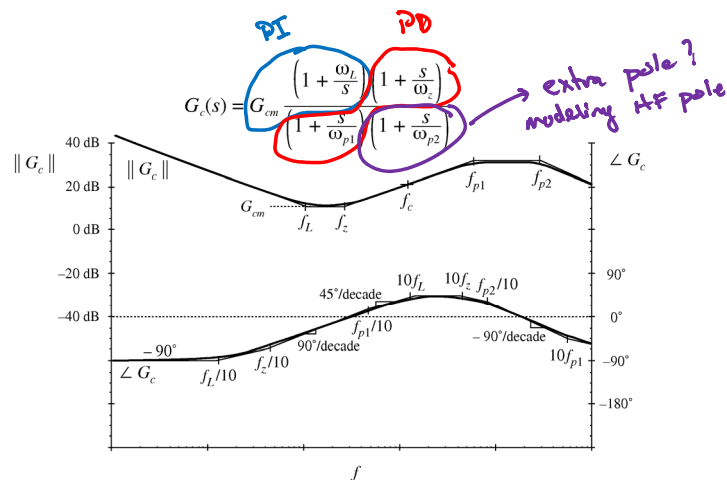


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Combined (PID) Compensator

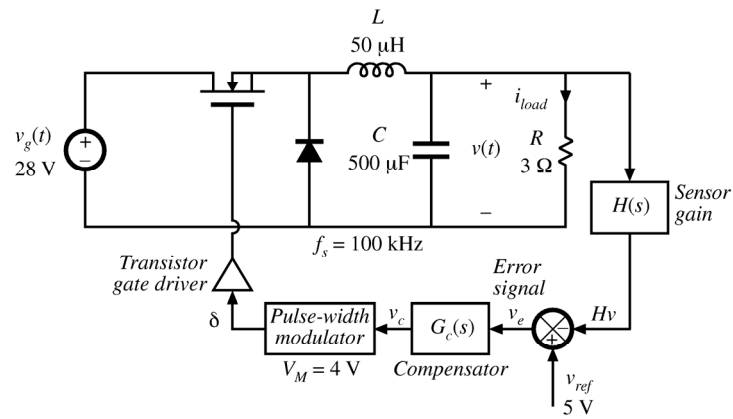


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Example Design of Buck Compensator



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