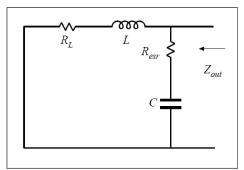


## **Output Impedance**

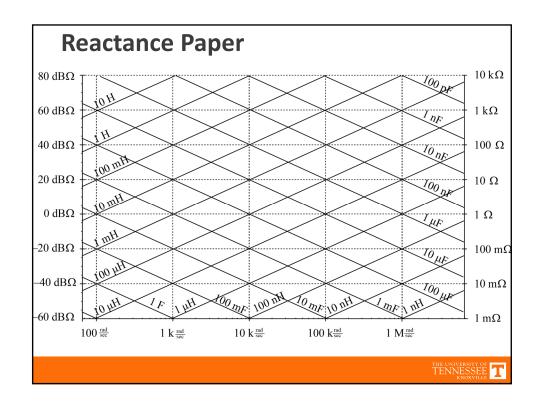
Synchronous buck open-loop output impedance

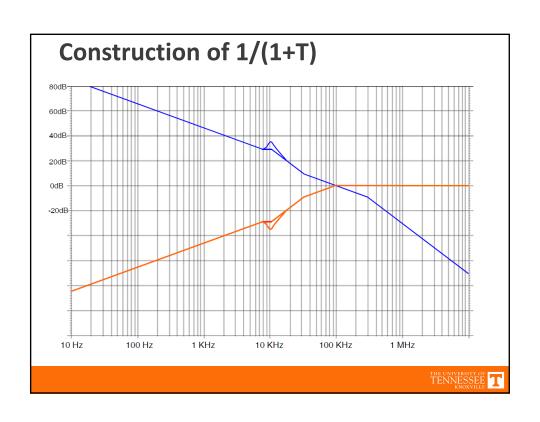


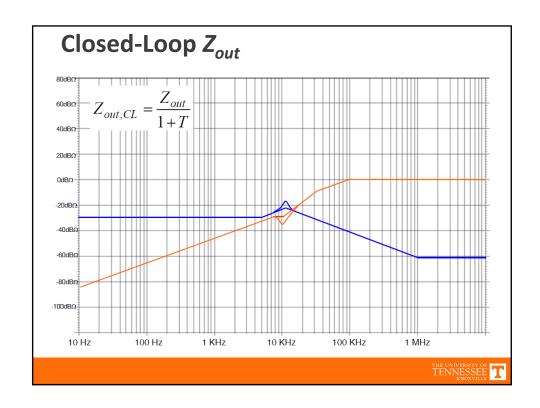
$$Z_{out}(s) = \left(R_{esr} + \frac{1}{sC}\right) ||\left(R_L + sL\right)|$$

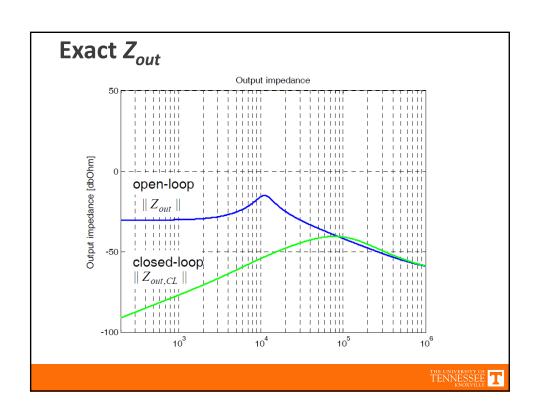
- *L* = 1 μH
- $R_L$  = 30 m $\Omega$
- C = 200 μF
- $R_{esr}$  = 0.8 m $\Omega$

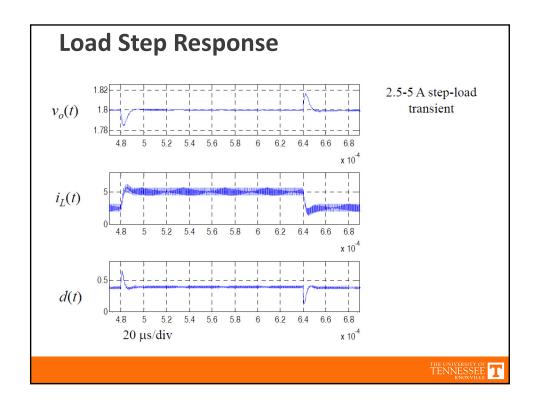
THE UNIVERSITY OF TENNESSEE T

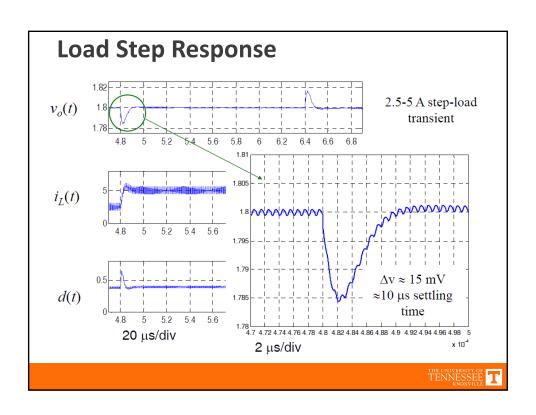












#### **Chapter 9: Summary**

- 1. Negative feedback causes the system output to closely follow the reference input, according to the gain 1/H(s). The influence on the output of disturbances and variation of gains in the forward path is reduced.
- 2. The loop gain T(s) is equal to the products of the gains in the forward and feedback paths. The loop gain is a measure of how well the feedback system works: a large loop gain leads to better regulation of the output. The crossover frequency  $f_c$  is the frequency at which the loop gain T has unity magnitude, and is a measure of the bandwidth of the control system.

Fundamentals of Power Electronics

Chapter 9: Controller design



#### **Chapter 9: Summary**

- 3. The introduction of feedback causes the transfer functions from disturbances to the output to be multiplied by the factor 1/(1+T(s)). At frequencies where T is large in magnitude (i.e., below the crossover frequency), this factor is approximately equal to 1/T(s). Hence, the influence of low-frequency disturbances on the output is reduced by a factor of 1/T(s). At frequencies where T is small in magnitude (i.e., above the crossover frequency), the factor is approximately equal to 1. The feedback loop then has no effect. Closed-loop disturbance-tooutput transfer functions, such as the line-to-output transfer function or the output impedance, can easily be constructed using the algebra-onthe-graph method.
- 4. Stability can be assessed using the phase margin test. The phase of Tis evaluated at the crossover frequency, and the stability of the important closed-loop quantities T/(1+T) and 1/(1+T) is then deduced. Inadequate phase margin leads to ringing and overshoot in the system transient response, and peaking in the closed-loop transfer functions.

Fundamentals of Power Electronics

Chapter 9: Controller design

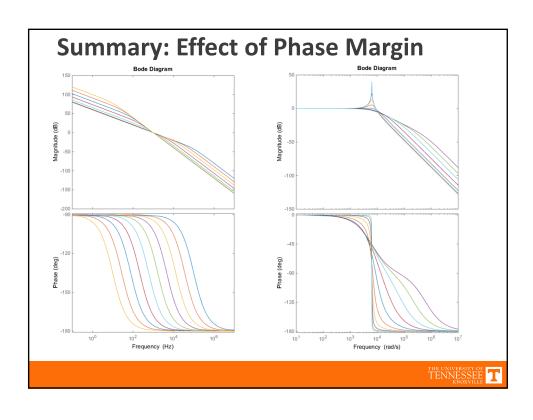


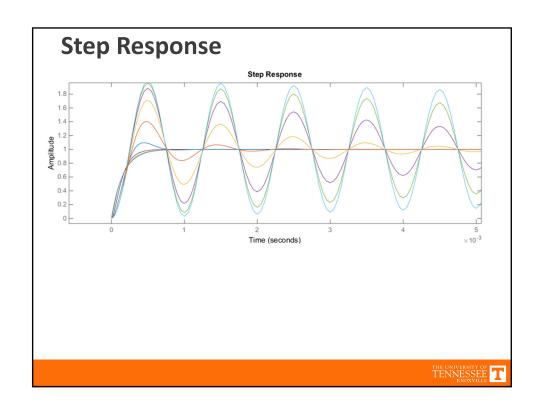
## **Chapter 9: Summary**

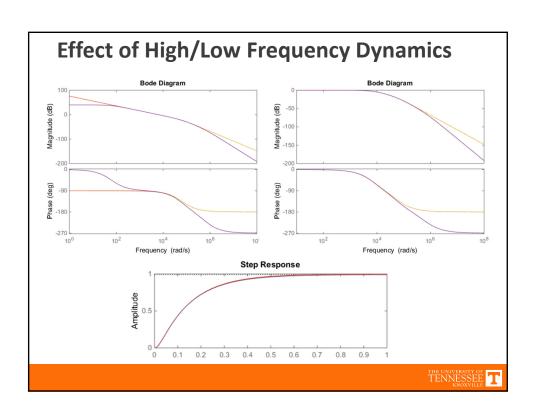
- 5. Compensators are added in the forward paths of feedback loops to shape the loop gain, such that desired performance is obtained. Lead compensators, or PD controllers, are added to improve the phase margin and extend the control system bandwidth. PI controllers are used to increase the low-frequency loop gain, to improve the rejection of low-frequency disturbances and reduce the steady-state error.
- 6. Loop gains can be experimentally measured by use of voltage or current injection. This approach avoids the problem of establishing the correct quiescent operating conditions in the system, a common difficulty in systems having a large dc loop gain. An injection point must be found where interstage loading is not significant. Unstable loop gains can also be measured.

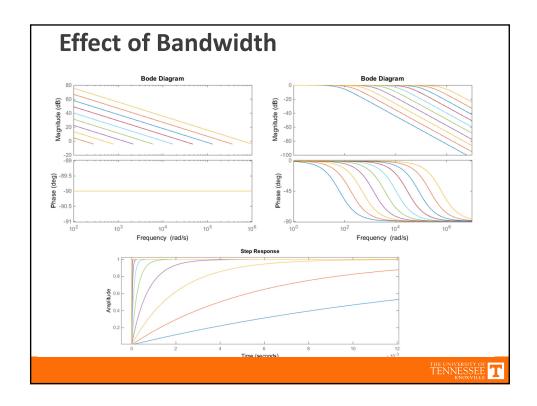
Fundamentals of Power Electronics

Chapter 9: Controller design









## **Part III: Magnetics**

- Ch 13 Basic Magnetics Theory
- Ch 14 Inductor Design
- Ch 15 Transformer Design



# **Chapter 13: Basic Magnetics Theory**

13.1 Review of Basic Magnetics

13.1.1 Basic relationships 13.1.2 Magnetic circuits

13.2 Transformer Modeling

13.2.1 The ideal transformer 13.2.3 Leakage inductances

13.2.2 The magnetizing inductance

13.3 Loss Mechanisms in Magnetic Devices

13.3.1 Core loss 13.3.2 Low-frequency copper loss

13.4 Eddy Currents in Winding Conductors

13.4.1 Skin and proximity effects 13.4.4 Power loss in a layer

13.4.2 Leakage flux in windings 13.4.5 Example: power loss in a transformer winding

13.4.3 Foil windings and layers 13.4.6 Interleaving the windings

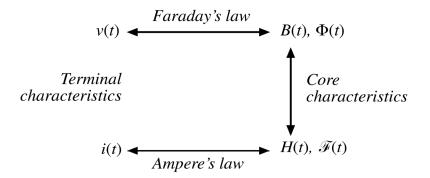
13.4.7 PWM waveform harmonics

Fundamentals of Power Electronics

Chapter 13: Basic Magnetics Theory



## **Basic Magnetics Relationships**



Fundamentals of Power Electronics

4

Chapter 13: Basic Magnetics Theory



Electric/Magnetic Duals	
	THE UNIVERSITY OF TENNESSEE T