Section 7.4

CANONICAL CIRCUIT MODEL

Canonical Circuit Model

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Chapter 7: AC equivalent circuit modeling

Canonical Form of Basic Converters

Table 7.1. Canonical model parameters for the ideal buck, boost, and buck-boost converters

Section 7.5

STATE SPACE AVERAGING

7.3: State Space Modeling of Buck Boost

Model nonidealities:

- · MOSFET onresistance R_{on}
- · Diode forward voltage drop V_D

 $v_g(t)$
 V_p $i(t)$
 $v(t)$ $\mathbf{x}(t) =$ $\mathbf{u}(t) =$

Model in Subinterval 1

State Space Model

Given: a PWM converter, operating in continuous conduction mode, with two subintervals during each switching period.

During subinterval 1, when the switches are in position 1, the converter reduces to a linear circuit that can be described by the following state equations:

$$
\mathbf{K} \frac{d\mathbf{x}(t)}{dt} = \mathbf{A}_1 \mathbf{x}(t) + \mathbf{B}_1 \mathbf{u}(t)
$$

$$
\mathbf{y}(t) = \mathbf{C}_1 \mathbf{x}(t) + \mathbf{E}_1 \mathbf{u}(t)
$$

During subinterval 2, when the switches are in position 2, the converter reduces to another linear circuit, that can be described by the following state equations:

$$
\mathbf{K} \frac{d\mathbf{x}(t)}{dt} = \mathbf{A}_2 \mathbf{x}(t) + \mathbf{B}_2 \mathbf{u}(t)
$$

$$
\mathbf{y}(t) = \mathbf{C}_2 \mathbf{x}(t) + \mathbf{E}_2 \mathbf{u}(t)
$$

State Space Averaging

The averaged (nonlinear) state equations:

$$
\mathbf{K} \frac{d\langle \mathbf{x}(t) \rangle_{T_s}}{dt} = \left(d(t) \mathbf{A}_1 + d'(t) \mathbf{A}_2 \right) \langle \mathbf{x}(t) \rangle_{T_s} + \left(d(t) \mathbf{B}_1 + d'(t) \mathbf{B}_2 \right) \langle \mathbf{u}(t) \rangle_{T_s}
$$

$$
\langle \mathbf{y}(t) \rangle_{T_s} = \left(d(t) \mathbf{C}_1 + d'(t) \mathbf{C}_2 \right) \langle \mathbf{x}(t) \rangle_{T_s} + \left(d(t) \mathbf{E}_1 + d'(t) \mathbf{E}_2 \right) \langle \mathbf{u}(t) \rangle_{T_s}
$$

The converter operates in equilibrium when the derivatives of all elements of $\langle x(t) \rangle_{T_s}$ are zero. Hence, the converter quiescent operating point is the solution of

 $0 = A X + B U$ $Y = C X + E U$

where $\mathbf{A} = D \mathbf{A}_1 + D' \mathbf{A}_2$ $X = equilibrium$ (dc) state vector and $\mathbf{B} = D \mathbf{B}_1 + D' \mathbf{B}_2$ $U = equilibrium$ (dc) input vector $C = D C_1 + D' C_2$ $Y = equilibrium$ (dc) output vector $E = D E_1 + D' E_2$ $D = equilibrium$ (dc) duty cycle

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DC Solution

DC state equations:
\n
$$
\begin{bmatrix} 0 \\ 0 \end{bmatrix} = \begin{bmatrix} -DR_{on} & D' \\ -D' & -\frac{1}{R} \end{bmatrix} \begin{bmatrix} I \\ V \end{bmatrix} + \begin{bmatrix} D & -D' \\ 0 & 0 \end{bmatrix} \begin{bmatrix} V_g \\ V_b \end{bmatrix}
$$
\n
$$
\begin{bmatrix} I_g \end{bmatrix} = \begin{bmatrix} D & 0 \end{bmatrix} \begin{bmatrix} I \\ V \end{bmatrix} + \begin{bmatrix} 0 & 0 \end{bmatrix} \begin{bmatrix} V_g \\ V_b \end{bmatrix}
$$

Corresponding equivalent circuit:

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Linearization of Averaged State Space Equations

Let
$$
\langle \mathbf{x}(t) \rangle_{T_s} = \mathbf{X} + \hat{\mathbf{x}}(t)
$$

\n $\langle \mathbf{u}(t) \rangle_{T_s} = \mathbf{U} + \hat{\mathbf{u}}(t)$
\n $\langle \mathbf{y}(t) \rangle_{T_s} = \mathbf{Y} + \hat{\mathbf{y}}(t)$
\n $d(t) = D + \hat{d}(t) \implies d'(t) = D' - \hat{d}(t)$
\n $\begin{aligned}\n\mathbf{X} &\gg \hat{\mathbf{x}}(t) \\
\mathbf{Y} &\gg \hat{\mathbf{x}}(t)\n\end{aligned}$

Substitute into averaged state equations:

$$
\mathbf{K} \frac{d\left(\mathbf{X} + \hat{\mathbf{x}}(t)\right)}{dt} = \left(\left(D + \hat{d}(t)\right)\mathbf{A}_1 + \left(D' - \hat{d}(t)\right)\mathbf{A}_2\right)\left(\mathbf{X} + \hat{\mathbf{x}}(t)\right) + \left(\left(D + \hat{d}(t)\right)\mathbf{B}_1 + \left(D' - \hat{d}(t)\right)\mathbf{B}_2\right)\left(\mathbf{U} + \hat{\mathbf{u}}(t)\right)
$$

$$
\begin{aligned} \left(\mathbf{Y} + \hat{\mathbf{y}}(t)\right) &= \left(\left(D + \hat{d}(t)\right) \mathbf{C}_1 + \left(D' - \hat{d}(t)\right) \mathbf{C}_2 \right) \left(\mathbf{X} + \hat{\mathbf{x}}(t)\right) \\ &+ \left(\left(D + \hat{d}(t)\right) \mathbf{E}_1 + \left(D' - \hat{d}(t)\right) \mathbf{E}_2 \right) \left(\mathbf{U} + \hat{\mathbf{u}}(t)\right) \end{aligned}
$$

Fundamentals of Power Electronics

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AC Solution

Evaluate matrices in small-signal model:

$$
\begin{aligned} \left(\mathbf{A}_1 - \mathbf{A}_2\right) \mathbf{X} + \left(\mathbf{B}_1 - \mathbf{B}_2\right) \mathbf{U} &= \begin{bmatrix} -V \\ I \end{bmatrix} + \begin{bmatrix} V_g - IR_{on} + V_D \\ 0 \end{bmatrix} = \begin{bmatrix} V_g - V - IR_{on} + V_D \\ I \end{bmatrix} \\ \left(\mathbf{C}_1 - \mathbf{C}_2\right) \mathbf{X} + \left(\mathbf{E}_1 - \mathbf{E}_2\right) \mathbf{U} &= \begin{bmatrix} I \end{bmatrix} \end{aligned}
$$

Small-signal ac state equations:

$$
\begin{bmatrix} L & 0 \\ 0 & C \end{bmatrix} \frac{d}{dt} \begin{bmatrix} \hat{i}(t) \\ \hat{v}(t) \end{bmatrix} = \begin{bmatrix} -DR_{on} & D' \\ -D' & -\frac{1}{R} \end{bmatrix} \begin{bmatrix} \hat{i}(t) \\ \hat{v}(t) \end{bmatrix} + \begin{bmatrix} D & -D' \\ 0 & 0 \end{bmatrix} \begin{bmatrix} \hat{v}_g(t) \\ \hat{v}_g^2(t) \end{bmatrix} + \begin{bmatrix} V_g - V - IR_{on} + V_D \\ I \end{bmatrix} \hat{d}(t)
$$

$$
\begin{bmatrix} \hat{i}_g(t) \\ \hat{v}_g(t) \end{bmatrix} = \begin{bmatrix} D & 0 \end{bmatrix} \begin{bmatrix} \hat{i}(t) \\ \hat{v}(t) \end{bmatrix} + \begin{bmatrix} 0 & 0 \\ 0 & 0 \end{bmatrix} \begin{bmatrix} \hat{v}_g(t) \\ \hat{v}_g^2(t) \end{bmatrix} + \begin{bmatrix} 0 \\ I \end{bmatrix} \hat{d}(t)
$$

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Resulting AC Equations

AVERAGE SWITCH MODELING

Removing Switch Network

Averaged Switch Network

Definition of Equivalent Sources

The waveforms of the dependent generators are defined to be identical to the actual terminal waveforms of the switch network.

The circuit is therefore electrical identical to the original converter.

So far, no approximations have been made.

Switch Averaging

Computation of Average Values

Linearization of Model

Averaged Switch Cells

CONVERTER TRANSFER FUNCTIONS

Chapter 8

Buck Boost Model

$$
G_{vg}(s) = \frac{\hat{v}(s)}{\hat{v}_g(s)} \bigg|_{\hat{d}(s) = 0} \qquad G_{vd}(s) = \frac{\hat{v}(s)}{\hat{d}(s)} \bigg|_{\hat{v}_g(s) = 0}
$$

Buck-Boost Control-to-Output TF

Buck-Boost Control-to-Output TF

