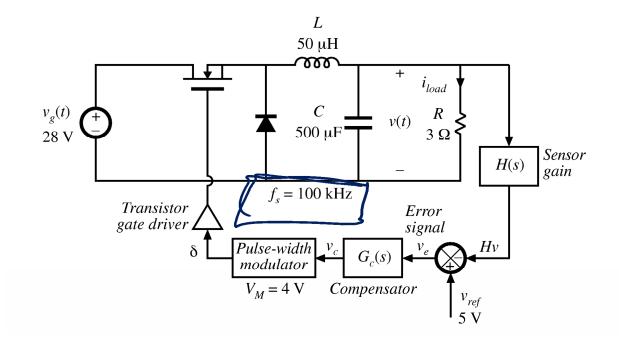
Example Design of Buck Compensator

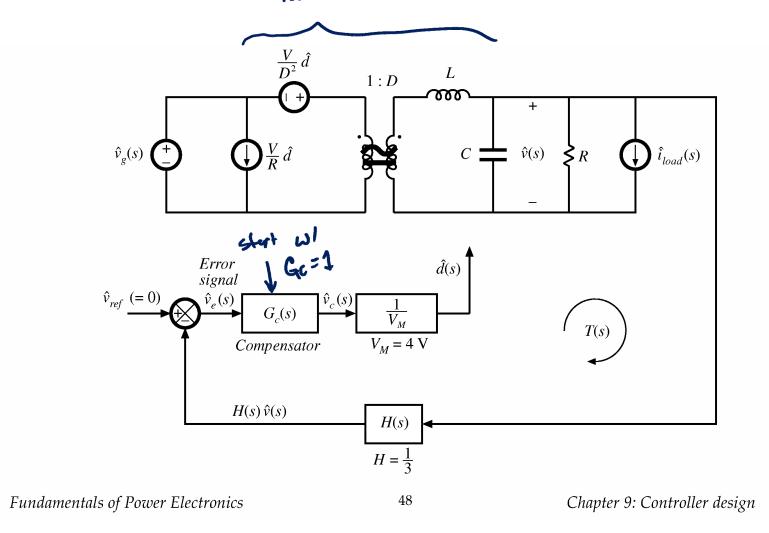


Input voltage	$V_g = 28$ V
Output	$V = 15$ V, $I_{load} = 5$ A, $R = 3\Omega$
Quiescent duty cycle	D = 15/28 = 0.536
Reference voltage	$V_{ref} = 5 V$
Quiescent value of control voltage	$V_c = DV_M = 2.14$ V
Gain H(s)	$H = V_{ref}/V = 5/15 = 1/3$



AC Power Stage Model

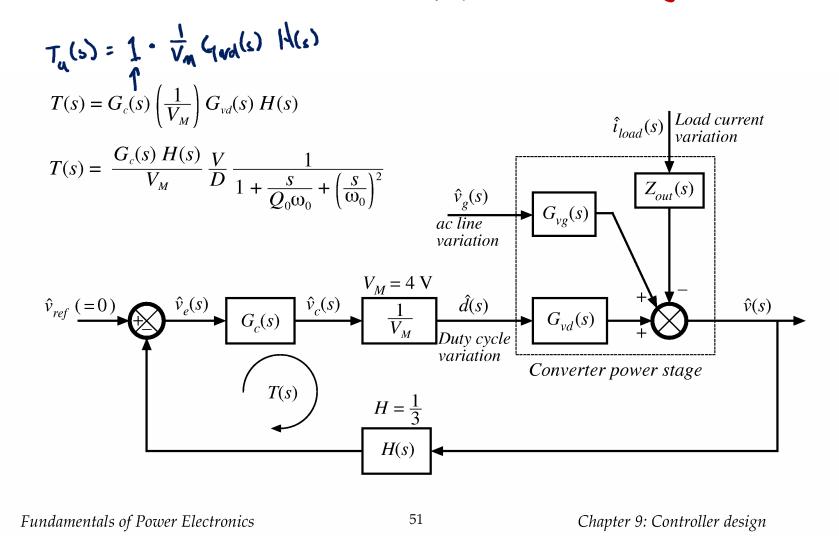
Tuble 7.1



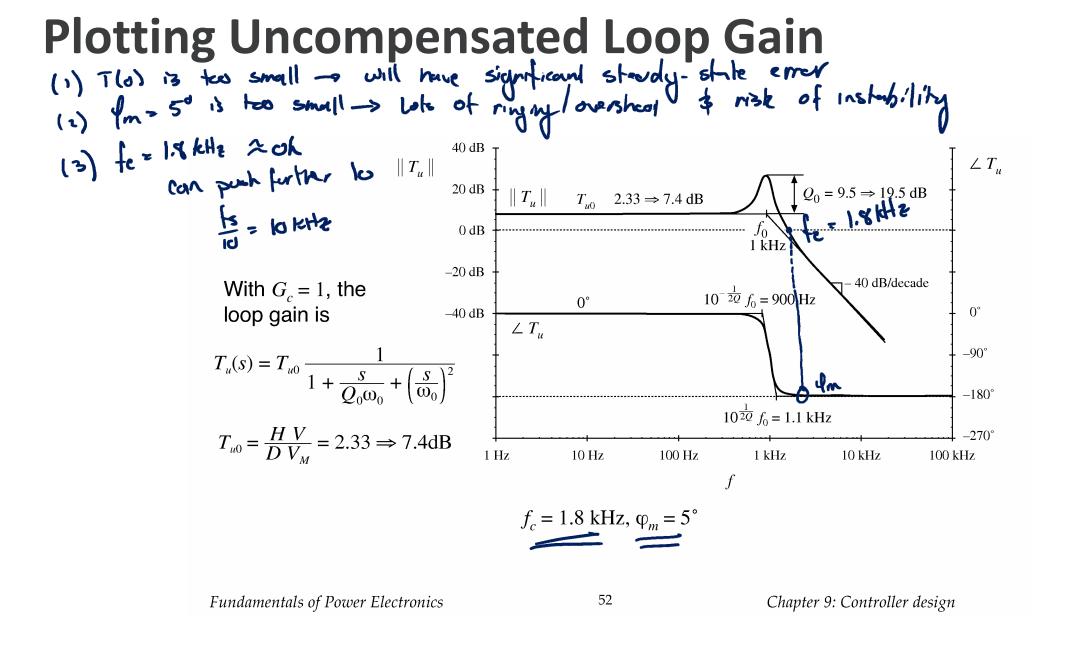


System Block Diagram

"Uncompensated loop Gooth"

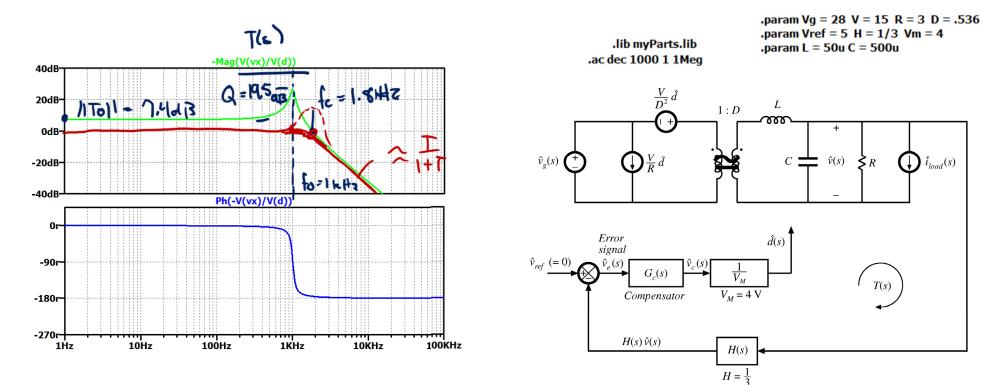








LTSpice Simulation – AC, Uncompensated



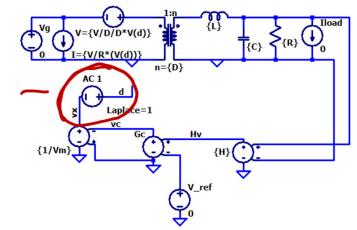


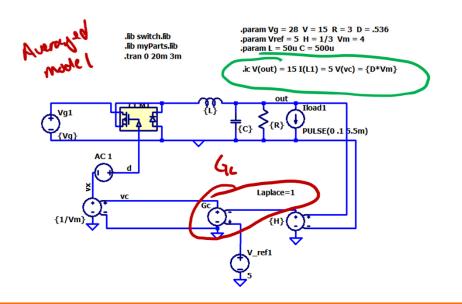
LTSpice ac Simulations

Usually, run a . op firet to make sure LTSpice finds the correct operating point, then run . ac $T = \frac{V(m)}{V(A)}$



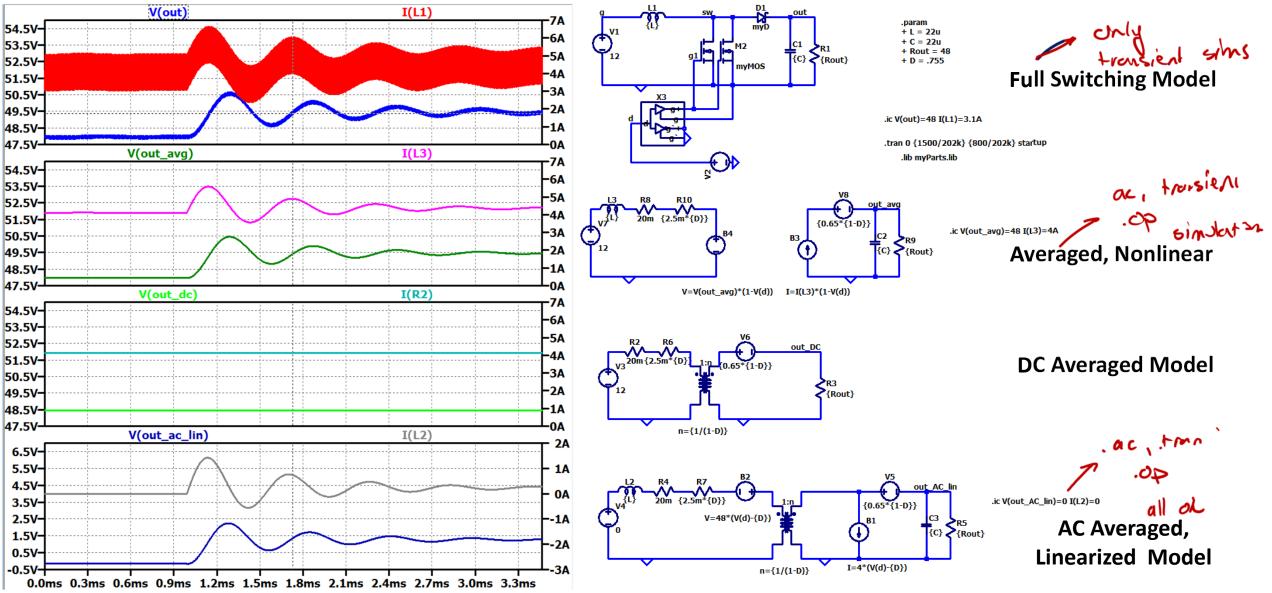
.param Vg = 28 V = 15 R = 3 D = .536 .param Vref = 5 H = 1/3 Vm = 4 .param L = 50u C = 500u





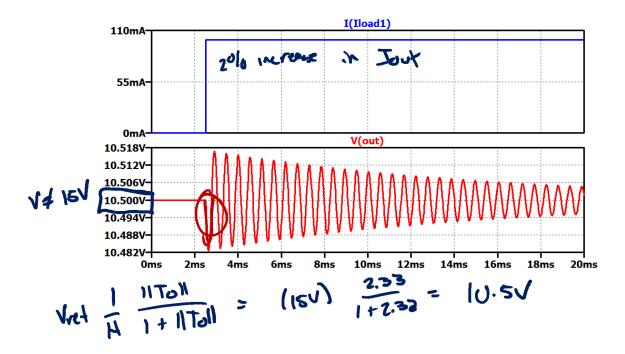


Model Simulation (L24)





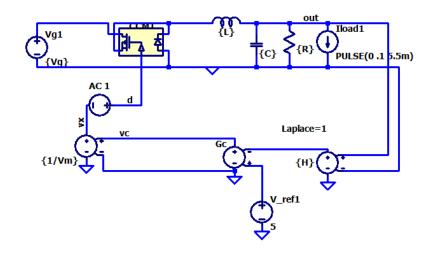
Transient Simulation, Uncompensated





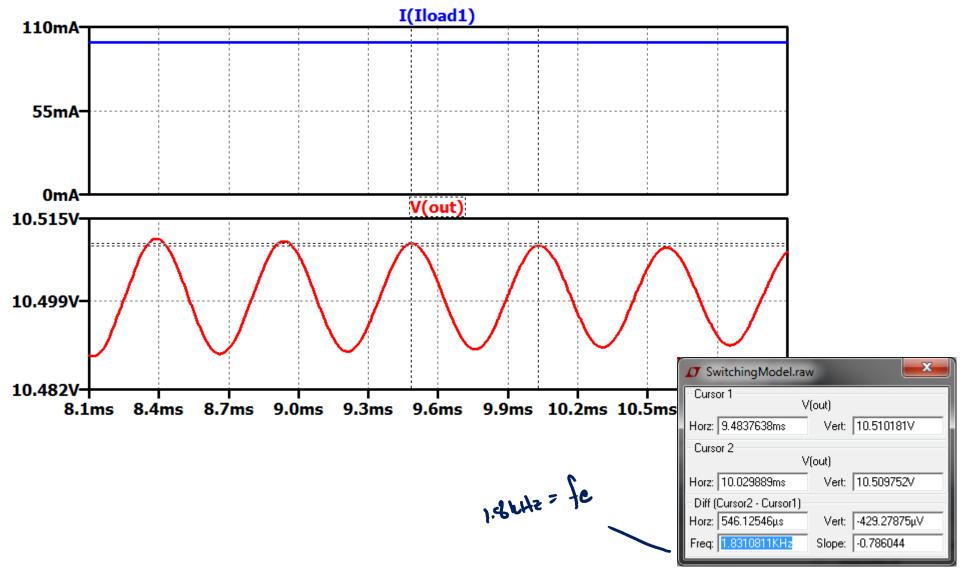
.param Vg = 28 V = 15 R = 3 D = .536 .param Vref = 5 H = 1/3 Vm = 4 .param L = 50u C = 500u

.ic V(out) = 15 I(L1) = 5 V(vc) = {D*Vm}



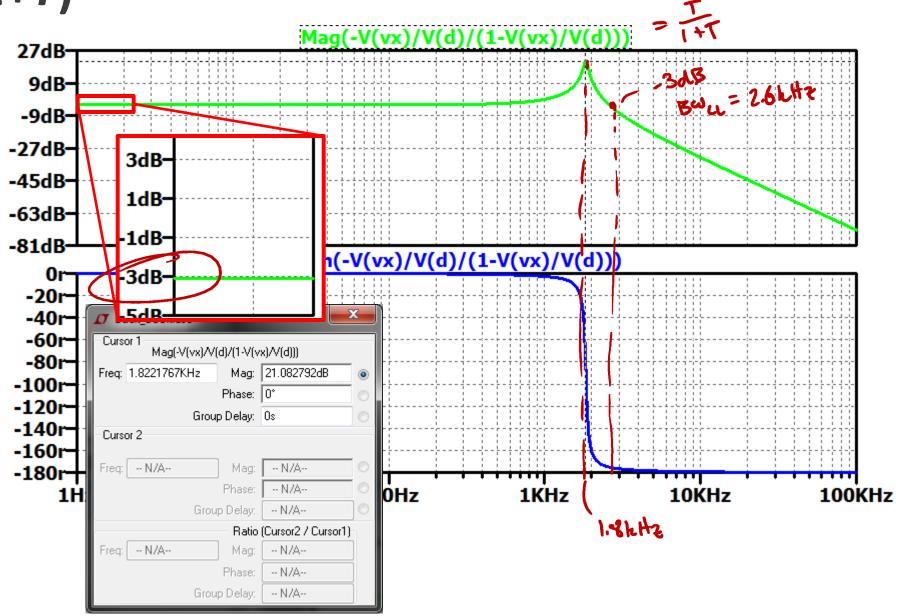


Ringing Frequency





T/(1+*T*)



TENNESSEE KNOXVILLE

Summary: Uncompensated Behavior

- Significant steady-state error
 - Need to increase low-frequency gain
- Barely stable; significant ringing
 - Need to increase ϕ_m
- Speed: ~ok
 - $-f_c = 1.8 \text{ kHz}$
 - $-(BW)_{CL} = 2.6 \text{ kHz}$
 - OK for $f_s \approx 10$ kHz or above

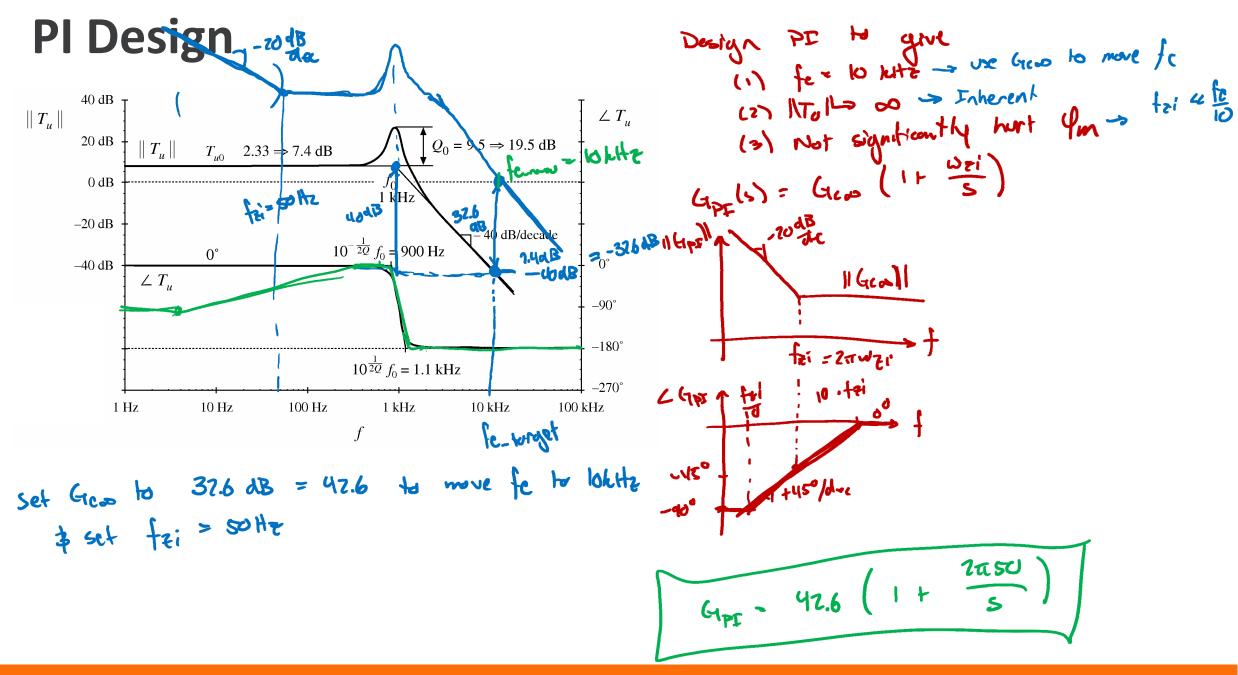


Compensator Design

- As an example, try to
 - Increase f_c to 10 kHz $\frac{f_s}{10}$
 - Increase ϕ_m to 76° (Q_{CL} =0.5) No my f
 - Increase $||T_0||$ to ∞ -> zero straly-style error

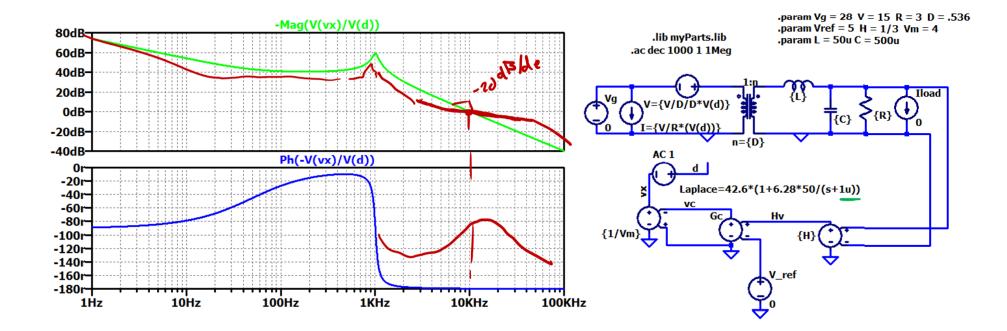
• Note: Book Chooses $f_c = 5$ kHz and $\phi_m = 52^\circ$ (Q=1)





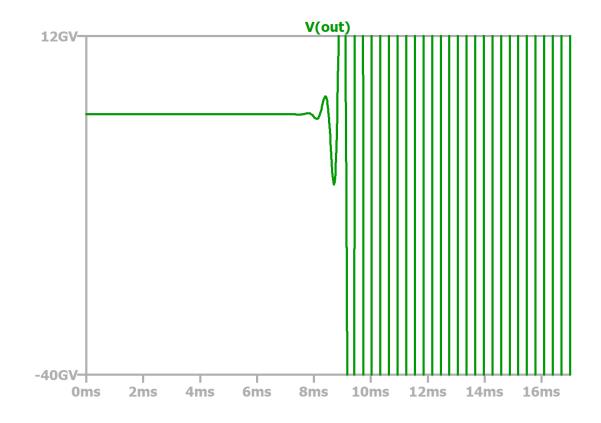


PI Simulation



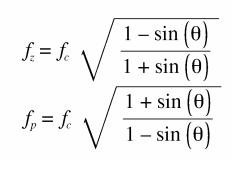


PI Transient Simulation





PD Design



 $G_{c0} = \sqrt{\frac{f_z}{f_p}}$

Design: wanted fm= 76° currently very fm= 0° Ls set Q= 76°

 $G_{PD} = G_{10} \frac{1+\frac{1}{\omega_2}}{1+\frac{1}{\omega_p}}$

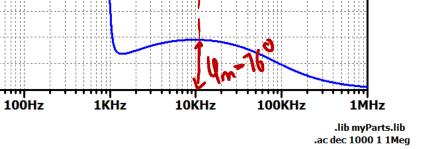


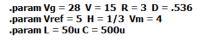
PID Simulation

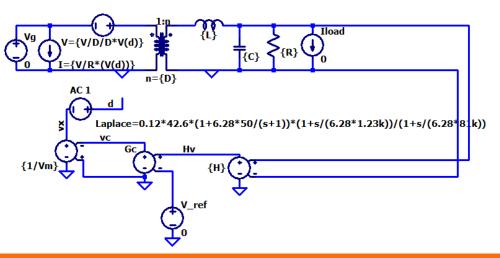
-20r--60r--100r-

-180r+ 1Hz

10Hz

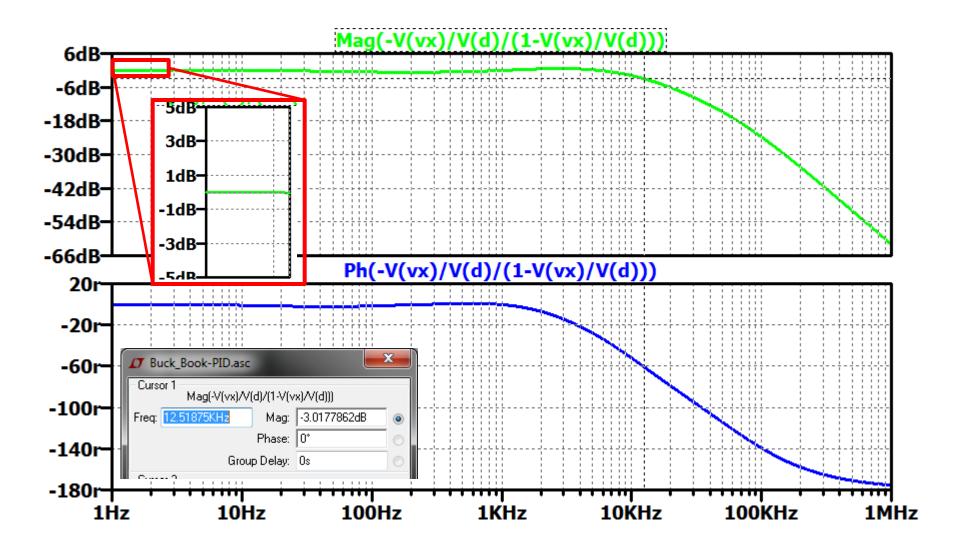








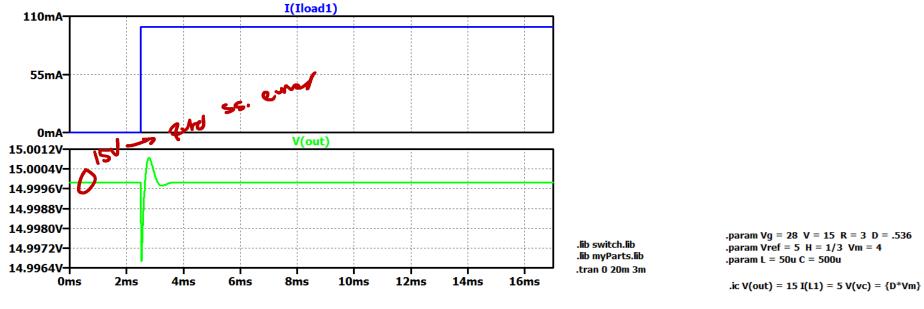
T/(1+*T*)











Vg1

{Vg}

{1/Vm}

AC 1

VC

Iload1

PULSE(0 .1 5.5m)

out

Laplace=0.12*42.6*(1+6.28*50/(s+1))*(1+s/(6.28*1.23k)), (1+s/(6.28*81k))

V_ref1

ウ

GC

Switching Simulation

