Announcements

- Engineering Job Fair Sept 16th, 2-6:00pm
 - tiny.utk.edu/EngFair

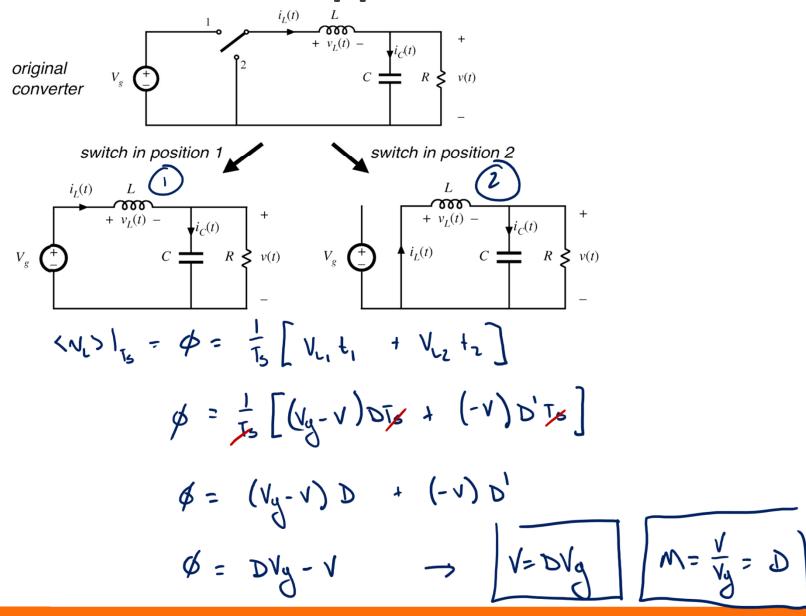


- Analog Design Engineer
- Digital Design Engineer
- Systems Engineer
- Test Engineer
- Applications Engineer
- Layout Designer

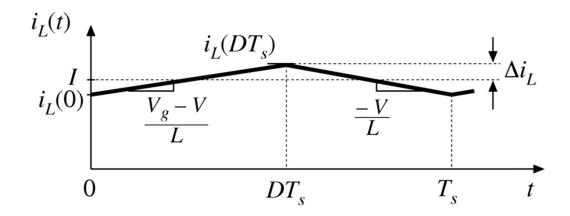
Juniors up to grad students



Volt-Second Balance: Direct Application



Current Ripple Magnitude

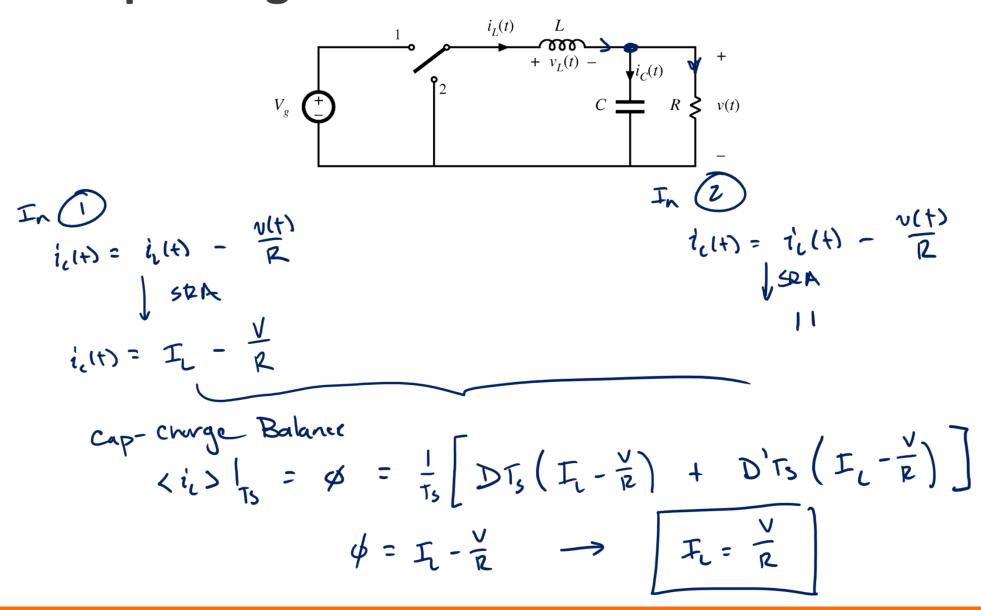


 $(change\ in\ i_L) = (slope)(length\ of\ subinterval)$

Reduce Die by

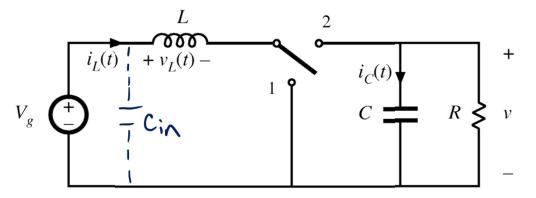
- 1. Increase L
- 2. Decrease Ts => Increase fs

Buck Cap Charge Balance - Dual of volt-second Dalance

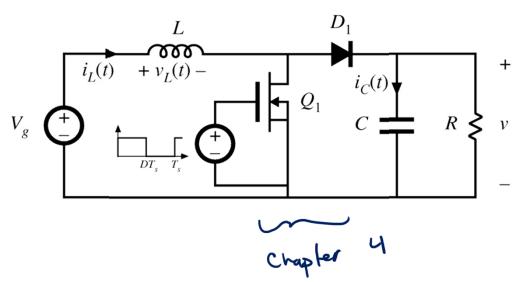


The Boost Converter

Boost converter with ideal switch



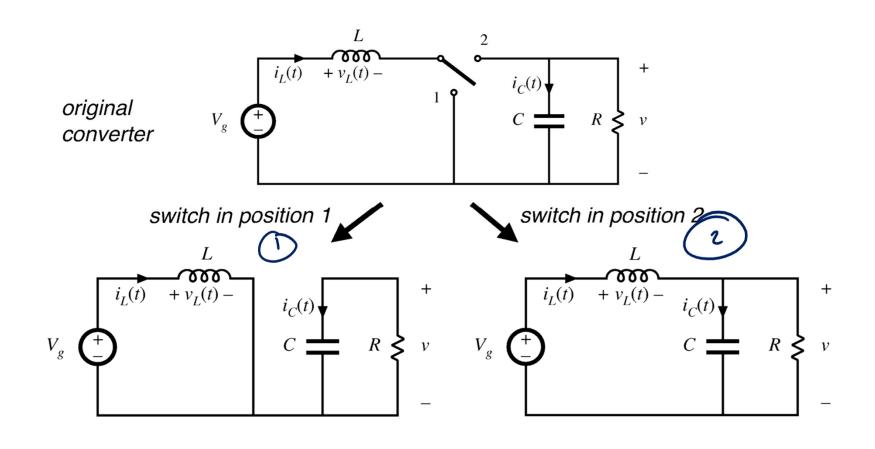
Realization using power MOSFET and diode



Fundamentals of Power Electronics

Chapter 2: Principles of steady-state converter analysis

Boost Subintervals



Fundamentals of Power Electronics

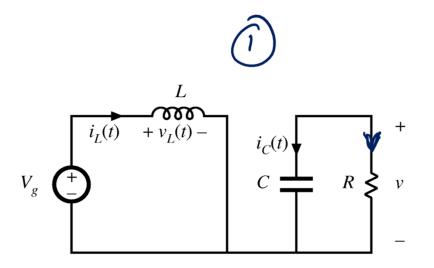
Chapter 2: Principles of steady-state converter analysis

Boost: Subinterval 1

$$i_{c}(t) = -\frac{v(t)}{R}$$

$$\int_{C} APP | y SRA$$

$$i_{c}(t) = -\frac{V}{R}$$

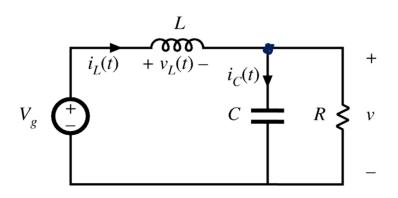


Boost: Subinterval 2

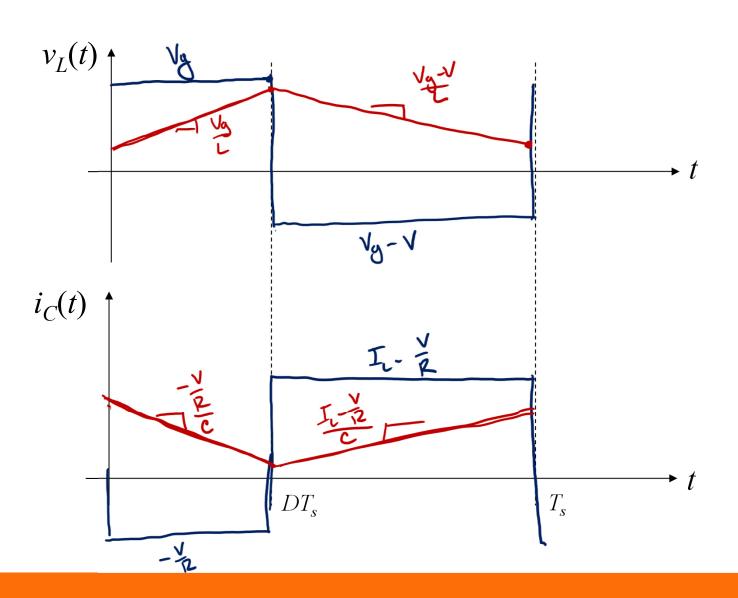
$$N_{L}(t) = V_{0} - N(t)$$

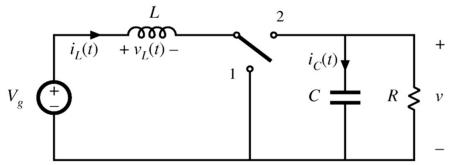
$$\int APPly SPA$$

$$N_{L}(t) = V_{0} - V$$



Waveforms





Volt-sec balance

$$\langle v_{L} \rangle |_{TS} = V_{Q} - D'V = \emptyset$$

$$V = \frac{1}{D'}V_{Q}$$

$$M = \frac{V}{V_{Q}} = \frac{1}{D'}$$

Boost: Conversion Ratio 3 M(D)2 0 0.4 0.5 0.6 0.75 0.8 0.2