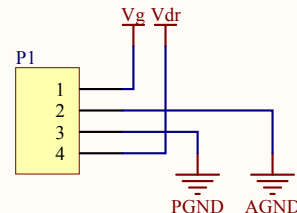
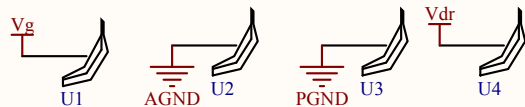


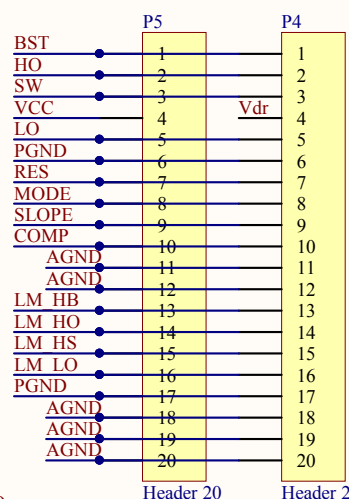
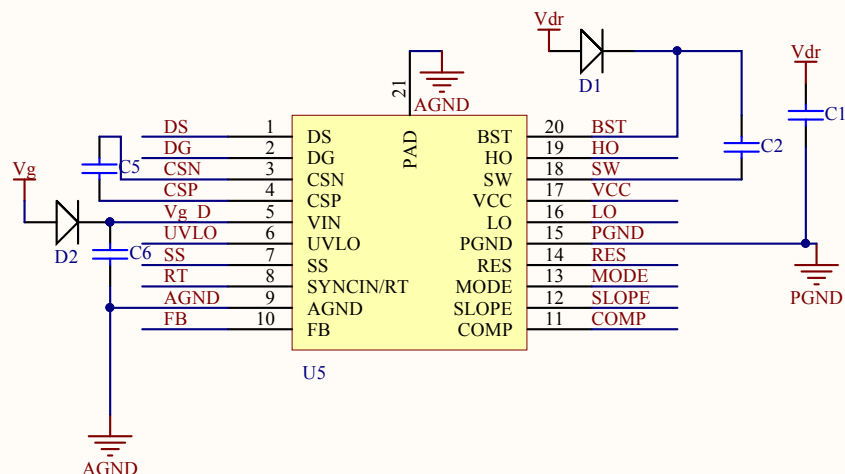
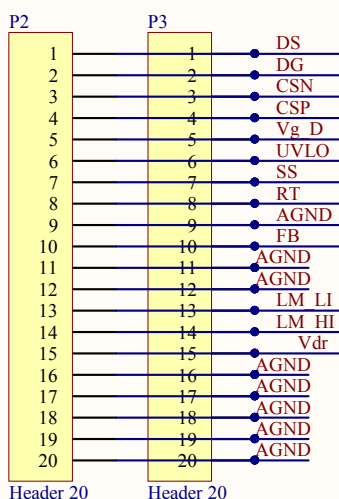
A

A



B

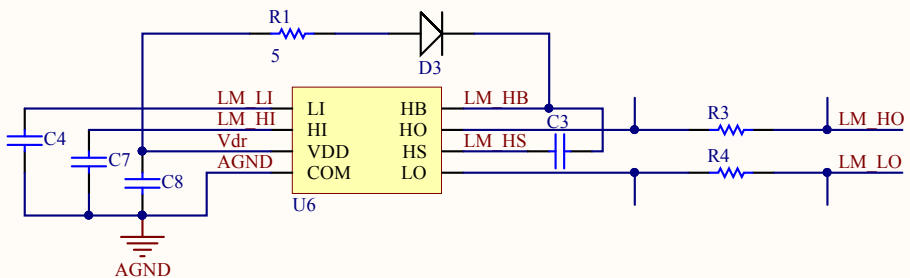
B



*Note: Vdr not connected to Vcc by default

C

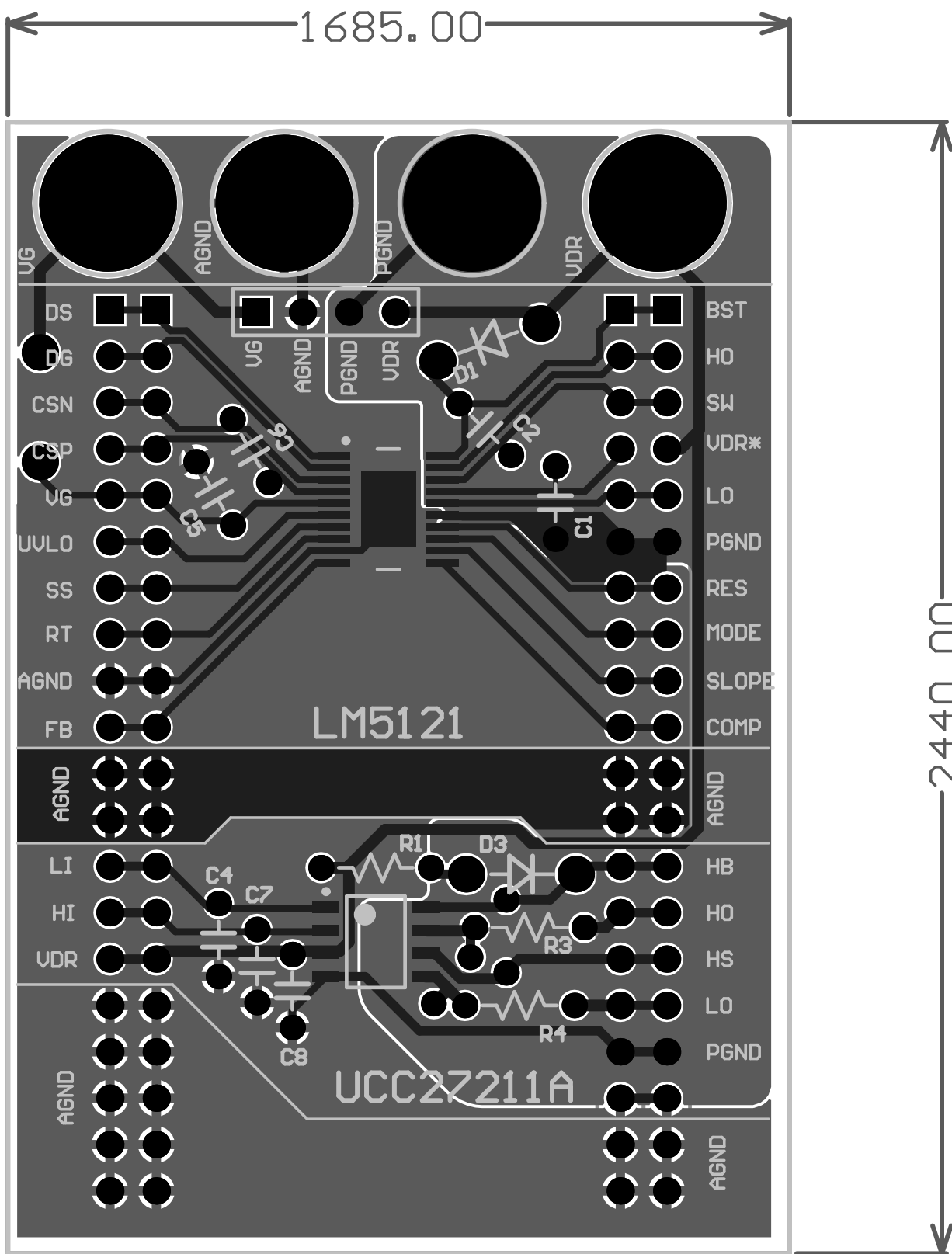
C



D

D

Title		
Size	Number	Revision
A		
Date:	1/25/2019	Sheet of
File:	T:\PCBs\...\BOB-Schem.SchDoc	Drawn By:



Comment	Description	Designator	Footprint	LibRef	Quantity
Cap	Capacitor	C1, C2, C3, C4, C5, C6, C7, C8	Axial C - Narrow	Cap	8
Diode		D1, D2, D3	Axial D	Diode	3
Header 4	Header, 4-Pin	P1	HDR1X4	Header 4	1
Header 20	Header, 20-Pin	P2, P3, P4, P5	HDR1X20	Header 20	4
Res Semi	Semiconductor Resistor	R1, R3, R4	Axial R - Narrow	Res Semi	3
Banana Jack	575-8	U1, U2, U3, U4	Banana Jack	Banana Jack	4
LM5121	V/I Boost Controller With Disconnect	U5	LM5121	LM5121	1
UCC27712	UCC27712	U6	UCC27712	UCC27712	1