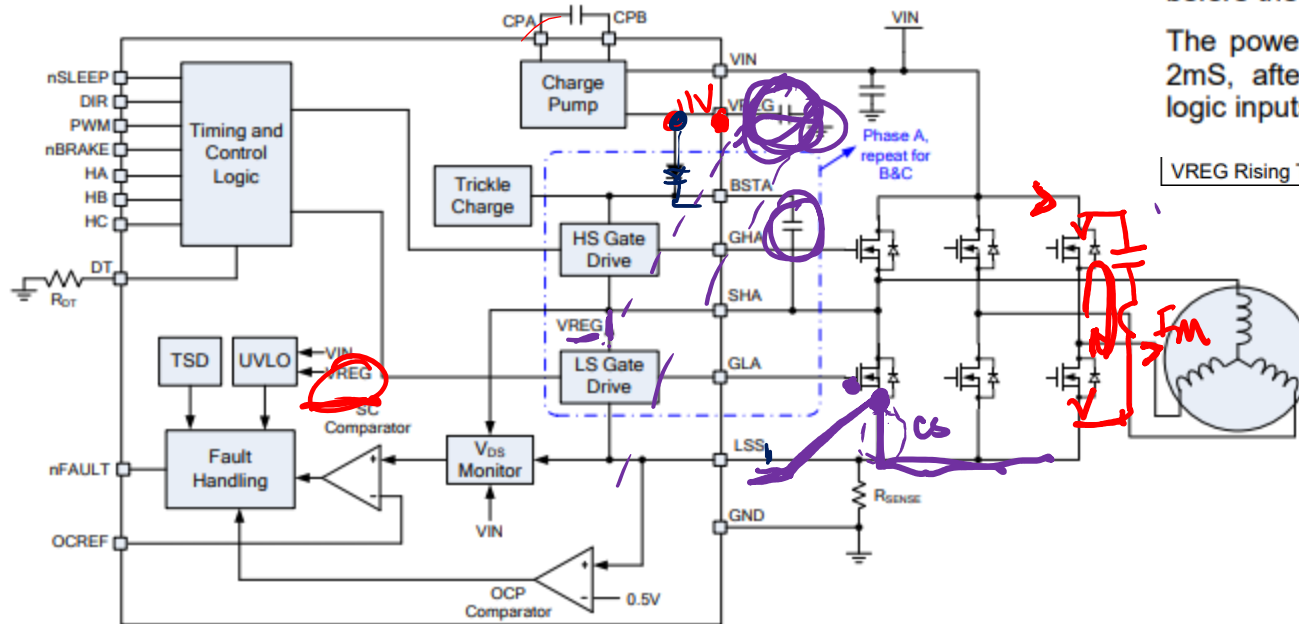


Power-Up Sequence

The power-up sequence is initiated by the application of voltage to VIN pin. To initiate power-up, VIN must be above the undervoltage lockout threshold V_{UVLO} .

After power-up begins, the VREG supply starts operating. VREG must rise above V_{REG_RISE} before the device becomes functional.

The power-up process takes between 1mS and 2mS, after which the MP6532 will respond to logic inputs and drive the outputs.



VREG Rising Threshold	V_{REG_RISE}	6.8	7.6	8.4	V

Figure 1: Functional Block Diagram

