

Announcements

- PCBs ordered
 - EECS ordering will contact you directly when parts/PCBs arrive
 - FPGA / Interface boards will be made available today
 - May require some population/testing
- TNvoice available
 - <https://utk.campuslabs.com/eval-home/>
 - Currently, 2/6 completed
 - Closes 12/1

Remaining Tasks

- TNVoice anonymous eval due 12/1
- Track I: PCB Testing
 - Final testing report due 12/9
 - Format template available on website
 - *short* additional narrative if results do not correspond to predictions
- Track II: Final Exam
 - Posted 11/30, Due 12/9
 - Same rules/format as midterm exam
 - Comprehensive, covering all course material

Historical Perspective



Robert D Middlebrook

PhD, Stanford, 1955

CalTech Professor, 1955-1998



Slobodan Cúk

PhD CalTech, 1976

CalTech Prof, 1977-1999

Modelling, analysis, and design of switching converters

Model a switched system as an averaged, time-invariant system with

$$\dot{x}(t) = Ax(t) + Bu(t)$$

where

$$A = DA_1 + D'A_2$$

$$B = DB_1 + D'B_2$$



Dennis John Packard

PhD, CalTech 1976

Discrete modeling and analysis of switching regulators

Model a switched system as a discrete-time system with

$$x[n + 1] = \Phi x[n] + \Psi U[n]$$

where

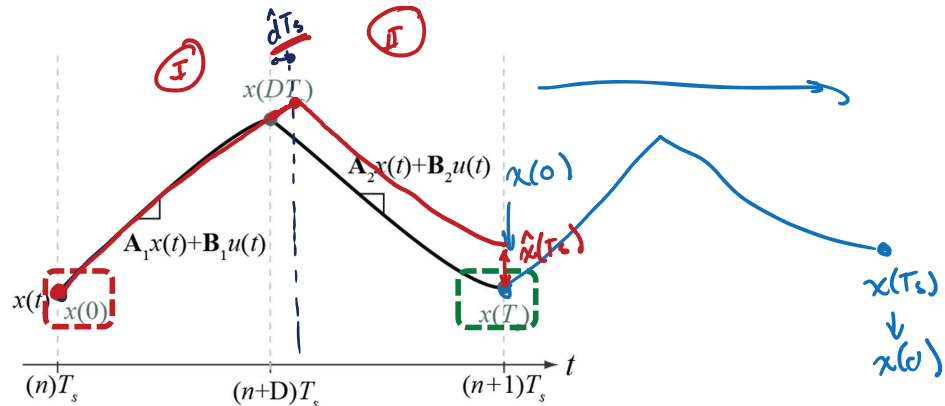
$$\Phi = \left(\prod_{i=n_{sw}}^1 e^{A_i t_i} \right)$$

$$\Psi = \sum_{i=1}^{n_{sw}} \left\{ \left(\prod_{k=n_{sw}}^{i+1} e^{A_k t_k} \right) A_i^{-1} (e^{A_i t_i} - I) B_i \right\}$$

A. R. Brown and R. D. Middlebrook, "Sampled-data Modeling of Switching Regulators" PESC 1981



Application to Switching Converter



$$x(DT_s) = e^{A_1 D T_s} x(0) + A_1^{-1} (e^{A_1 D T_s} - I) B_1 U$$

$$x(T_s) = e^{A_2 D' T_s} x(DT_s) + A_2^{-1} (e^{A_2 D' T_s} - I) B_2 U$$

$$\boxed{x(T_s)} = e^{A_2 D' T_s} e^{A_1 D T_s} \boxed{x(0)} + A_2^{-1} (e^{A_2 D' T_s} - I) B_2 U + e^{A_2 D' T_s} A_1^{-1} (e^{A_1 D T_s} - I) B_1 U$$



General Form

Generally, for n_{sw} separate switching positions

$$x(T_s) = \left(\prod_{i=n_{sw}}^1 e^{A_i t_i} \right) x(0) + \sum_{i=1}^{n_{sw}} \left\{ \left(\prod_{k=n_{sw}}^{i+1} e^{A_k t_k} \right) A_i^{-1} (e^{A_i t_i} - I) B_i \right\} U$$

Equation is in the form of a discrete-time system with

$$x[n+1] = \Phi x[n] + \Psi U[n] \leftarrow \text{LTE DT system}$$

Again, the effect of changing modulation (i.e. t_i) is hidden in nonlinear terms

$$\hat{x}[n+1] = \Phi \hat{x}[n] + \Psi \hat{u}[n] + \Gamma \hat{d}[n]$$

Find Γ by small-signal modeling

Steady-State Large-Signal Analysis

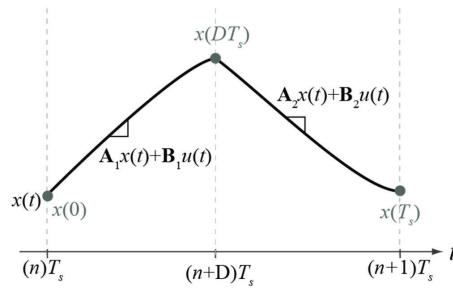
$$x(T_s) = \left(\prod_{i=n_{sw}}^1 e^{A_i t_i} \right) x(0) + \sum_{i=1}^{n_{sw}} \left\{ \left(\prod_{k=n_{sw}}^{i+1} e^{A_k t_k} \right) A_i^{-1} (e^{A_i t_i} - I) B_i \right\} U$$

In steady-state, $x(T_s) = x(0)$

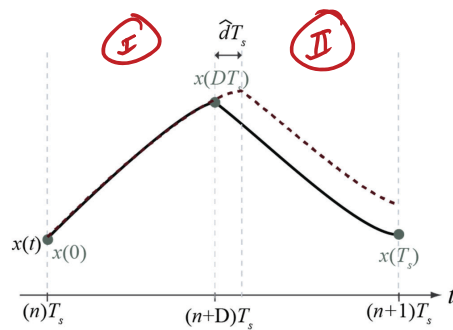
$$x(T_s) = \left(I - \prod_{i=n_{sw}}^1 e^{A_i t_i} \right)^{-1} \sum_{i=1}^{n_{sw}} \left\{ \left(\prod_{k=n_{sw}}^{i+1} e^{A_k t_k} \right) A_i^{-1} (e^{A_i t_i} - I) B_i \right\} U$$

Gives explicit solution for steady-state operation of any switching circuit

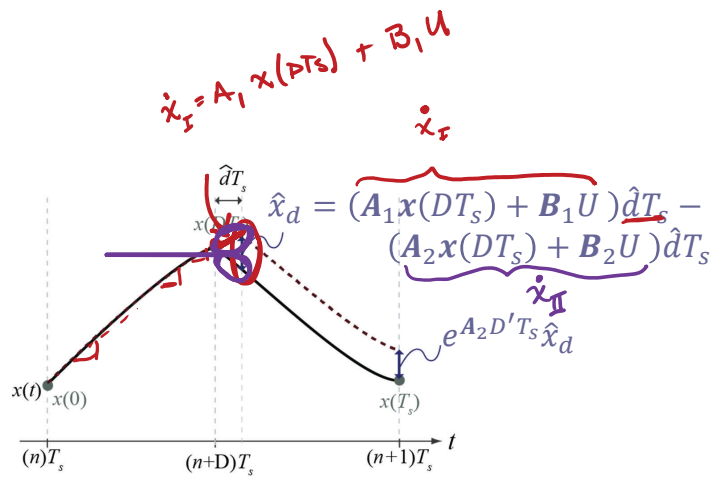
Small Signal Modeling



Small Signal Modeling



Small Signal Modeling



Complete Small Signal Model

This completes the small-signal model

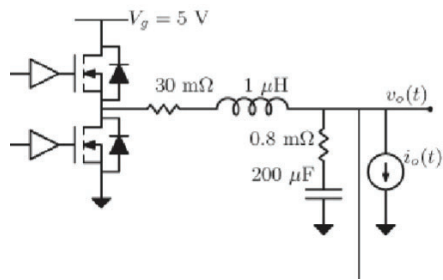
$$\hat{\mathbf{x}}[n+1] = \mathbf{\Phi} \hat{\mathbf{x}}[n] + \mathbf{\Psi} \hat{u}[n] + \mathbf{\Gamma} \hat{d}[n]$$

where

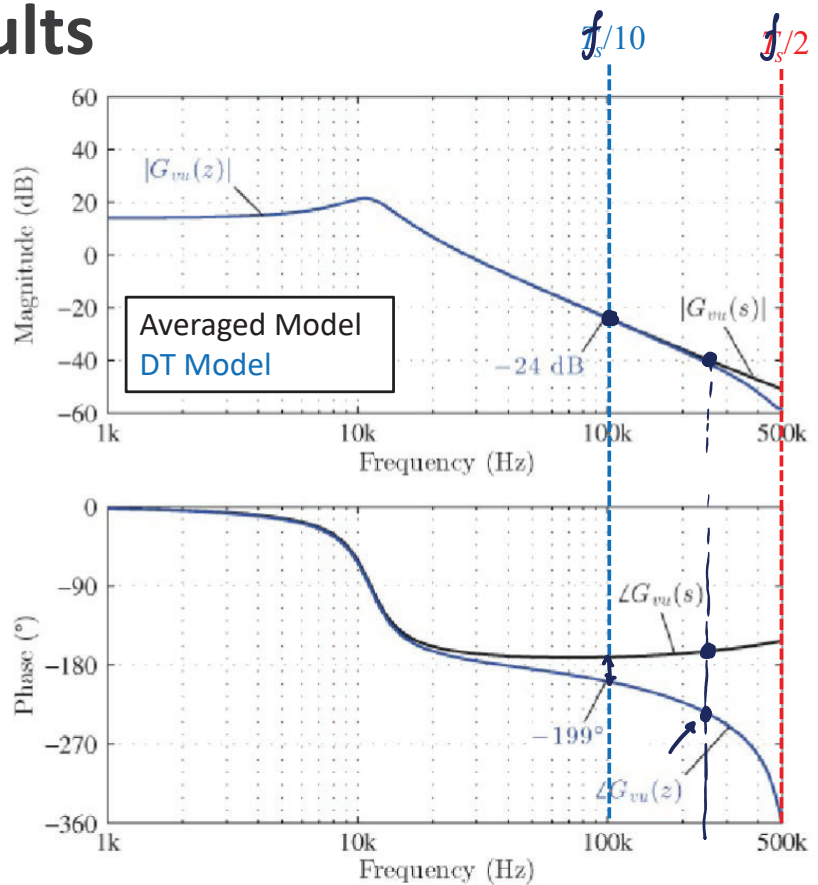
$$\mathbf{\Gamma} = e^{A_2 D' T_s} \left((A_1 - A_2) \overset{\text{purple arrow}}{\underset{x(DTs) \text{ in steady-state}}{X_D}} + (B_1 - B_2) U \right) T_s$$

with $X_D = x(DT_s)$ in steady-state

Example Results



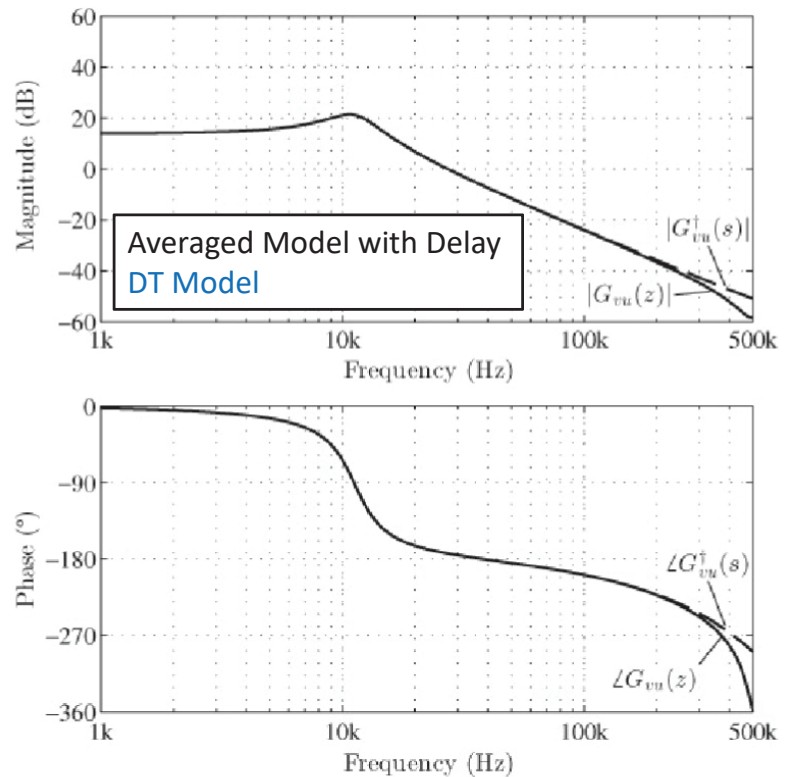
* Includes $t_d=760\text{ns}$ of delay in feedback loop



L. Corradini et. al. *Digital Control of High Frequency Switched-Mode Power Converters*, Section 3.2

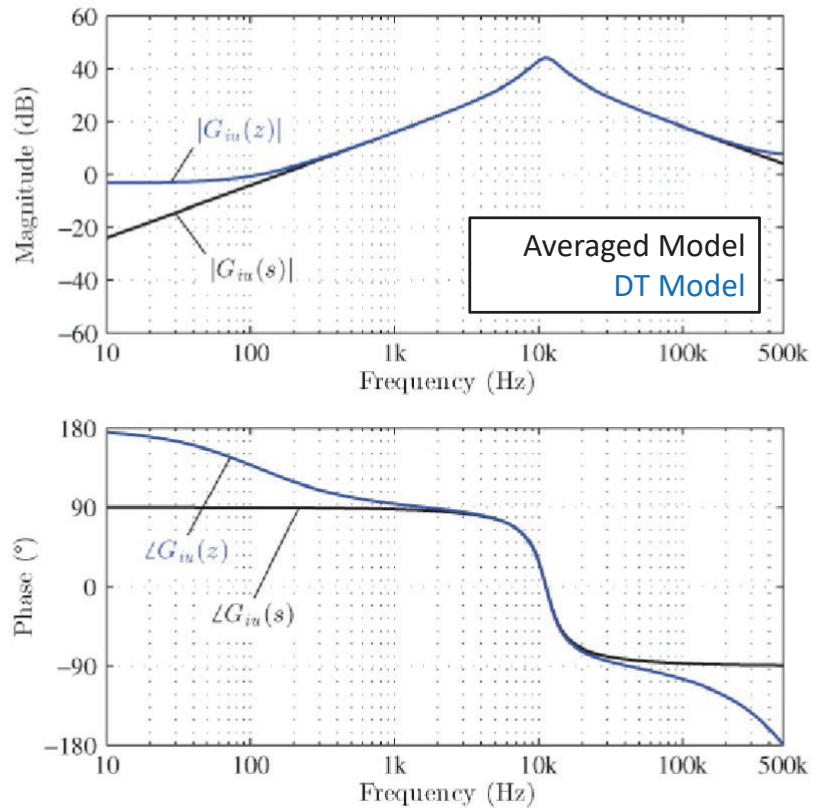
Inclusion of Delay

$$G_{vu}^+(s) = G_{vu}(s)e^{-st_d}$$



L. Corradini et. al. *Digital Control of High Frequency Switched-Mode Power Converters*, Section 3.2

Current Control



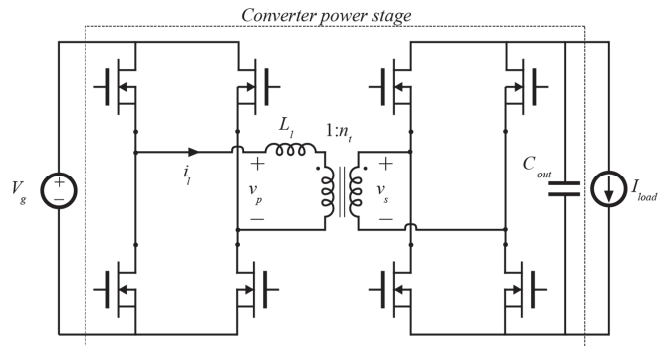
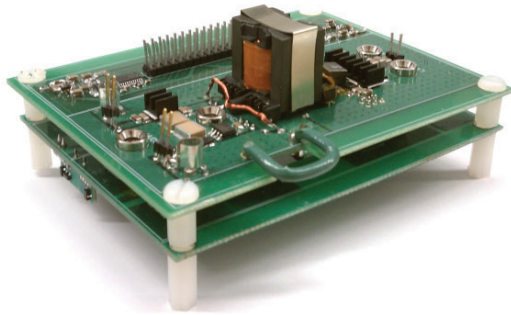
L. Corradini et. al. *Digital Control of High Frequency Switched-Mode Power Converters*, Section 3.2

Discrete Time Analysis: Results

$$X_{ss} = \left(I - \prod_{i=n_{sw}}^1 e^{A_i t_i} \right)^{-1} \sum_{i=1}^{n_{sw}} \left\{ \left(\prod_{k=n_{sw}}^{i+1} e^{A_k t_k} \right) A_i^{-1} (e^{A_i t_i} - I) B_i \right\} U_i$$

- Valid for any switched circuit, as long as
 1. Inputs, U , are constant or slowly varying
 2. All times t_i are known
 3. Every subinterval can be described by a linear circuit
- Requires no dedicated analysis other than finding A_i and B_i
- Decisively **not** a design-oriented equation

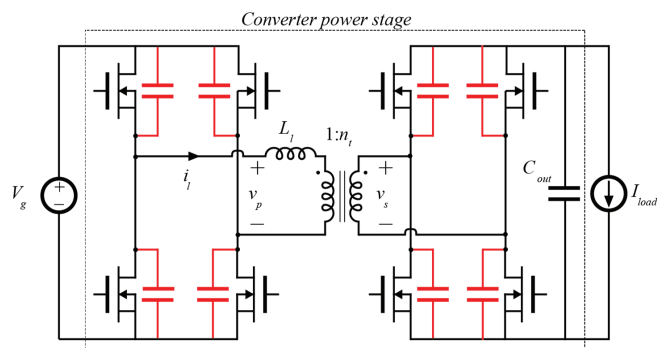
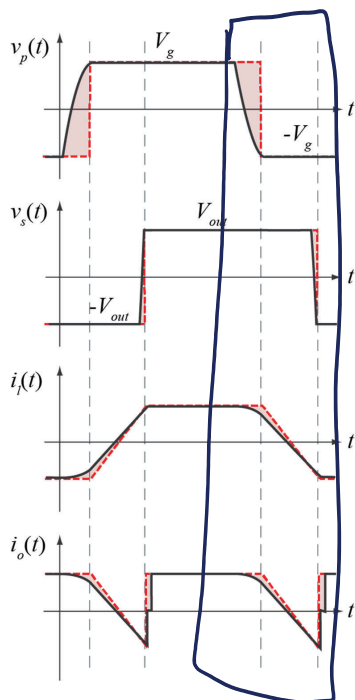
Example: DAB Design Using Dedicated Analysis



- Design of a high step-down DAB for Data Centers
- 150-to-12V, 120 W, 1MHz, design
- Prototype achieved 98.4% peak efficiency

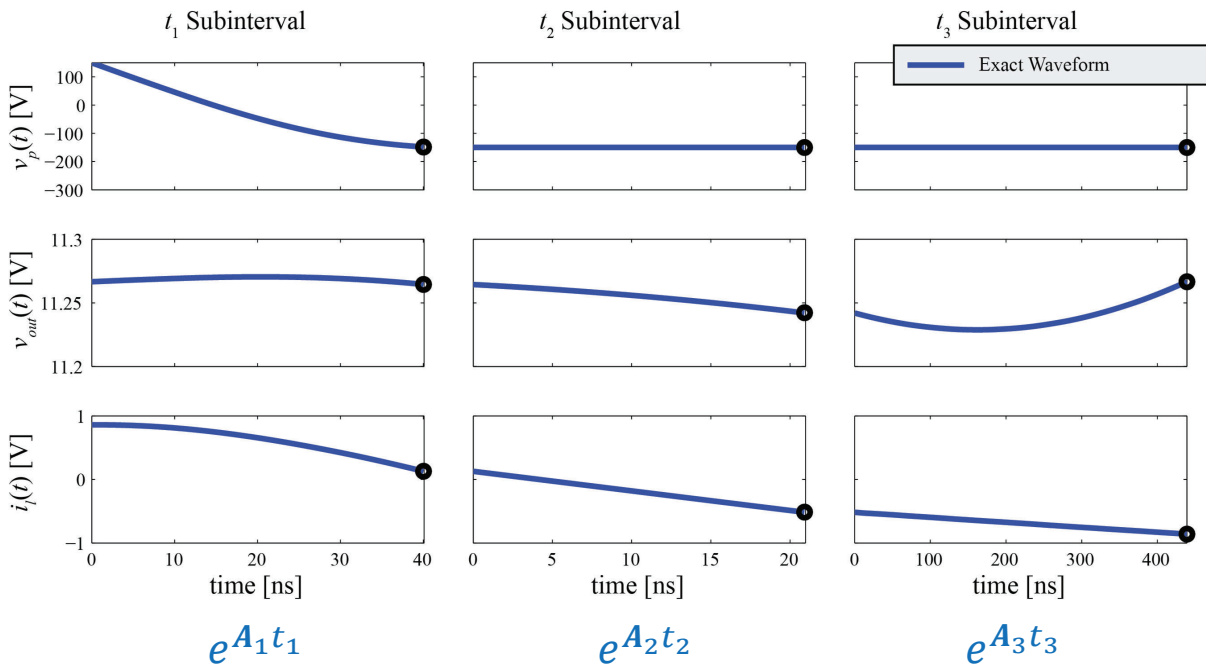
D Costinett, D. Maksimovic, and R Zane, "Design and Control for High Efficiency in High Step-Down Dual Active Bridge Converters Operating at High Switching Frequency," *IEEE Trans. On Pwr. Elec.*, 2013

DAB Operated at High Frequency

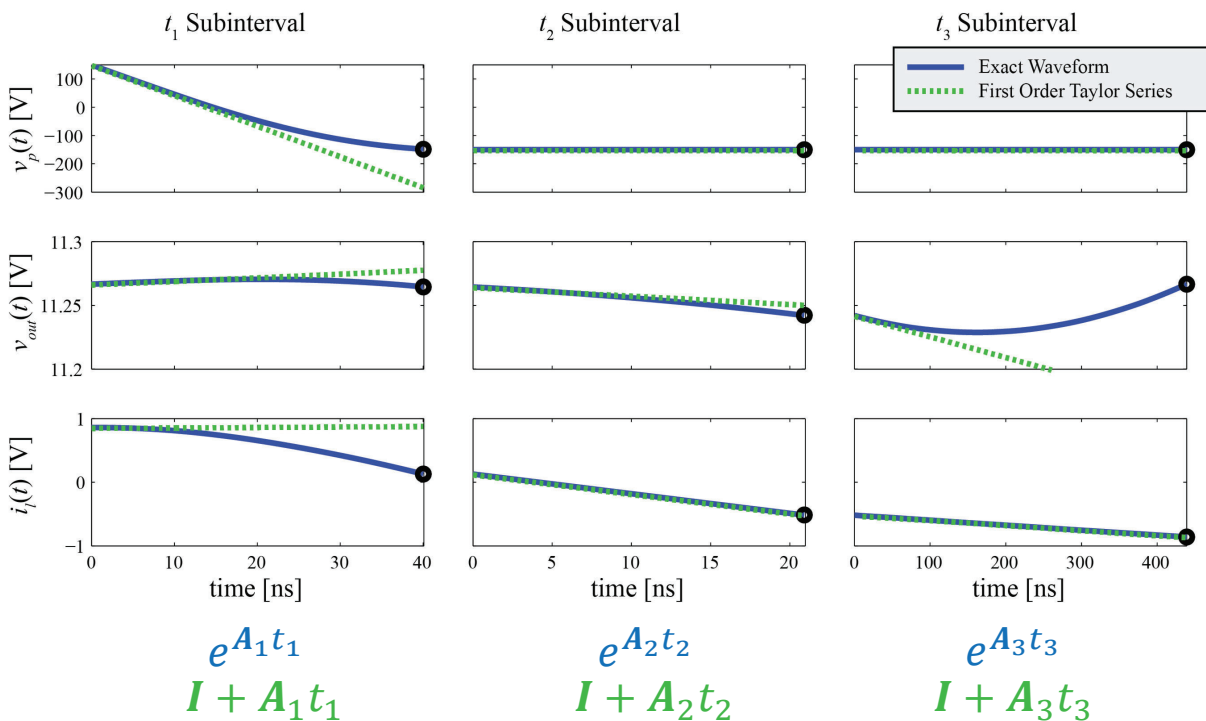


- Resonance between L_l and transistor capacitance distorts waveforms
- Resonance *may* need to be modeled when operating at high frequency

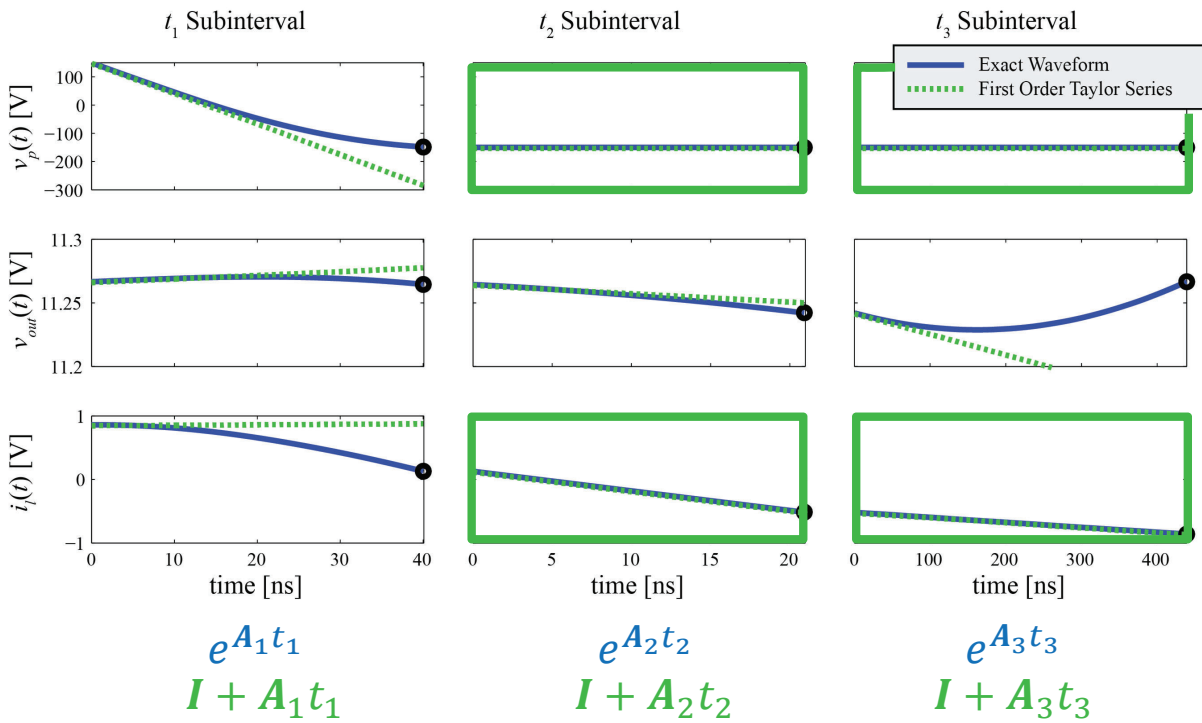
DAB Waveforms



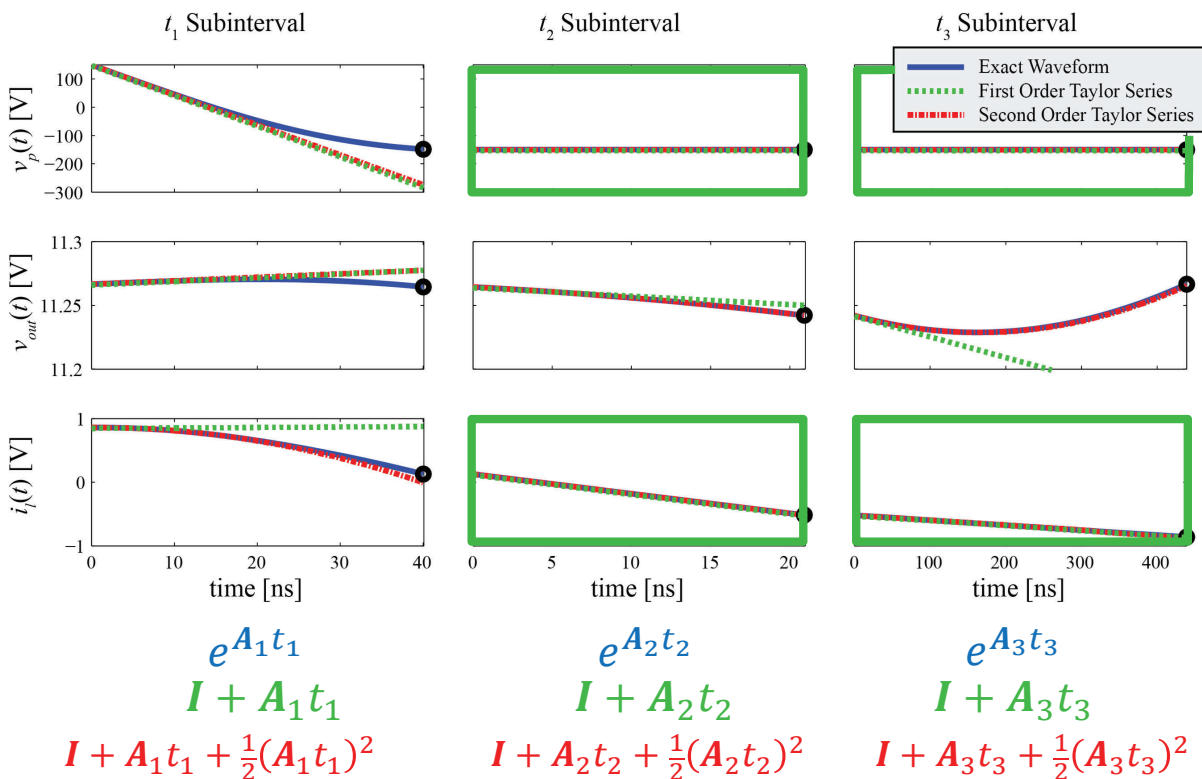
Linear Waveform Approximation



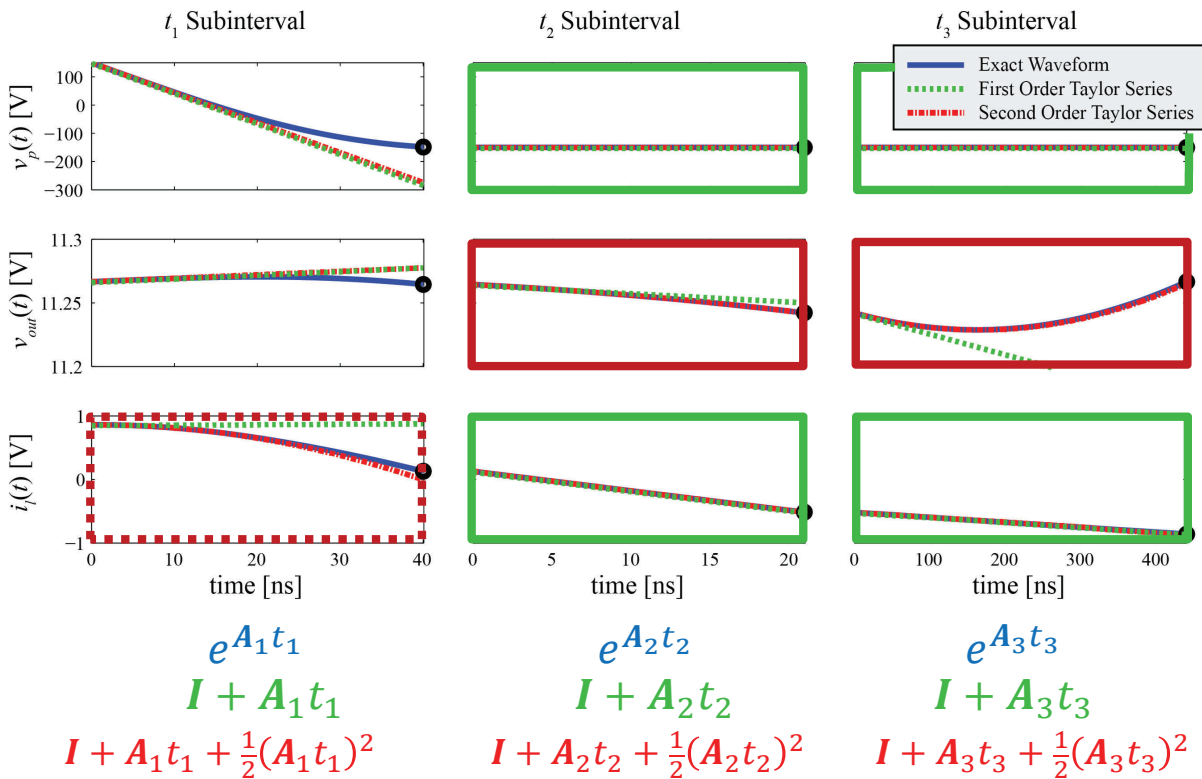
Linear Waveform Approximation



Second Order Approximation



Second Order Approximation



State Plane Solution

Solution “Read off” state plane

$$\alpha = \sin^{-1} \frac{2}{J_p}$$

Primary Dead Time

$$\beta = \frac{1}{2}(J_2 + J_1)$$

Phase Shift

$$\delta = n_t \sin^{-1} \frac{2n_t^2}{\frac{R_0'}{R_0} J_p} \sqrt{\frac{C_s}{C_p}}$$

Secondary Dead Time

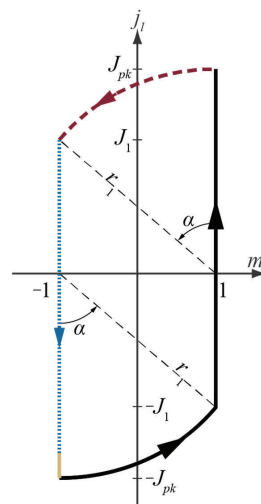
$$\zeta = \frac{F}{\pi} - \alpha - \beta - \delta$$

$$J_1 = \sqrt{J_p^2 - 4}$$

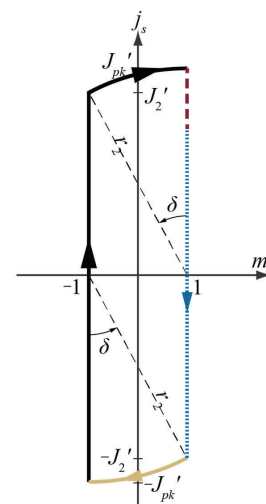
$$J_2 = \frac{R_0}{R_0'} \sqrt{\left(J_p \frac{R_0'}{R_0} \right)^2 - (2n_t)^2}$$

$$J = \frac{F}{\pi} \left[2 + \frac{1}{4}(J_1^2 - J_2^2) + J_p \left(\frac{\pi}{F} - \alpha - \beta - \delta \right) \right]$$

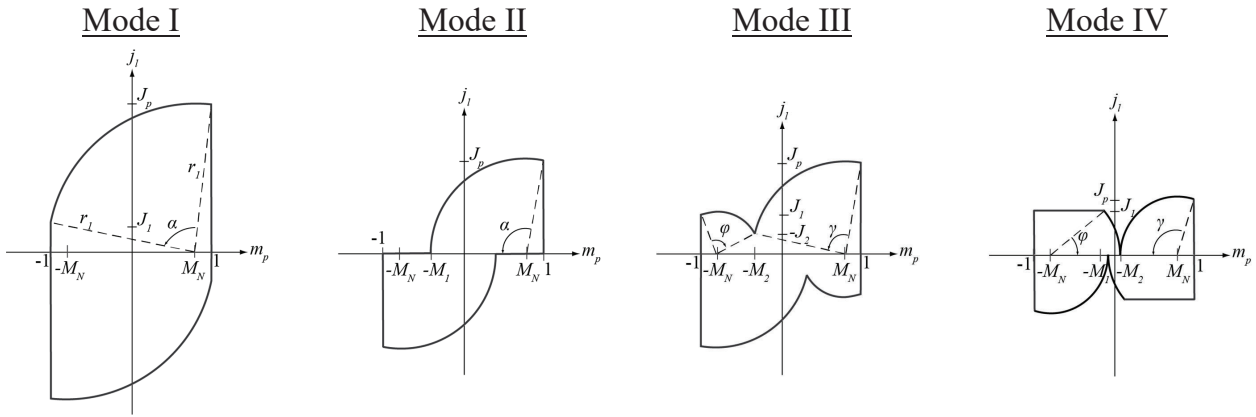
Primary



Secondary

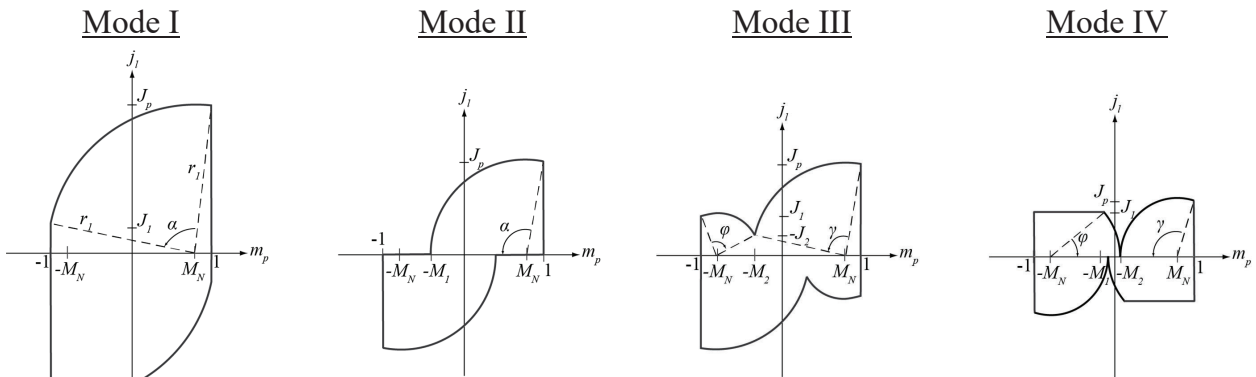


Different Operating Modes



- As control, input and load vary, operating mode changes
- In each mode, solution is a set of transcendental equations

Different Operating Modes



$$J_1 = \sqrt{J_p^2 - 4M_N}$$

$$\alpha = \cos^{-1} \left(1 - \frac{(J_p - J_1)^2 + 4}{2J_p^2 + 2(1 - M_N)^2} \right)$$

$$\beta = \frac{J_1 + J_2}{1 + M_N}$$

$$\zeta = \frac{J_p - J_2}{1 - M_N}$$

$$\frac{\pi}{F} = \alpha + \beta + \delta$$

$$J = \frac{\langle i_o \rangle}{I_{base}} = \frac{F}{n_t \pi} \left(2 + \frac{J_p + J_2}{2} \zeta + \frac{J_1 - J_2}{2} \beta \right)$$

$$M_1 = \sqrt{J_p^2 + (1 - M_N)^2} - M_N$$

$$\alpha = \cos^{-1} \left(1 - \frac{J_p^2 + (1 + M_1)^2}{2(M_1 - M_N)^2} \right)$$

$$J = \frac{\langle i_o \rangle}{I_{base}} = \frac{F}{n_t \pi} \left(1 + M_1 + \frac{J_p + J_2}{2} \zeta - \frac{J_2}{2} \beta \right)$$

$$\beta = \frac{J_1 + J_2}{1 + M_N}$$

$$\zeta = \frac{J_p - J_2}{1 - M_N}$$

$$\frac{\pi}{F} = \alpha + \beta + \delta$$

$$\gamma = \cos^{-1} \left(1 - \frac{(J_p + J_2)^2 + (1 + M_2)^2}{2(1 - M_N)^2 + 2J_p^2} \right)$$

$$\phi = \cos^{-1} \left(1 - \frac{(J_1 + J_2)^2 + (1 - M_2)^2}{2(M_N - M_2)^2 + 2J_2^2} \right)$$

$$J_2^2 = (1 - M_N)^2 + J_p^2 - (M_2 + M_N)^2$$

$$J_1^2 = -(1 - M_N)^2 + J_2^2 + (M_N - M_2)^2$$

$$\zeta = \frac{J_p + J_1}{1 - M_N}$$

$$\frac{\pi}{F} = \gamma + \phi + \zeta = \alpha + \zeta$$

$$J = \frac{\langle i_o \rangle}{I_{base}} = \frac{F}{n_t \pi} \left(2M_2 + \frac{J_p - J_1}{2} \zeta \right)$$

$$\phi = \cos^{-1} \left(1 - \frac{J_1^2 + (M_1 - M_2)^2}{2(M_N - M_2)^2} \right)$$

$$J = \frac{\langle i_o \rangle}{I_{base}} = \frac{F}{n_t \pi} \left(2M_2 + 1 - M_1 + \frac{J_p - J_1}{2} \zeta \right)$$

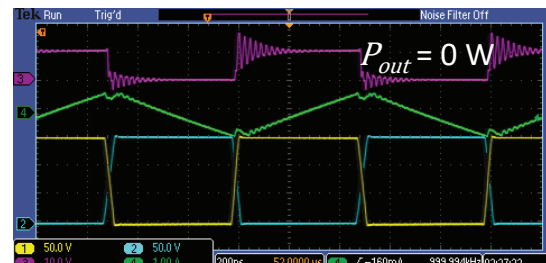
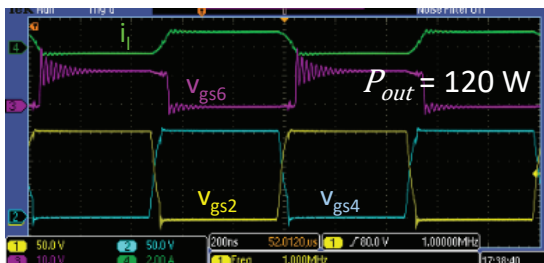
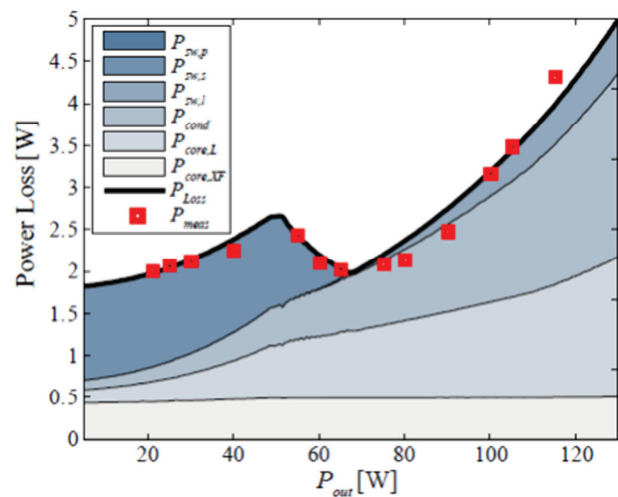
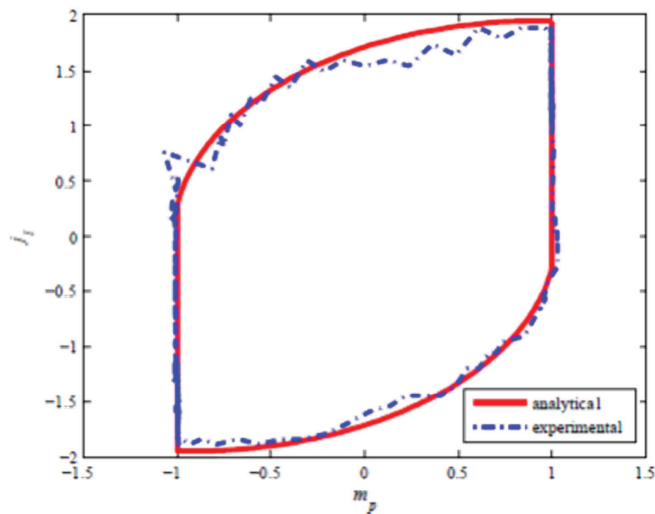
$$J_2^2 = (1 - M_N)^2 + J_p^2 - (M_2 + M_N)^2$$

$$J_1^2 = -(1 - M_N)^2 + J_2^2 + (M_N - M_2)^2$$

$$\zeta = \frac{J_p + J_1}{1 - M_N}$$

$$\frac{\pi}{F} = \gamma + \phi + \zeta = \alpha + \zeta$$

Model Validation

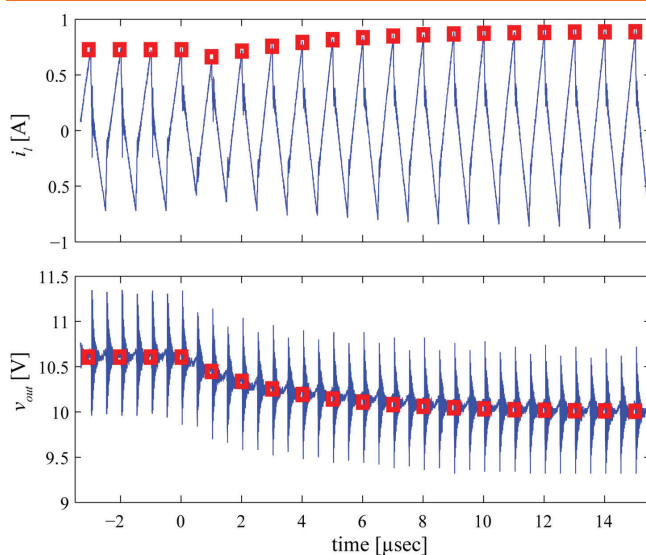


D Costinett, D. Maksimovic, and R Zane, "Design and Control for High efficiency in High Step-Down Dual Active Bridge Converters Operating at High Switching Frequency," *IEEE Trans. On Pwr. Elec.*, 2013

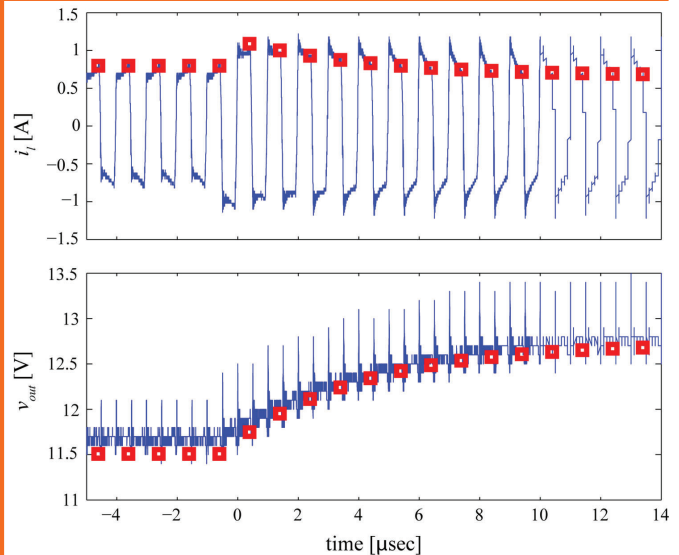
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Discrete Time Model Validation

$P_{out} = 30 \text{ W}$



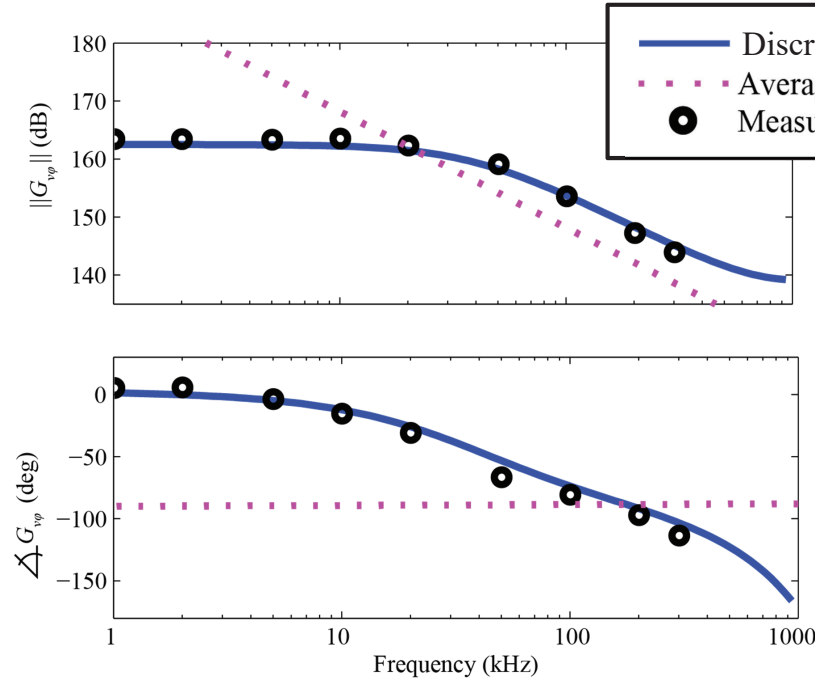
$P_{out} = 90 \text{ W}$



D. Costinett, R. Zane, and D. Maksimovic, "Discrete-time small-signal modeling of a 1 MHz efficiency-optimized dual active bridge converter with varying load," in *Proc. IEEE Workshop Contr. Modl. (COMPEL)*, june 2012, pp. 1–7.

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Discrete Time Dynamic Model Validation

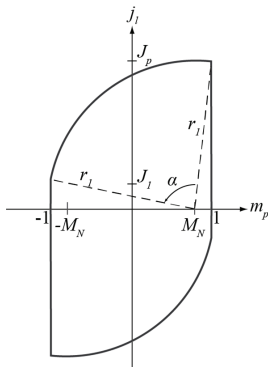


D. Costinett, R. Zane, and D. Maksimovic, "Discrete-time small-signal modeling of a 1 MHz efficiency-optimized dual active bridge converter with varying load," in Proc. IEEE Workshop Contr. Modl. (COMPEL), june 2012, pp. 1–7.

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Different Operating Modes

Mode I



$$J_1 = \sqrt{J_p^2 - 4M_N}$$

$$\alpha = \cos^{-1} \left(1 - \frac{(J_p - J_1)^2 + 4}{2(J_p^2 + 2(1 - M_N)^2)} \right)$$

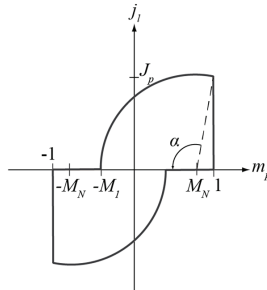
$$\beta = \frac{J_1 + J_2}{1 + M_N}$$

$$\zeta = \frac{J_p - J_2}{1 - M_N}$$

$$\frac{\pi}{F} = \alpha + \beta + \delta$$

$$J = \frac{\langle i_o \rangle}{I_{base}} = \frac{F}{n_t \pi} \left(2 + \frac{J_p + J_2}{2} \zeta + \frac{J_1 - J_2}{2} \beta \right)$$

Mode II



$$M_1 = \sqrt{J_p^2 + (1 - M_N)^2} - M_N$$

$$\alpha = \cos^{-1} \left(1 - \frac{J_p^2 + (1 + M_1)^2}{2(M_1 - M_N)^2} \right)$$

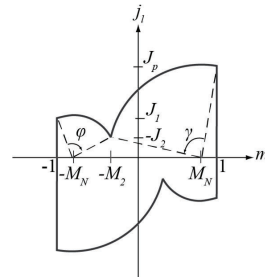
$$J = \frac{\langle i_o \rangle}{I_{base}} = \frac{F}{n_t \pi} \left(1 + M_1 + \frac{J_p + J_2}{2} \zeta - \frac{J_2}{2} \beta \right)$$

$$\beta = \frac{J_1 + J_2}{1 + M_N}$$

$$\zeta = \frac{J_p - J_2}{1 - M_N}$$

$$\frac{\pi}{F} = \alpha + \beta + \delta$$

Mode III



$$\gamma = \cos^{-1} \left(1 - \frac{(J_p + J_2)^2 + (1 + M_2)^2}{2(1 - M_N)^2 + 2J_p^2} \right)$$

$$\phi = \cos^{-1} \left(1 - \frac{(J_1 + J_2)^2 + (1 - M_2)^2}{2(M_N - M_2)^2 + 2J_2^2} \right)$$

$$J_2^2 = (1 - M_N)^2 + J_p^2 - (M_2 + M_N)^2$$

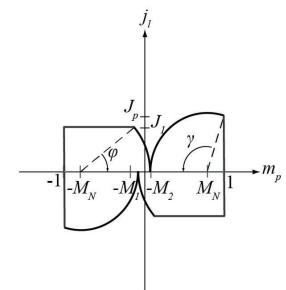
$$J_1^2 = -(1 - M_N)^2 + J_2^2 + (M_N - M_2)^2$$

$$\zeta = \frac{J_p + J_1}{1 - M_N}$$

$$\frac{\pi}{F} = \gamma + \phi + \zeta = \alpha + \zeta$$

$$J = \frac{\langle i_o \rangle}{I_{base}} = \frac{F}{n_t \pi} \left(2M_2 + \frac{J_p - J_1}{2} \zeta \right)$$

Mode IV



$$\phi = \cos^{-1} \left(1 - \frac{J_1^2 + (M_1 - M_2)^2}{2(M_N - M_2)^2} \right)$$

$$J = \frac{\langle i_o \rangle}{I_{base}} = \frac{F}{n_t \pi} \left(2M_2 + 1 - M_1 + \frac{J_p - J_1}{2} \zeta \right)$$

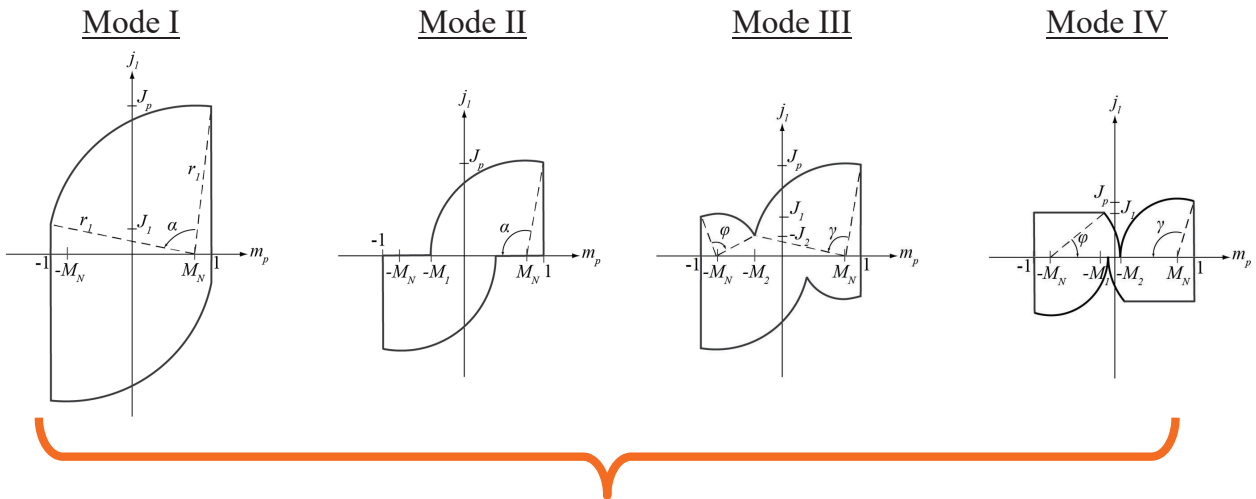
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$$J_1^2 = -(1 - M_N)^2 + J_2^2 + (M_N - M_2)^2$$

$$\zeta = \frac{J_p + J_1}{1 - M_N}$$

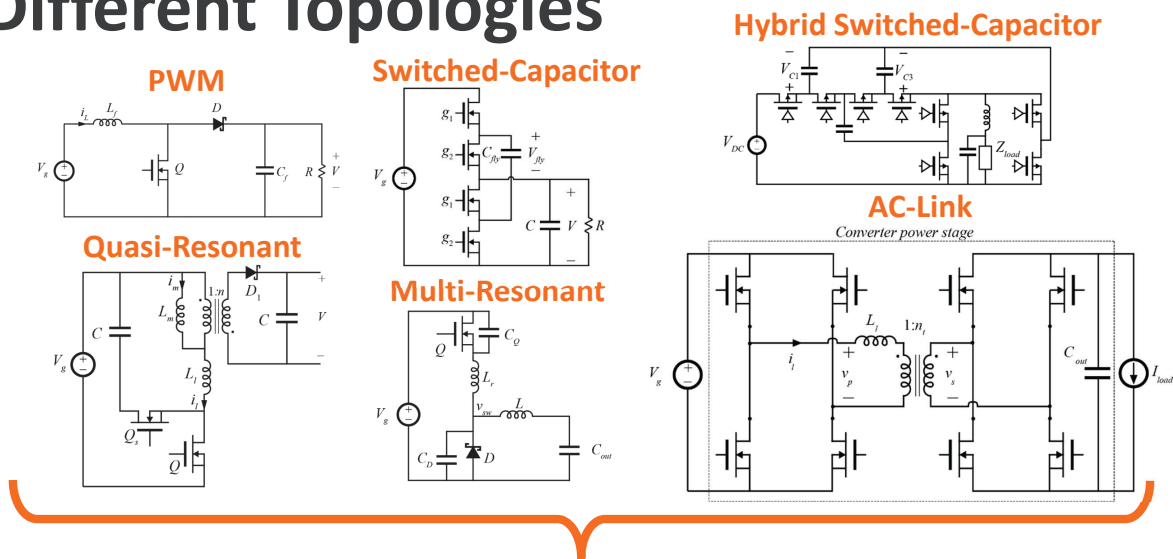
$$\frac{\pi}{F} = \gamma + \phi + \zeta = \alpha + \zeta$$

Different Operating Modes



$$X_{ss} = \left(I - \prod_{i=n_{sw}}^1 e^{A_i t_i} \right)^{-1} \sum_{i=1}^{n_{sw}} \left\{ \left(\prod_{k=n_{sw}}^{i+1} e^{A_k t_k} \right) A_i^{-1} (e^{A_i t_i} - I) B_i \right\} U$$

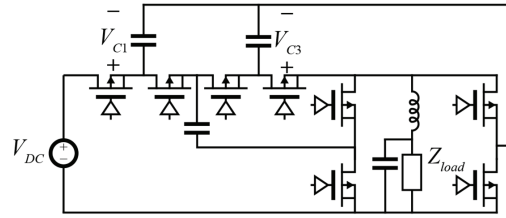
Different Topologies



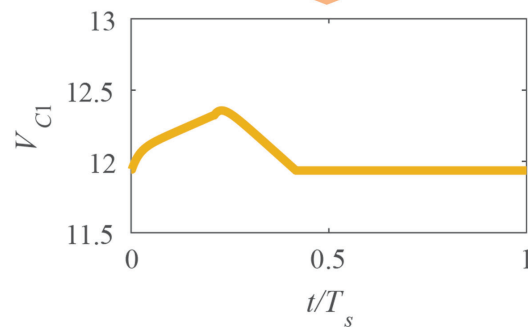
$$X_{ss} = \left(I - \prod_{i=n_{sw}}^1 e^{A_i t_i} \right)^{-1} \sum_{i=1}^{n_{sw}} \left\{ \left(\prod_{k=n_{sw}}^{i+1} e^{A_k t_k} \right) A_i^{-1} (e^{A_i t_i} - I) B_i \right\} U$$

Numerical Approach: HDSC Example

- 4:1 Hybrid Dickson Switched-Capacitor Converter
- 48-to-5 V, 0-100 A output
- Including C_{oss} , 13 states, 3 subintervals

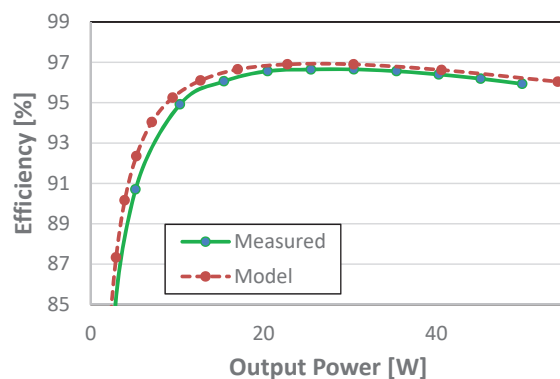
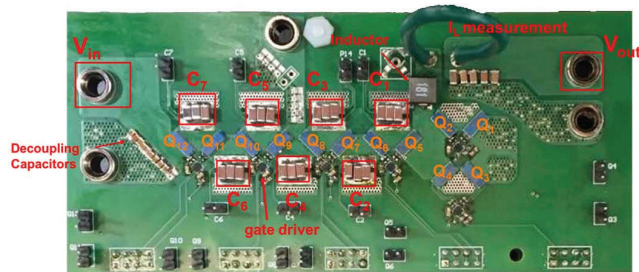


$$X_{ss} = \left(I - \prod_{i=n_{sw}}^1 e^{A_i t_i} \right)^{-1} \sum_{i=1}^{n_{sw}} \left\{ \left(\prod_{k=n_{sw}}^{i+1} e^{A_k t_k} \right) A_i^{-1} (e^{A_i t_i} - I) B_i \right\} U$$



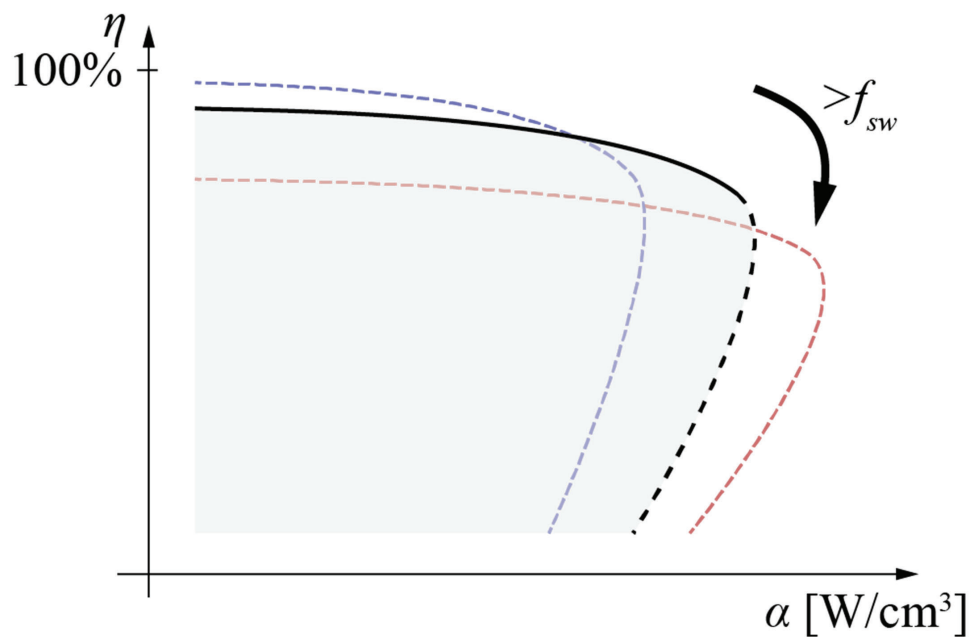
Model Validation

- Constructed 8:1 HDSC converter
 - Measured 96.7% peak efficiency at 30W
 - Model predicts 96.9% at 30.4W
- Model includes capacitor ESR



COURSE CONCLUSIONS

HF Power Electronics – When and Why



Thank you for all your hard work, and good luck
with finals!