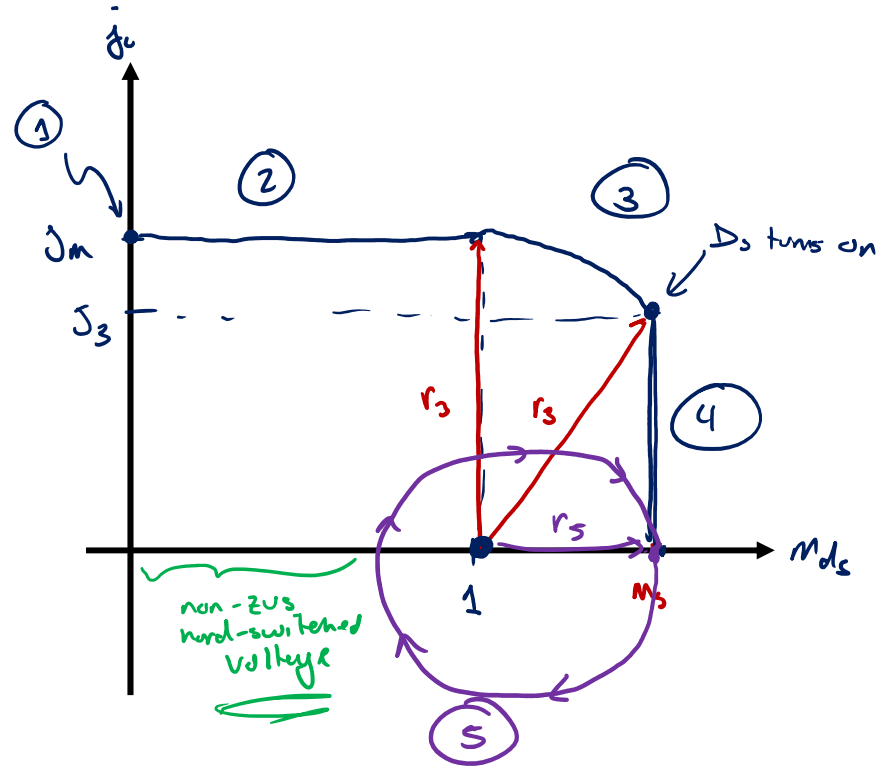
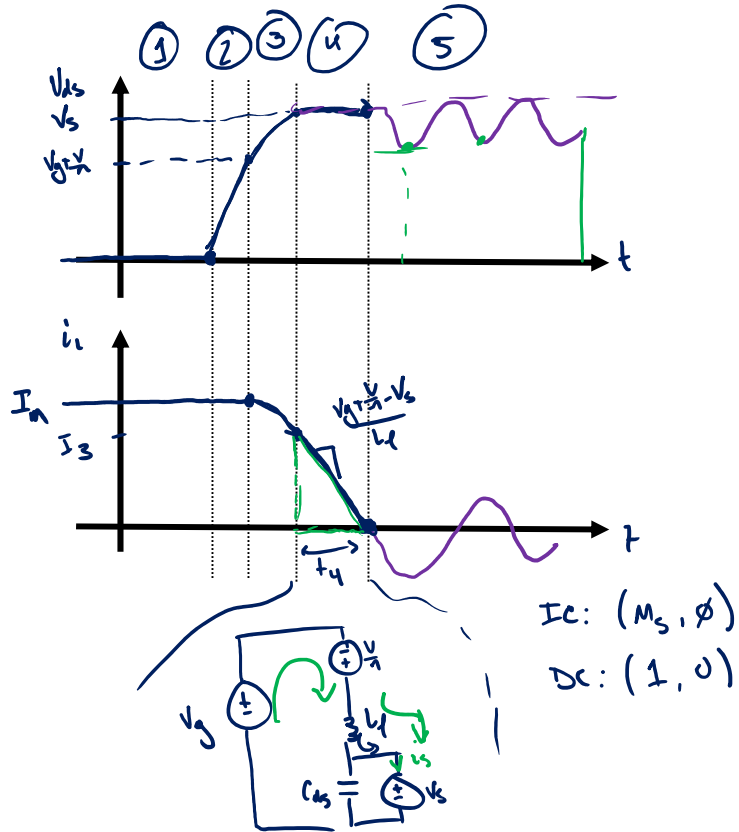
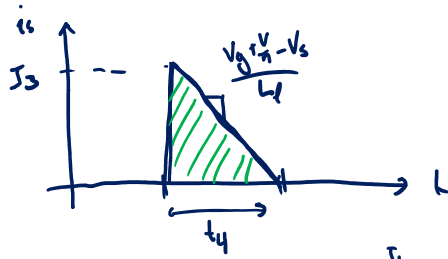


Clamped State Plane



Loss Comparison



$$P_s = E_s f_s = f_s \int_0^{t_4} V_s \cdot i_s dt = f_s V_s \int_0^{t_4} i_s dt$$

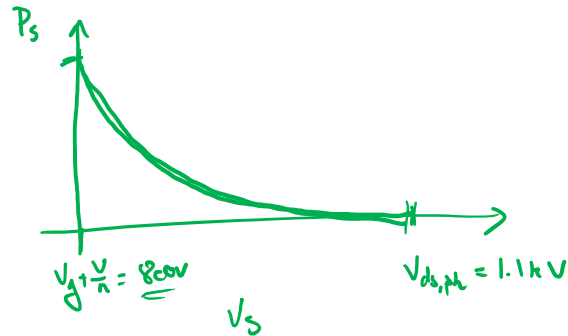
$$= f_s V_s \frac{1}{2} t_4 I_3 = f_s V_s \frac{1}{2} I_3 \left[\frac{I_3 t_4}{V_g + \frac{V}{n} - V_s} \right]$$

Plug in from previous design with $V_s = 850V$

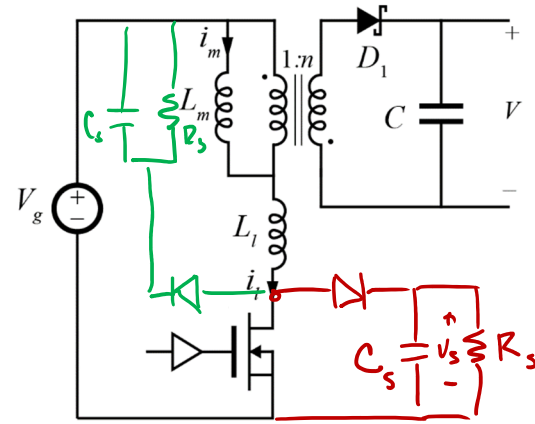
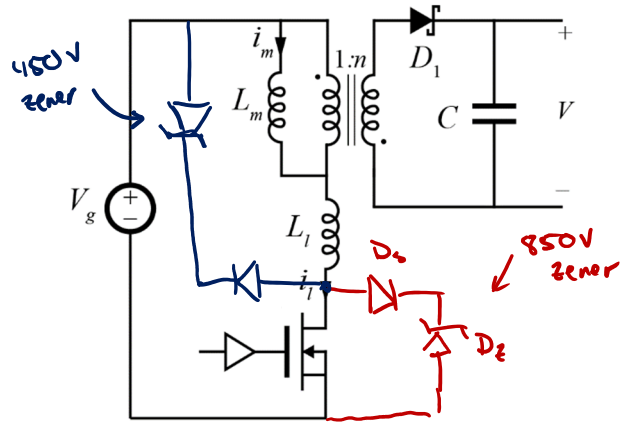
$$E_s = 72 \mu s$$

$$P_s = 72W \rightarrow \text{Huge!}$$

$$I_3 = \sqrt{I_m^2 - (m_s - 1)^2}$$



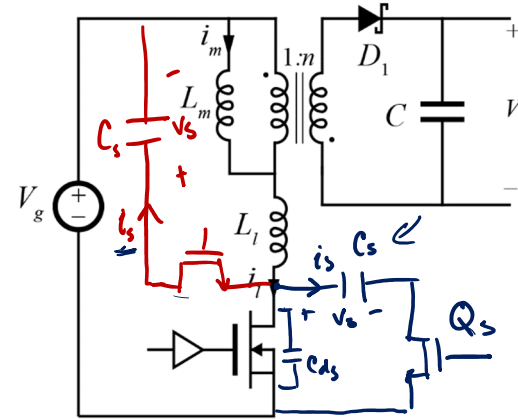
Snubber Implementations



$C_s \rightarrow$ large filter element

$$P_s = \frac{V_s^2}{R_s}$$

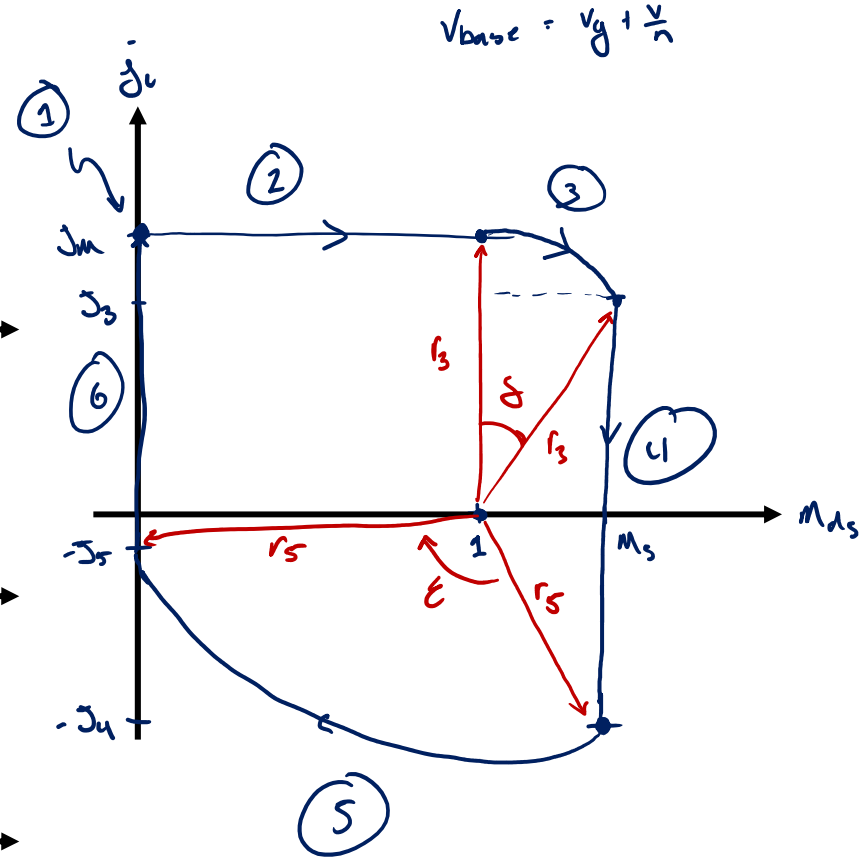
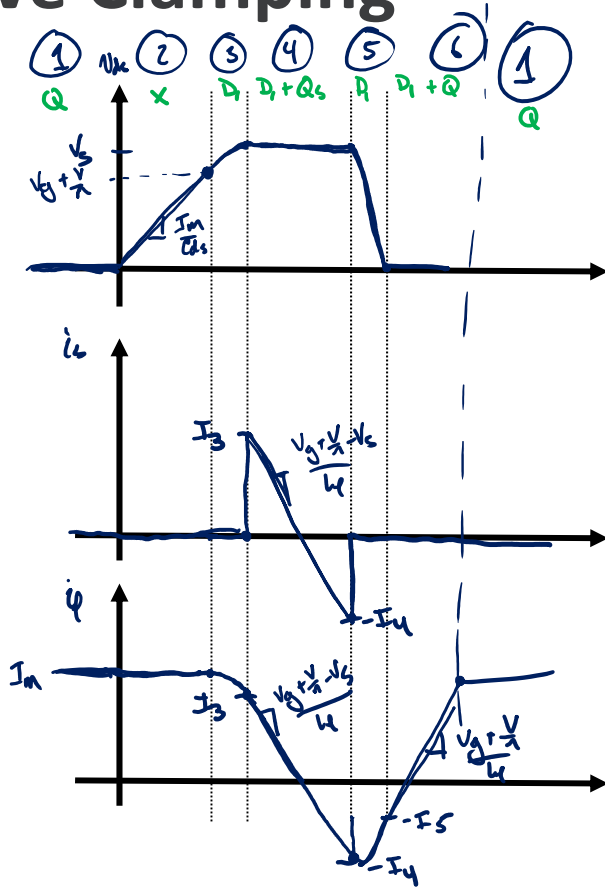
Active Clamping



C_s (& L_m & C) \rightarrow large filter elements

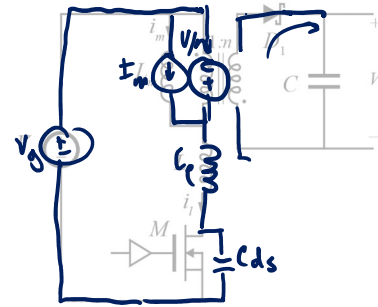
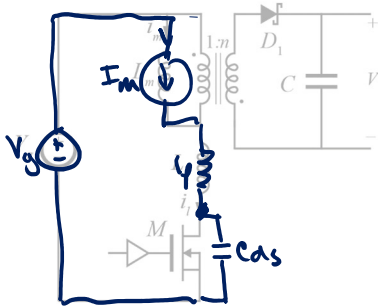
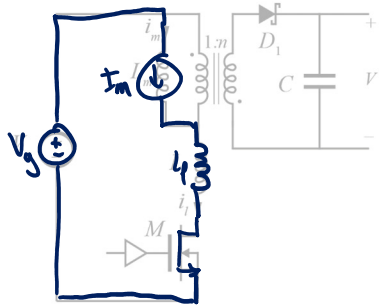
Active Clamping

Conducting device

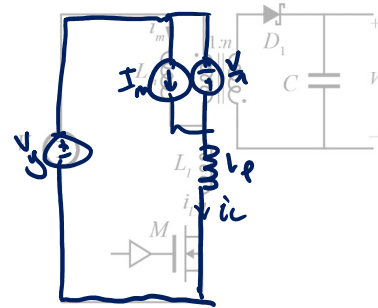
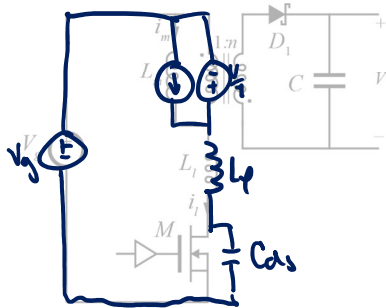
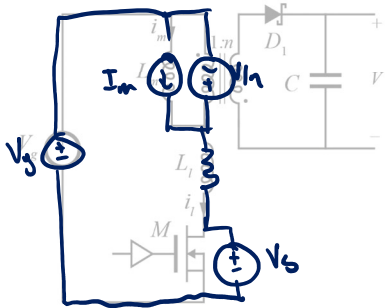


Subinterval Equivalent Circuits

① → Q_1 off → ② → D_1 turns on → ③ → turn on Q_2



④ → shut off Q_2 → ⑤ → turn on Main FET → ⑥ → D_1 off →



ZVS Conditions

3

$$r_3 \geq m_s - 1$$

$$\boxed{D_m \geq m_s - 1}$$

→

Only violated
clamping

$$V_{ds,pt} < V_s \rightarrow$$

No need for

5

ZVS of Q

$$r_5 \geq 1$$

$$\boxed{\sqrt{D_4^2 + (1 - m_s)^2} \geq 1}$$

Analysis: Intervals 1-3

① No equation from state plane or time domain

$$T_3 = t_1 + t_2 + \dots + t_6$$

$$\frac{2\pi}{F} = \theta_1 + \theta_2 + \theta_3 + \theta_4 + \theta_5 + \theta_6$$

② $\left(\frac{1}{I_{base}}\right) \frac{I_m}{C_d s} t_2 = V_g + \frac{V}{n} \left(\frac{1}{I_{base}}\right)$

$$I_m \frac{t_2}{C_d s} = 1 \cdot R_o$$

$$I_m t_2 \frac{1}{C_d s R_o} = 1 \rightarrow$$

$$I_m \theta_2 = 1$$

$$\frac{1}{C_d s R_o} = \frac{1}{C_d s \sqrt{L_e C_d}} = \frac{1}{\sqrt{L_e C_d}} = \omega_o$$

③

$$r_3^2 = I_m^2 = (M_3 - 1)^2 + I_3^2$$

$$\theta_3 = \tan^{-1} \left(\frac{M_3 - 1}{I_3} \right)$$

