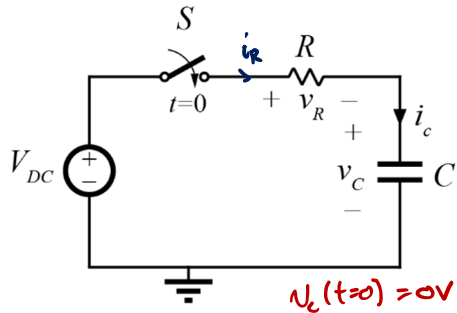


M_1 Energy Loss



$$\bar{E}_R = \int_0^{\infty} i_R v_R dt$$

$$v_R = V_{DC} - v_C$$

$$i_R = i_C = C \frac{dv_C}{dt}$$

$$\bar{E}_R = \int_0^{\infty} C \frac{dv_C}{dt} (V_{DC} - v_C) dt$$

$$E_R = \int_0^{V_{DC}} C V_{DC} dv_C - \int_0^{V_{DC}} C \cdot v_C dv_C$$

$$E_R = V_{DC} \underbrace{\int_0^{V_{DC}} C dv_C}_{Q_{tot}} - \underbrace{\int_0^{V_{DC}} C \cdot v_C dv_C}_{E_{tot}}$$

Insight

$$E_R = V_{DC} (V_{DC} C_{eq}) - \frac{1}{2} C_{eq} V_{DC}^2$$

Total Half Bridge C_{oss} Loss

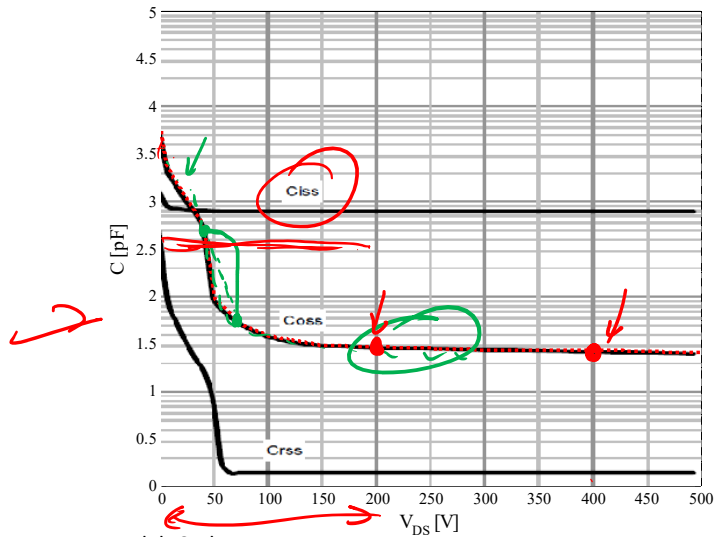
$$E_{total} = \frac{1}{2} C_{eq, E-M2} V_{DC}^2 + V_{DC}^2 C_{eq, Q-M1} - \frac{1}{2} C_{eq, E-M1} V_{DC}^2$$

If M_1 & M_2 are identical

$$E_{total} = V_{DC}^2 C_{eq, Q}$$

then energy loss depends only on Q -equiv capacitance

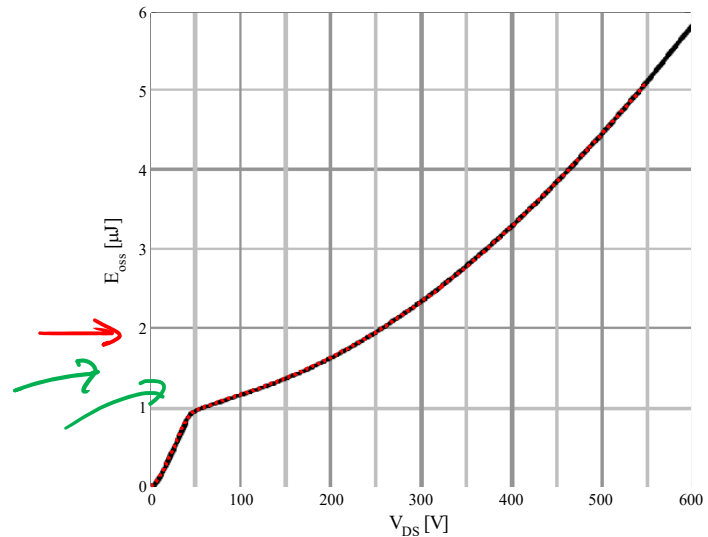
Energy Equivalent



Matlab Code:

Vdc = 550;

- `Vds = [0 5 10 40 50 75 100 150 200 300 400 500 600];`
- `Coss = [5500 2500 1900 550 95 50 38 30 29 27 27 25 24]*1e-12;`
- `vx = 0.01:.01:Vdc;`
- `Cx = 10.^interp1(Vdc,log10(Coss),vx,'linear');`
- `E = cumtrapz(vx, Cx.*vx);`
- `Ceq_e = 2*(E)./vx.^2;`



Nonlinear Capacitance Extraction

- <http://web.eecs.utk.edu/~dcostine/personal/PowerDeviceLib/DigiTest/index.html>

Datasheet Reported Capacitance

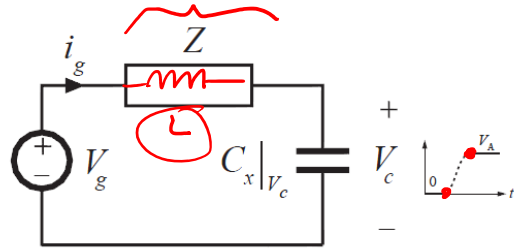
Dynamic characteristics

Input capacitance	C_{iss}	$V_{GS}=0\text{ V}, V_{DS}=100\text{ V},$	-	790	-	pF
Output capacitance	C_{oss}	$f=1\text{ MHz}$	-	38	-	
Effective output capacitance, energy related ⁶⁾	$C_{o(er)}$	$V_{GS}=0\text{ V}, V_{DS}=0\text{ V}$ to 480 V	-	36	-	
			Effective output capacitance, time related ⁷⁾	$C_{o(tr)}$	-	
Turn-on delay time	$t_{d(on)}$	$V_{DD}=400\text{ V},$ $V_{GS}=10\text{ V}, I_D=5.2\text{ A},$ $R_G=3.3\ \Omega$	-	10	-	ns
Rise time	t_r		-	5	-	
Turn-off delay time	$t_{d(off)}$		-	40	-	
Fall time	t_f		-	5	-	

\rightarrow ⁶⁾ $C_{o(er)}$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to 80% V_{DSS} .

\rightarrow ⁷⁾ $C_{o(tr)}$ is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 to 80% V_{DSS} .

Example Simulation

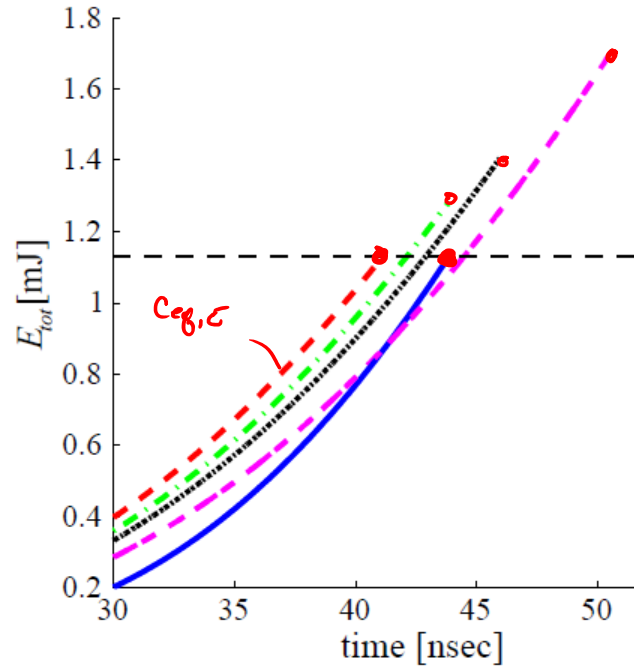


$$C_{eq,Q} = 70.5 \text{ pF}$$

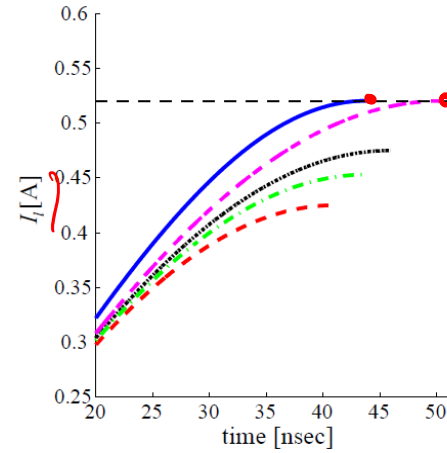
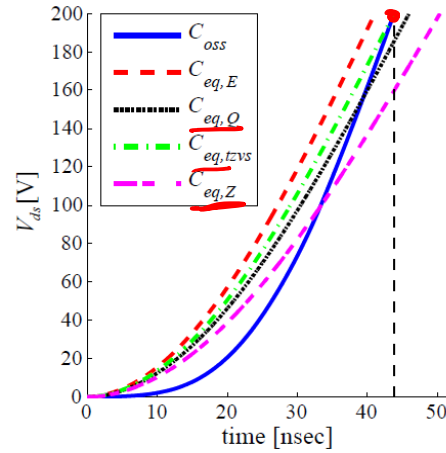
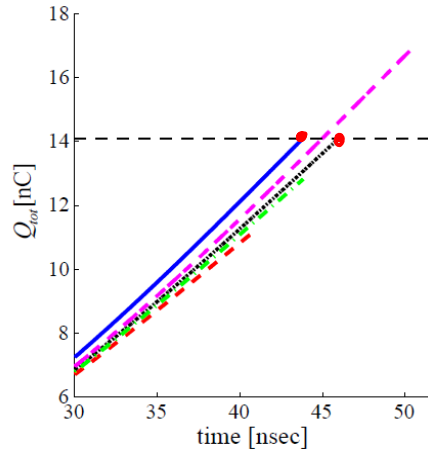
$$C_{eq,E} = 56.4 \text{ pF}$$

$$C_{eq,tzvs} = 64.1 \text{ pF}$$

$$C_{eq,Z} = 84.5 \text{ pF}$$



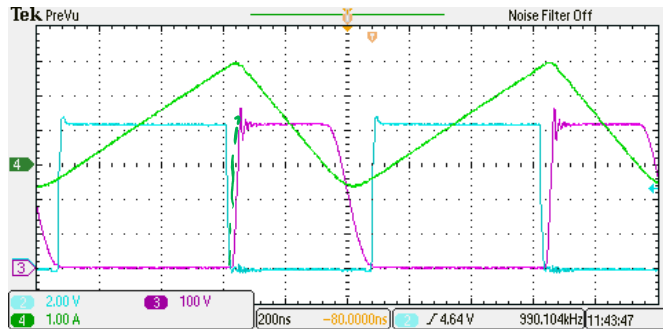
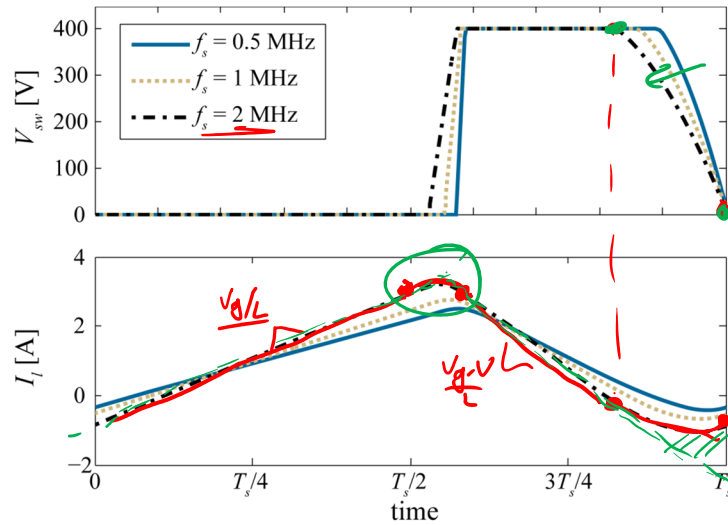
Further Simulation



Solving resonance in power electronics

STATE PLANE ANALYSIS

Motivation



Time-Domain Analysis of Switching Transitions

(1) Assume C_{out} is a filter element with small ripple

(2) Assume C_{ds} is linear

