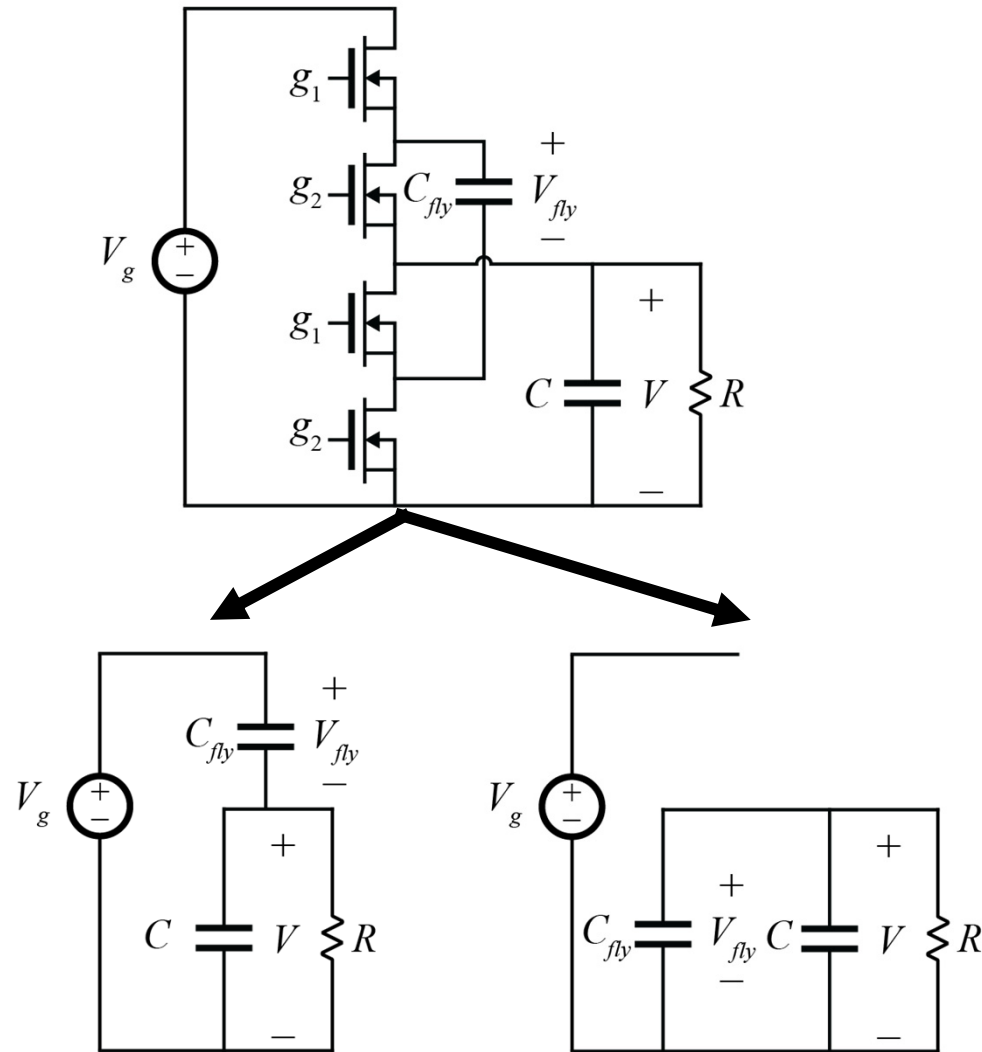


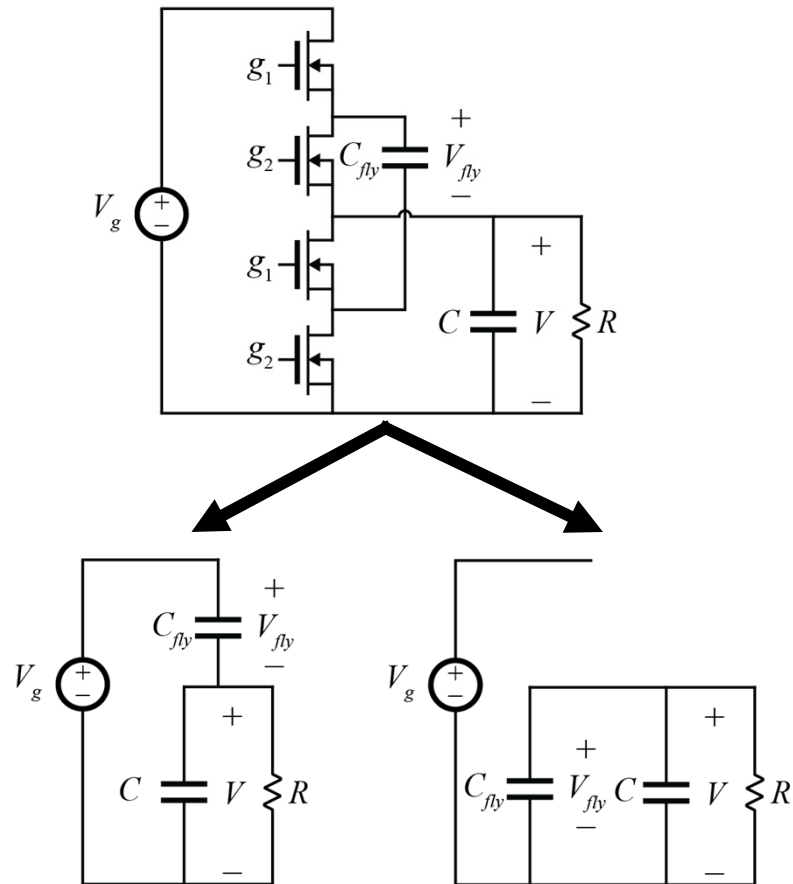
2:1 Converter Charge Vector Analysis



Tellegen's Theorem

SSL Output Resistance

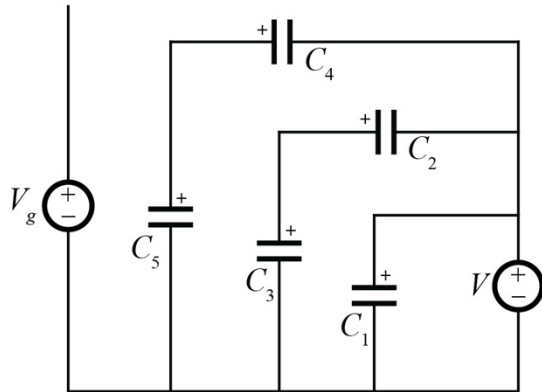
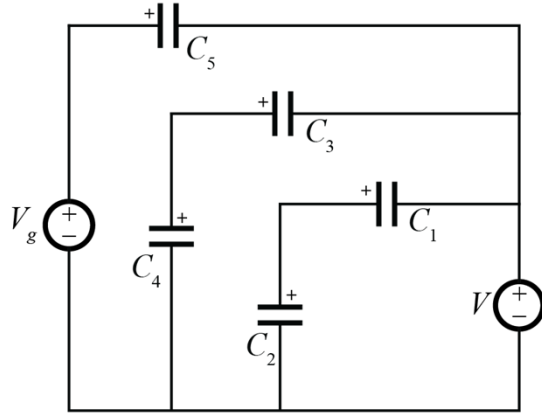
Output Resistance



$$\bar{a}^I = \begin{bmatrix} -\frac{1}{2} & \frac{1}{2} & \frac{1}{2} \end{bmatrix}$$

$$\bar{a}^{II} = \begin{bmatrix} 0 & -\frac{1}{2} & \frac{1}{2} \end{bmatrix}$$

Dickson Charge Vector Analysis



Dickson SSL Output Resistance

Charge Vector Analysis in FSL

Dickson Converter FSL Output Resistance

