

CS 160: Computer Organization

Fall 2006

Instructor: Dr Micah Beck

<http://www.cs.utk.edu/~cs160>

Date	Subject	Topics
Aug 24	Introduction	
Aug 28	Combinational logic	gates & boolean algebra
Aug 31	App A	equivalent expressions; optimization
Sept 5		decoders & multiplexers
Sept 7		PALs & FPGAs
Sept 12	Sequential Logic	flip flops & state tables
Sept 14	App A	counters & state machines
Sept 19	Exam 1	
Sept 21	Machine Arithmetic Ch 2.1-2.2 & 6.1-6.2 & 6.7	signed integers & floating point
Sept 26	Processor Architecture	functional units
Sept 28	Ch 1 & 7 & 8	structure & operation
Oct 3		pipelining & superscalar
Oct 5		instruction sets: CISC and RISC
Oct 10	Exam 2	
Oct 12	Fall Break	
Oct 17	Machine Programming	types, variables & registers
Oct 19	Ch 3, Pt I & App. B	arithmetic/logic operations
Oct 24		branching and addressing
Oct 26	Input/Output	polling & interrupts
Oct 31	Ch 4	busses
Nov 2	Memory	technology & organization
Nov 7	Ch 5	caches
Nov 9		management and virtualization
Nov 14	Exam 3	
Nov 16	Instruction Level	Pipelining and hazards
Nov 21	Parallelism Ch 8	Hazard avoidance
Nov 23	Thanksgiving Holiday	
Nov 28	Massive Parallelism	shared memory
Nov 30	Ch 12	message passing
Dec 5	Catch-up class	
Dec 11	Final Exam 10:15am - 12:15pm	